

A OTHER LOOPING CONSTRUCTS

Using the primitive looping construct `loop e`, the loop exiting construct `breakn e`, and a simple register library, we can implement other types of loops as follows:

<pre> repeat e \triangleq loop let x = e in if x = 0 then 0 else break₁ e </pre>	<pre> for i = e₁ to e₂ do e₃ \triangleq let ivar = alloc(1) in store(ivar, e₁); loop let i = load(ivar) in store(ivar, i + 1); if i ≤ e₂ then e₃ else break₁ 0 </pre>
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B PROOFS OF §5 THEOREMS

B.1 Correctness of Theorem 2

Given an execution G , let us write $\text{isMX}(G)$ for the following:

- $G.\text{lbh}$ is a strict total order on $G.E$;
- $G.\text{lbh}|_{\text{imm}} \subseteq (C^x \times \mathcal{L}^x) \cup (\mathcal{L}^x \times \mathcal{U}^x) \cup (\mathcal{U}^x \times \mathcal{L}^x)$; and
- $[\mathcal{L}^x]; (G.\text{lbh} \setminus G.\text{po}); [\mathcal{L}^x \cup \mathcal{U}^x] \subseteq G.\text{po}; [\mathcal{U}^x]; G.\text{lbh}$.

We are then required to show that for all x and G , if $G \in \mathcal{G}_c^{\text{MX}} \cap \mathcal{G}_{\text{wf}}^{\text{MX}}$, then $\text{isMX}(G_x)$ holds.

PROOF. Pick an arbitrary x and $G \in \mathcal{G}_c^{\text{MX}} \cap \mathcal{G}_{\text{wf}}^{\text{MX}}$ with $G_x = \langle E, \text{po}, \text{com}, \text{so}, \text{lbh} \rangle$. Pick an event e_0 from E such that it is minimal in lbh ; that is, $\forall e \in E \setminus \{e_0\}. (e, e_0) \neq \text{lbh}$. We first demonstrate that $e_0 \in C^x$. As e_0 is minimal with respect to lbh , $\text{po} \subseteq \text{lbh}$ and since from the well-formedness of G_x we know that the first event (in po order) in each thread is either a constructor or lock event, we know that $e_0 \in C^x \cup \mathcal{L}^x$. Let us assume $e_0 \in \mathcal{L}^x$. From the consistency of G_x we know that there exists u such that $(u, e_0) \in \text{com} \cap \text{so} \subseteq \text{lbh}$. This however contradicts our assumption that e_0 is minimal with respect to lbh and we thus know $e_0 \in C^x$.

Let $E = E_o^0 \cup E_r^0$ with $E_o^0 \triangleq \{e_0\}$ and $E_r^0 \triangleq E \setminus \{e_0\}$. Let us write $\text{numL}(S)$ to denote the number of lock events in the set of events S ; and write $\text{numU}(S)$ to denote the number of unlock events in the set of events S . Note that from the well-formedness of G_x we know that $\text{numU}(E_r^0) \leq \text{numL}(E_r^0)$. Moreover, from the well-formedness of G_x (namely the uniqueness of the constructor event) we know that $E_r^0 \cap C^x = \emptyset$. Without loss of generality, let m denote the number of lock events in E_r^0 ; i.e. $\text{numL}(E_r^0) = m$. We next demonstrate that:

$$\begin{aligned}
 & \forall n \in \mathbb{N}. \forall E_r, E_o. \\
 & E = E_o \uplus E_r \wedge \text{numL}(E_r) = n \wedge \text{numU}(E_r) \leq \text{numL}(E_r) \wedge E_r \cap C = \emptyset \\
 & \wedge [E_r]; \text{lbh}; [E_o] = \emptyset \wedge \text{isMX}(G_x|_{E_o}) \wedge G_x|_{E_r} \text{ is MX-well-formed on } x \\
 & \wedge \exists ! e_m. e_m = \max(E_o, \text{lbh}) \wedge e_m \in C^x \cup \mathcal{U}^x \wedge \forall e \in E_o \setminus \{e_m\}. \text{com}(e) \in E_o \quad \Rightarrow \text{isMX}(G_x) \\
 & \wedge (\forall l \in \mathcal{L}^x \cap E_o. (l, e_m) \in (\text{lbh}|_{E_o})|_{\text{imm}} \Rightarrow (l, e_m) \in \text{po})
 \end{aligned} \tag{1}$$

where $\max(E_o, \text{lbh})$ denotes the maximal elements in E_o with respect to lbh – note that lbh is total on E_o due to $\text{isMX}(G_x|_{E_o})$.

The desired result then follows immediately from (1) and the definitions of E_o^0 and E_r^0 . To show (1) we proceed by induction on n .

Base case $n = 0$

From the assumptions of the left-hand side we have $E_r = \emptyset$ and thus $E = E_o$. As such, from

isMX($G_x|_{E_o}$) we have isMX(G_x), as required.

Inductive case $n=k+1$

$\forall j \in \mathbb{N}. \forall E_r, E_o.$

$j \leq k \wedge E = E_o \uplus E_r \wedge \text{numL}(E_r) = n \wedge \text{numU}(E_r) \leq \text{numL}(E_r) \wedge E_r \cap C = \emptyset$

$\wedge [E_r]; \text{lbh}; [E_o] = \emptyset \wedge \text{isMX}(G_x|_{E_o}) \wedge G_x|_{E_r} \text{ is MX-well-formed on } x$

$\wedge \exists! e_m. e_m = \max(E_o, \text{lbh}) \wedge e_m \in C^x \cup \mathcal{U}^x \wedge \forall e \in E_o \setminus \{e_m\}. \text{com}(e) \in E_o \Rightarrow \text{isMX}(G_x)$

$\wedge (\forall l \in \mathcal{L}^x \cap E_o. (l, e_m) \in (\text{lbh}|_{E_o})|_{\text{imm}} \Rightarrow (l, e_m) \in \text{po})$

(I.H.)

Pick an arbitrary E_r, E_o, e_m such that $E = E_o \uplus E_r$, $\text{numL}(E_r) = n$, $\text{numU}(E_r) \leq \text{numL}(E_r)$, $E_r \cap C = \emptyset$, $[E_r]; \text{lbh}; [E_o] = \emptyset$, $\text{isMX}(G_x|_{E_o})$, $G_x|_{E_r}$ is MX-well-formed on x . e_m is unique and $e_m = \max(E_o, \text{lbh})$, $e_m \in C^x \cup \mathcal{U}^x$, $\forall e \in E_o \setminus \{e_m\}. \text{com}(e) \in E_o$, and $\forall l \in \mathcal{L}^x \cap E_o. (l, e_m) \in (\text{lbh}|_{E_o})|_{\text{imm}} \Rightarrow (l, e_m) \in \text{po}$.

Pick an event $e_l \in E_r$ such that it is minimal in lbh ; that is, $\forall e \in E_r \setminus \{e_l\}. (e, e_l) \neq \text{lbh}$. As e_l is minimal with respect to lbh , $\text{po} \subseteq \text{lbh}$, and since from the MX-well-formedness of $G_x|_{E_r}$ we know that the first event (in po order) in each thread is either a constructor or lock event, we know that $e_l \in C^x \cup \mathcal{L}^x$. Moreover, since $E_r \cap C^x = \emptyset$, we know that $e_l \in \mathcal{L}^x$. We next demonstrate that $(e_m, e_l) \in \text{com} = \text{so} \subseteq \text{lbh}$.

From the consistency of G_x we know there exists $u \in C^x \cup \mathcal{U}^x$ such that $(u, e_l) \in \text{com}$. There are then three cases to consider: a) $u = e_m$; or b) $u \in E_o \setminus \{e_m\}$; or c) $u \in E_r$. In case (a) we then have $(e_m, e_l) \in \text{com}$ as required. In case (b) from $\forall e \in E_o \setminus \{e_m\}. \text{com}(e) \in E_o$ we know there exists another $l \in E_o$ such that $(u, l) \in \text{com}$. Since $l \in E_o$ and $e_l \in E_r$ we know $l \neq e_l$. Consequently, we have $(u, l), (u, e_l) \in \text{com}$, contradicting the assumption that com is functional (since G_x is consistent). In case (c), from the well-formedness of $G_x|_{E_r}$ we know there exists l such that $(l, u) \in \text{po}|_{\text{imm}}$. We then have $l \xrightarrow{\text{po}} u \xrightarrow{\text{com}} e_l$; that is we have $l \xrightarrow{\text{lbh}} e_l$, contradicting the assumption that e_l is a minimal element of E_r with respect to lbh .

From the MX-well-formedness of $G_x|_{E_r}$ we know there exists $e_u \in \mathcal{U}^x$ such that $(e_l, e_u) \in \text{po}|_{\text{imm}}$. As $\text{po} \subseteq \text{lbh}$ and $[E_r]; \text{lbh}; [E_o] = \emptyset$, we know that $e_u \in E_r$. Let us then define $E'_o = E_o \uplus \{e_l, e_u\}$ and $E'_r = E_r \setminus \{e_l, e_u\}$; i.e. (1) $E = E'_o \uplus E'_r$, $\text{numL}(E'_r) = n-1=k$, $\text{numU}(E'_r) \leq \text{numL}(E'_r)$, $E'_r \cap C = \emptyset$.

We next demonstrate that $[E'_r]; \text{lbh}; [E'_o] = \emptyset$. As we already have $[E_r]; \text{lbh}; [E_o] = \emptyset$ from the assumption, it suffices to show: $\forall e \in E'_r. (e, e_l) \notin \text{lbh}$ and $\forall e \in E'_r. (e, e_u) \notin \text{lbh}$. The former follows from the fact that e_l is a minimal element of E_r with respect to lbh . For the latter, let us proceed by contradiction and assume there exists $e \in E'_r$ such that $(e, e_u) \in \text{lbh}$. As e_u is an unlock event without incoming so edges and $(e_l, e_u) \in \text{po}|_{\text{imm}}$, we know $(e, e_l) \in \text{lbh}$. This however contradicts our assumption that e_l is a minimal element of E_r with respect to lbh . We thus have: (2) $[E'_r]; \text{lbh}; [E'_o] = \emptyset$. We next show that $\text{isMX}(G_x|_{E'_o})$ holds. As $\text{isMX}(G_x|_{E_o})$ holds, we know:

lbh is a strict total order on E_o ;

$G_x|_{E_o}. \text{lbh}|_{\text{imm}} \subseteq (C^x \times \mathcal{L}^x) \cup (\mathcal{L}^x \times \mathcal{U}^x) \cup (\mathcal{U}^x \times \mathcal{L}^x)$; and

$[\mathcal{L}^x]; (G_x|_{E_o}. \text{lbh} \setminus G_x|_{E_o}. \text{po}); [\mathcal{L}^x \cup \mathcal{U}^x] \subseteq G_x|_{E_o}. \text{po}; [\mathcal{U}^x]; G_x|_{E_o}. \text{lbh}$.

As $e_m = \max(E_o, \text{lbh})$ and $e_m \xrightarrow{\text{lbh}} e_l \xrightarrow{\text{po}} e_u$, we know:

lbh is a strict total order on E'_o and

$G_x|_{E'_o}. \text{lbh}|_{\text{imm}} \subseteq (C^x \times \mathcal{L}^x) \cup (\mathcal{L}^x \times \mathcal{U}^x) \cup (\mathcal{U}^x \times \mathcal{L}^x)$.

Lastly, since $\forall l \in \mathcal{L}^x \cap E_o. (l, e_m) \in (\text{lbh}|_{E_o})|_{\text{imm}} \Rightarrow (l, e_m) \in \text{po}$, we have

$[\mathcal{L}^x]; (G_x|_{E'_o}. \text{lbh} \setminus G_x|_{E'_o}. \text{po}); [\mathcal{L}^x \cup \mathcal{U}^x] \subseteq G_x|_{E'_o}. \text{po}; [\mathcal{U}^x]; G_x|_{E'_o}. \text{lbh}$.

We thus know: (3) $\text{isMX}(G_x|_{E'_o})$ holds.

Since $G_x|_{E_r}$ is MX-well-formed on x , from the definition of E'_r we have (4) $G_x|_{E'_r}$ is MX-well-formed on x . As $e_m = \max(E_o, \text{lbh})$ and $e_m \xrightarrow{\text{lbh}} e_l \xrightarrow{\text{po}} e_u$, from the definition of E'_o and since $e_u \in \mathcal{U}^x$ we

have: (5) $e_u = \max(E'_o, \text{lbh})$ and $e_u \in C^x \cup \mathcal{U}^x$. Moreover, as $\forall e \in E_o \setminus \{e_m\}. \text{com}(e) \in E_o$, and $(e_m, e_l) \in \text{com}$, from the definition of E'_o we have (6) $\forall e \in E'_o \setminus \{e_u\}. \text{com}(e) \in E'_o$. Lastly, since $e_m = \max(E_o, \text{lbh})$ and $e_m \xrightarrow{\text{lbh}} e_l \xrightarrow{\text{po}} e_u$, and $(e_l, e_u) \in \text{po}$, we know that:

(7) $\forall l \in \mathcal{L}^x \cap E'_o. (l, e_u) \in (\text{lbh}|_{E'_o})|_{\text{imm}} \Rightarrow (l, e_u) \in \text{po}$.

Consequently, from (1)-(7) and (I.H.) we have $\text{isMX}(G)$ as required. \square

B.2 Correctness of Theorem 3

Given a relation r , let us define the following relations for all $n, m > 0$ and $k \geq 0$:

$$\begin{aligned} A(r) &\triangleq \text{com}^{-1}; r & B(r) &\triangleq \text{com}; r \\ C^{n,m}(r) &\triangleq (A(r); C^{n-1,m}) \cup (B(r); C^{n,m-1}) & C^{k,0} &\triangleq A(r)^k & C^{0,k} &\triangleq B(r)^k \end{aligned} \quad (2)$$

Pick an arbitrary execution $G = (E, \text{po}, \text{com}, \text{so}, \text{lbh})$ of the queue library such that $\text{irreflexive}(C^{n,n})$ holds for all $n > 0$. Let $\text{to}_0 \triangleq \text{lbh}$, $TO_0 = \{\text{to}_0\}$ and for all $i \geq 0$ let us define:

$$\begin{aligned} TO_{i+1} &\triangleq \left\{ \begin{aligned} &(\text{to}_i \cup \{(d_1, d_2)\})^+ \mid \text{to}_i \in TO_i \wedge (P_1(\text{to}_i, d_1, d_2) \vee P_2(\text{to}_i, d_1, d_2)) \\ &\cup \{(\text{to}_i \cup \{(e_1, e_2)\})^+ \mid \text{to}_i \in TO_i \wedge (P_3(\text{to}_i, e_1, e_2) \vee P_4(\text{to}_i, e_1, e_2))\} \\ &\cup \left\{ \text{to}_i \mid \begin{aligned} &\text{to}_i \in TO_i \wedge \forall d_1, d_2 \in \mathcal{D}^q. \forall e_1, e_2 \in \mathcal{E}^q. \\ &\neg P_1(\text{to}_i, d_1, d_2) \wedge \neg P_2(\text{to}_i, d_1, d_2) \wedge \neg P_3(\text{to}_i, e_1, e_2) \wedge \neg P_4(\text{to}_i, e_1, e_2) \end{aligned} \right\} \end{aligned} \right\} \end{aligned}$$

$$\begin{aligned} P_1(\text{to}_i, d_1, d_2) &\stackrel{\text{def}}{\Leftrightarrow} d_1, d_2 \in \mathcal{D}^q \wedge (d_1, d_2) \notin \text{to}_i \cup \text{to}_i^{-1} \wedge \exists e_1, e_2. \\ &\quad (e_1, d_1), (e_2, d_2) \in \text{com} \wedge (e_1, e_2) \in \text{to}_i \end{aligned}$$

$$\begin{aligned} P_2(\text{to}_i, d_1, d_2) &\stackrel{\text{def}}{\Leftrightarrow} d_1, d_2 \in \mathcal{D}^q \wedge (d_1, d_2) \notin \text{to}_i \cup \text{to}_i^{-1} \wedge \nexists e_1, e_2 \\ &\quad (e_1, d_1), (e_2, d_2) \in \text{com} \wedge (e_1, e_2) \in \text{to}_i \cup \text{to}_i^{-1} \wedge \forall n \in \mathbb{N}^+. \text{irreflexive}(C^{n,n}(\text{to}_{i+1})) \end{aligned}$$

$$\begin{aligned} P_3(\text{to}_i, e_1, e_2) &\stackrel{\text{def}}{\Leftrightarrow} e_1, e_2 \in \mathcal{E}^q \wedge (e_1, e_2) \notin \text{to}_i \cup \text{to}_i^{-1} \wedge \exists d_1, d_2. \\ &\quad (e_1, d_1), (e_2, d_2) \in \text{com} \wedge (d_1, d_2) \in \text{to}_i \end{aligned}$$

$$\begin{aligned} P_4(\text{to}_i, e_1, e_2) &\stackrel{\text{def}}{\Leftrightarrow} e_1, e_2 \in \mathcal{E}^q \wedge (e_1, e_2) \notin \text{to}_i \cup \text{to}_i^{-1} \wedge \nexists d_1, d_2 \\ &\quad (e_1, d_1), (e_2, d_2) \in \text{com} \wedge (d_1, d_2) \in \text{to}_i \cup \text{to}_i^{-1} \wedge \forall n \in \mathbb{N}^+. \text{irreflexive}(C^{n,n}(\text{to}_{i+1})) \end{aligned}$$

In what follows, we write A_i for $A(\text{to}_i)$, B_i for $B(\text{to}_i)$ and $C_i^{n,m} \triangleq C^{n,m}(\text{to}_i)$, when the choice of to_i is clear from the context.

We next demonstrate that:

$$\forall i \in \mathbb{N}. \forall \text{to}_i \in TO_i. \forall n \in \mathbb{N}^+. \text{irreflexive}(C_i^{n,n}) \quad (3)$$

PROOF. We proceed by induction on i .

Base case $i = 0$

Follows immediately from the definition of $TO_0 = \{\text{to}_0\}$ (as $\text{to}_0 \triangleq \text{lbh}$) and the assumption of the lemma.

Inductive case $i = j+1$

$$\forall k \leq j. \forall \text{to}_k \in TO_k. \forall n \in \mathbb{N}^+. \text{irreflexive}(C_k^{n,n}) \quad (\text{I.H.})$$

Pick an arbitrary $\text{to}_i \in \text{TO}_i$. Let us proceed by contradiction and assume that there exists a $C_i^{k,k}$ cycle for some $k > 0$. Given the definition of to_i , there are now five cases to consider. The proof of the second and fourth cases follow immediately from the definition of to_i . The proof of the fifth case follows from (I.H.).

Case 1

We then know there exist $\text{to}_j \in \text{TO}_j$ and e_1, e_2, d_1, d_2 such that $\text{to}_i = \text{to}_j \cup \{(d_1, d_2)\}$, $d_1, d_2 \in \mathcal{D}^q$, $(d_1, d_2) \notin \text{to}_j \cup \text{to}_j^{-1}$, $(e_1, d_1), (e_2, d_2) \in \text{com}$ and $(e_1, e_2) \in \text{to}_j$.

As $C_j^{n,n}$ is irreflexive for all $n > 0$, we know the $C_i^{k,k}$ cycle involves the newly added edge $d_1 \xrightarrow{\text{to}_i} d_2$. That is, either i) $e_1 \xrightarrow{\text{com}} d_1 \xrightarrow{\text{to}_j} d_2 \xrightarrow{C_j^{k,k-1}} e_1$; or ii) there exist a, b such that $a \xrightarrow{\text{to}_j^?} d_1 \xrightarrow{\text{to}_j} d_2 \xrightarrow{\text{to}_j} b \xrightarrow{C_j^{k,k}} a$. In case (i), as d_2 is a dequeue event (and cannot have an outgoing B_j edge), we know its outgoing edge is an A_j edge. That is, there exists a such that $d_2 \xrightarrow{\text{com}^{-1}} e_2 \xrightarrow{\text{to}_j} a$. We thus have $a \xrightarrow{C_j^{k-1,k-1}} e_1 \xrightarrow{\text{to}_j} a$. As to_j is transitively closed, we have $a \xrightarrow{C_j^{k-1,k-1}} e_1 \xrightarrow{\text{to}_j} a$. From the definition of $C_j^{k-1,k-1}$ we know that it ends with to_j . As such, we have $a \xrightarrow{C_j^{k-1,k-1}} a$, contradicting (I.H.).

In case (ii), we also have $d_1 \xrightarrow{\text{com}^{-1}} e_1 \xrightarrow{\text{to}_j} e_2 \xrightarrow{\text{com}} d_2$. We thus have $d_1 \xrightarrow{\text{com}^{-1}} e_1 \xrightarrow{\text{to}_j} e_2 \xrightarrow{\text{com}} d_2 \xrightarrow{C_j^{k,k}} b \xrightarrow{\text{to}_j^?} a \xrightarrow{\text{to}_j} d_1$. That is, we have $d_1 \xrightarrow{A_j} e_2 \xrightarrow{B_j} b \xrightarrow{C_j^{k,k}} a \xrightarrow{\text{to}_j^?} d_1$. From the definition of $C_j^{k,k}$ we know that it ends with to_j . As to_j is transitively closed and $a \xrightarrow{\text{to}_j^?} d_1$, we thus also have $b \xrightarrow{C_j^{k,k}} d_1$. We then have $d_1 \xrightarrow{A_j} e_2 \xrightarrow{B_j} b \xrightarrow{C_j^{k,k}} d_1$. That is, $d_1 \xrightarrow{C_j^{k+1,k+1}} d_1$, contradicting (I.H.).

Case 3

We then know there exist $\text{to}_j \in \text{TO}_j$ and e_1, e_2, d_1, d_2 such that $\text{to}_i = \text{to}_j \cup \{(e_1, e_2)\}$, $e_1, e_2 \in \mathcal{D}^q$, $(e_1, e_2) \notin \text{to}_j \cup \text{to}_j^{-1}$, $(e_1, d_1), (e_2, d_2) \in \text{com}$ and $(d_1, d_2) \in \text{to}_j$.

As $C_j^{n,n}$ is irreflexive for all $n > 0$, we know the $C_i^{k,k}$ cycle involves the newly added edge $e_1 \xrightarrow{\text{to}_i} e_2$. That is, either i) $d_1 \xrightarrow{\text{com}^{-1}} e_1 \xrightarrow{\text{to}_j} e_2 \xrightarrow{C_j^{k-1,k}} d_1$; or ii) there exist a, b such that $a \xrightarrow{\text{to}_j^?} e_1 \xrightarrow{\text{to}_j} e_2 \xrightarrow{\text{to}_j} b \xrightarrow{C_j^{k,k}} a$. In case (i), as e_2 is an enqueue event (and cannot have an outgoing A_j edge), we know its outgoing edge is an B_j edge. That is, there exists a such that $e_2 \xrightarrow{\text{com}} d_2 \xrightarrow{\text{to}_j} a$. We thus have $a \xrightarrow{C_j^{k-1,k-1}} d_1 \xrightarrow{\text{to}_j} a$. As to_j is transitively closed, we have $a \xrightarrow{C_j^{k-1,k-1}} d_1 \xrightarrow{\text{to}_j} a$. From the definition of $C_j^{k-1,k-1}$ we know that it ends with to_j . As such, we have $a \xrightarrow{C_j^{k-1,k-1}} a$, contradicting (I.H.).

In case (ii), we also have $e_1 \xrightarrow{\text{com}} d_1 \xrightarrow{\text{to}_j} d_2 \xrightarrow{\text{com}^{-1}} e_2$. We thus have $e_1 \xrightarrow{\text{com}} d_1 \xrightarrow{\text{to}_j} d_2 \xrightarrow{\text{com}^{-1}} e_2 \xrightarrow{C_j^{k,k}} b \xrightarrow{\text{to}_j^?} a \xrightarrow{\text{to}_j} d_1$. That is, we have $e_1 \xrightarrow{B_j} e_2 \xrightarrow{A_j} b \xrightarrow{C_j^{k,k}} a \xrightarrow{\text{to}_j^?} d_1$. From the definition of $C_j^{k,k}$ we know that it ends with to_j . As to_j is transitively closed and $a \xrightarrow{\text{to}_j^?} d_1$, we thus also have $b \xrightarrow{C_j^{k,k}} e_1$. We then have $e_1 \xrightarrow{B_j} e_2 \xrightarrow{A_j} b \xrightarrow{C_j^{k,k}} e_1$. That is, $e_1 \xrightarrow{C_j^{k+1,k+1}} e_1$, contradicting (I.H.).

□

We next demonstrate that:

$$\forall i \in \mathbb{N}. \forall \text{to}_i \in TO_i. \forall \text{to}_{i+1} \in TO_{i+1}. \text{to}_i = \text{to}_{i+1} \Rightarrow \forall d_1, d_2 \in \mathcal{D}^q. (d_1, d_2) \in \text{to}_i \cup \text{to}_i^{-1} \quad (4)$$

PROOF. Pick an arbitrary $i \in \mathbb{N}$, $\text{to}_i \in TO_i$ and $\text{to}_{i+1} \in TO_{i+1}$ such that $\text{to}_i = \text{to}_{i+1}$. We then proceed by contradiction. Let us assume there exist $d_1, d_2 \in \mathcal{D}^q$ such that $(d_1, d_2) \notin \text{to}_i \cup \text{to}_i^{-1}$. Let us write e_1 for $\text{com}^{-1}(d_1)$ when it exists (i.e. when $(e_1, d_1) \in \text{com}$) and write e_2 for $\text{com}^{-1}(d_2)$ when it exists (i.e. when $(e_2, d_2) \in \text{com}$). Let $S_1 = \text{to}_i \uplus \{(d_1, d_2)\}$ and $S_2 = \text{to}_i \uplus \{(d_2, d_1)\}$. From the definition of to_{i+1} we then know that $(e_1, e_2) \notin \text{to}_i$ and there exist k, n such that $\neg \text{irreflexive}(C^{k,k}(S_1))$ and $\neg \text{irreflexive}(C^{n,n}(S_2))$.

As from (3) we know $\text{irreflexive}(C_i^{k,k})$ holds, we know the cycle in $C^{k,k}(S_1)$ is due to the (d_1, d_2) edge. That is, either 1) $d_1 \xrightarrow{S_1 \setminus \text{to}_i} d_2 \xrightarrow{C_i^{k,k}} d_1$; or 2) there exist a, b such that $a \xrightarrow{\text{to}_i^?} d_1 \xrightarrow{S_1 \setminus \text{to}_i} d_2 \xrightarrow{\text{to}_i} b \xrightarrow{C_i^{k,k}} a$. Similarly, as from (3) we know $\text{irreflexive}(C_i^{n,n})$ holds, we know the cycle in $C^{n,n}(S_2)$ is due to the (d_2, d_1) edge: either a) $d_2 \xrightarrow{S_2 \setminus \text{to}_i} d_1 \xrightarrow{C_i^{n,n}} d_2$; or b) there exist f, g such that $f \xrightarrow{\text{to}_i^?} d_2 \xrightarrow{S_2 \setminus \text{to}_i} d_1 \xrightarrow{\text{to}_i} g \xrightarrow{C_i^{n,n}} f$.

There are now four cases to consider. In case (1.a) we have $d_1 \xrightarrow{C_i^{n,n}} d_2 \xrightarrow{C_i^{k,k}} d_1$, i.e. $d_1 \xrightarrow{C_i^{n+k, n+k}} d_1$, contradicting our result in (3).

In case (1.b) we then have $g \xrightarrow{C_i^{n,n}} f \xrightarrow{\text{to}_i^?} d_2 \xrightarrow{C_i^{k,k}} d_1 \xrightarrow{\text{to}_i} g$. As from the definitions of $C_i^{n,n}$ and $C_i^{k,k}$ we know they end with to_i , we then have $g \xrightarrow{C_i^{n,n}} d_2 \xrightarrow{C_i^{k,k}} g$. That is, we have $g \xrightarrow{C_i^{n+k, n+k}} g$, contradicting our result in (3).

Similarly, in (2.a) we have $b \xrightarrow{C_i^{k,k}} a \xrightarrow{\text{to}_i^?} d_1 \xrightarrow{C_i^{n,n}} d_2 \xrightarrow{\text{to}_i} b$. As from the definitions of $C_i^{n,n}$ and $C_i^{k,k}$ we know they end with to_i , we then have $b \xrightarrow{C_i^{k,k}} d_1 \xrightarrow{C_i^{n,n}} b$. That is, we have $b \xrightarrow{C_i^{n+k, n+k}} b$, contradicting our result in (3).

Lastly, in (2.b) we have $b \xrightarrow{C_i^{k,k}} a \xrightarrow{\text{to}_i^?} d_1 \xrightarrow{\text{to}_i} g \xrightarrow{C_i^{n,n}} f \xrightarrow{\text{to}_i^?} d_2 \xrightarrow{\text{to}_i} b$. As from the definitions of $C_i^{n,n}$ and $C_i^{k,k}$ we know they end with to_i and to_i is transitively closed, we then have $b \xrightarrow{C_i^{k,k}} g \xrightarrow{C_i^{n,n}} b$. That is, we have $b \xrightarrow{C_i^{n+k, n+k}} b$, contradicting our result in (3). \square

Similarly, we can demonstrate that:

$$\forall i \in \mathbb{N}. \forall \text{to}_i \in TO_i. \forall \text{to}_{i+1} \in TO_{i+1}. \text{to}_i = \text{to}_{i+1} \Rightarrow \forall e_1, e_2 \in \mathcal{E}^q. (e_1, e_2) \in \text{to}_i \cup \text{to}_i^{-1} \quad (5)$$

Theorem 7. For a given tuple $(E, \text{po}, \text{com}, \text{so}, \text{lbh})$, the $C^{n,n}$ is irreflexive for all $n \in \mathbb{N}^+$ iff condition (7) on page 20 holds.

PROOF. For the \Rightarrow direction, pick an arbitrary execution $G = (E, \text{po}, \text{com}, \text{so}, \text{lbh})$ of the queue library such that $\text{irreflexive}(C^{n,n})$ holds for all $n > 0$. Let $m \in \mathbb{N}$ be the least natural number for which there exist $\text{to}_m \in TO_m$ and $\text{to}_{m+1} \in TO_{m+1}$ such that $\text{to}_m = \text{to}_{m+1}$. Let us then define to as an arbitrary extension of to_m to a strict total order. From the definition of to_m we then know that to agrees with lbh . Let H denote the enumeration of events in E according to to . From (3), (4) and (5) it is then straightforward to demonstrate that $\text{fifo}(\epsilon, H)$ holds.

For the \Leftarrow direction pick an arbitrary execution $G = (E, \text{po}, \text{com}, \text{so}, \text{lahb})$ with a sequential enumeration H of $E \setminus C^q$ such that: (i) H agrees with lahb ; (ii) $\text{fifo}(\epsilon, H)$ holds. Let to denote the strict total order on E induced by H . We next demonstrate that:

$$\forall n \in \mathbb{N}^+. C^{n,n}(\text{to}) \subseteq \text{to}$$

We then have $C^{n,n} \triangleq C^{n,n}(\text{lahb}) \subseteq C^{n,n}(\text{to})$. As such, the irreflexivity of $C^{n,n}$ for an arbitrary $n \in \mathbb{N}^+$ simply follows from above and the irreflexivity of the strict total order to .

To show that $\forall n \in \mathbb{N}^+. C^{n,n}(\text{to}) \subseteq \text{to}$, we proceed by induction on n .

Base case $n = 1$

Pick an arbitrary $(a, b) \in C^{1,1}(\text{to})$. From the definition of $C^{1,1}(\text{to})$ we then know that there exists c such that either 1) $(a, c) \in \text{com}^{-1}; \text{to}; \text{com}$ and $(c, b) \in \text{to}$; or 2) $(a, c) \in \text{com}; \text{to}; \text{com}^{-1}$ and $(c, b) \in \text{to}$.

In both cases from $\text{fifo}(\epsilon, H)$ and the definition of to we know that $(a, c) \in \text{to}$. As in both cases we have $(c, b) \in \text{to}$ and to is transitively closed, we have $(a, b) \in \text{to}$ as required.

Base case $n = k+1$

$$\forall m \in \mathbb{N}^+. m \leq k \Rightarrow C^{m,m}(\text{to}) \subseteq \text{to} \quad (\text{I.H.})$$

Pick an arbitrary $(a, b) \in C^{k+1,k+1}(\text{to})$. From the definition of $C^{k+1,k+1}$ we know there exists at least one adjacent pair of $A(\text{to})$ and $B(\text{to})$ edges. That is, there exists i, j, c, d such that: $a \xrightarrow{C^{i,j}(\text{to})} c \xrightarrow{C^{1,1}(\text{to})} d \xrightarrow{C^{k-i,k-j}(\text{to})} b$. As such, from the proof of the base case we know $a \xrightarrow{C^{i,j}(\text{to})} c \xrightarrow{\text{to}} d \xrightarrow{C^{k-i,k-j}(\text{to})} b$. As the $C^{i,j}(\text{to})$ path ends with a to edge for all i, j and to is transitive, we have $a \xrightarrow{C^{i,j}(\text{to})} d \xrightarrow{C^{k-i,k-j}(\text{to})} b$. That is, $a \xrightarrow{C^{k,k}(\text{to})} b$. Consequently, from (I.H.) we have $(a, b) \in \text{to}$, as required. \square

C ADDITIONAL SPECIFICATIONS

C.1 Multiple-Readers-Single-Writer Lock Library Specification

We consider a multiple-readers-single-writer (MRSW) lock library with six methods: 1) *new-MSRW()*, for constructing a lock; 2) *wlock(x)*, for acquiring x in writer mode; 3) *wunlock(x)*, for releasing the writer lock on x ; 4) *rlock(x)*, for acquiring x in reader mode; 5) *runlock(x)*, for releasing a reader lock on x ; and 6) *plock(x)*, for promoting a reader lock on x to a writer one. A reader lock on x is promoted once all reader locks on x (except that of the promoter) are released.

The MRSW interface is $\langle \mathcal{M}^{\text{RW}}, \mathcal{M}_c^{\text{RW}}, \text{loc}^{\text{RW}} \rangle$, where $\mathcal{M}_c^{\text{RW}} \triangleq \bigcup_{x \in \text{Loc}} \mathcal{M}_c^x$ with $\mathcal{M}_c^x \triangleq \{\text{new-MSRW}(x)\}$; $\mathcal{M}^{\text{RW}} \triangleq \bigcup_{x \in \text{Loc}} \mathcal{M}^x$ with $\mathcal{M}^x \triangleq \mathcal{M}_c^x \cup \{\text{wlock}(x), \text{wunlock}(x), \text{rlock}(x), \text{runlock}(x), \text{plock}(x)\}$; and $\forall l \in \mathcal{M}^x. \text{loc}^{\text{MX}}(l) = \{x\}$. For an MRSW lock at location x , we define the following event sets:

$$\begin{aligned} \mathcal{WL}^x &\triangleq \{e \mid \text{lab}(e) = \text{wlock}(x)\} & \mathcal{WU}^x &\triangleq \{e \mid \text{lab}(e) = \text{wunlock}(x)\} \\ \mathcal{RL}^x &\triangleq \{e \mid \text{lab}(e) = \text{rlock}(x)\} & \mathcal{RU}^x &\triangleq \{e \mid \text{lab}(e) = \text{runlock}(x)\} \\ \mathcal{CL}^x &\triangleq \{e \mid \text{lab}(e) = \text{new-MSRW}(x)\} & \mathcal{PL}^x &\triangleq \{e \mid \text{lab}(e) = \text{plock}(x)\} \end{aligned}$$

Let $\mathcal{L}^x \triangleq \mathcal{WL}^x \cup \mathcal{RL}^x \cup \mathcal{PL}^x$ and $\mathcal{U}^x \triangleq \mathcal{WU}^x \cup \mathcal{RU}^x$.

A tuple $\langle E, \text{po}, \text{com}, \text{so}, \text{lahb} \rangle$ is *RW-consistent* on x iff:

- (1) there is at most one constructor event: $E^c = \emptyset \vee \exists c \in C^x. E^c = \{c\}$;
- (2) com relates matching lock events: $\text{com} = \text{com}_w \cup \text{com}_r \cup \text{com}_p$ with:
 $\text{com}_w, \text{com}_p \subseteq (C^x \cup \mathcal{U}^x) \times \mathcal{WL}^x$ $\text{com}_r \subseteq (C^x \cup \mathcal{WU}^x) \times \mathcal{RL}^x$

- (3) each event is matched by at most one lock except for reader locks, i.e. for all e, e_1, e_2 :
 $e_1 \neq e_2 \wedge (e, e_1), (e, e_2) \in \text{com} \Rightarrow (e_1, e_2 \in \mathcal{RL}^x) \vee (e_1 \in \mathcal{RL}^x \wedge e_2 \in \mathcal{PL}^x) \vee (e_1 \in \mathcal{PL}^x \wedge e_2 \in \mathcal{RL}^x)$
- (4) each lock is matched by at most one event: com^{-1} is functional;
- (5) all lock events are matched: $E \cap \mathcal{L}^x = \text{rng}(\text{com})$; and
- (6) every matching edges is synchronising: $\text{so} = \text{com}$.

Intuitively, com describes the order of lock acquisition. For each $l \in \mathcal{WL}^x$ with $(e, l) \in \text{com}_w$, when $e \in \mathcal{U}^x$ then e denotes the unlock event releasing the lock *immediately* before it is acquired by l ; when $e \in C^x$ then e denotes the constructor initialising the lock, and thus l corresponds to the very first $\text{wlock}(x)$ call. As l acquires the lock in the (exclusive) writer mode, no other lock may be matched with its predecessor e (see (3)). For each $l \in \mathcal{RL}^x$ with $(e, l) \in \text{com}_r$, the case where $e \in C^x$ can be described analogously; when $e \in \mathcal{WU}^x$, then e denotes the last time the lock in writer mode was released. As MRSW locks allow for multiple reader locks simultaneously, multiple events in \mathcal{RL}^x may be matched with the same event in \mathcal{WL}^x (see (3)). Lastly, for each $l \in \mathcal{PL}^x$ with $(e, l) \in \text{com}_p$, when $e \in \mathcal{RU}^x$ then e denotes the event releasing the last reader lock on x ; when $e \in \mathcal{WU}^x$, then e denotes the last time the lock in writer mode was released; when $e \in C^x$, then l denotes the first $\text{plock}(x)$ call, prior to any writer lock acquisition. In the latter two cases, at the time of promoting the lock via l , no other reader locks (other than that being promoted) are held on x and thus l follows the last writer lock release or the constructor. As such, the event acquiring a reader lock as well as its subsequent promotion may both be matched by e (see (3)).

A tuple $\langle E, \text{po}, \text{com}, \text{so}, \text{lhb} \rangle$ is *RW-well-formed* on x iff:

- $\min(\text{po}) \subseteq C^x \cup \mathcal{L}^x$ and $\text{po}|_{\text{imm}}(E^c) \subseteq \mathcal{L}^x$;
- $[\mathcal{RL}^x]; \text{po}|_{\text{imm}} = \text{po}|_{\text{imm}}; [\mathcal{PL}^x \cup \mathcal{RU}^x]$; and
- $[\mathcal{WL}^x \cup \mathcal{PL}^x]; \text{po}|_{\text{imm}} = \text{po}|_{\text{imm}}; [\mathcal{WU}^x]$.

Analogously to mutex well-formedness, MRSW well-formedness requires that the first call in each thread be either to the constructor or for lock acquisition, a constructor call be immediately followed (in po) by a lock acquisition; each reader unlock or lock promotion call be immediately preceded (in po) by a reader lock acquisition call and vice versa; and each writer unlock call be immediately preceded (in po) by a writer lock acquisition or lock promotion call and vice versa.

Strong MRSW-Consistency. Note that there are no com edges between events of reader locks. Consequently, as $\text{so} = \text{com}$, reader lock events do not synchronise with one another. This is to keep the specification as general as possible and admit certain MRSW implementations with weaker guarantees such as those discussed in §E. Nevertheless, we can strengthen this specification by extending the domain of com_r as: $\text{com}_r \subseteq (C^x \cup \mathcal{WU}^x \cup \mathcal{RL}^x \cup \mathcal{RU}^x) \times \mathcal{RL}^x$, and the domain of com_p as: $\text{com}_p \subseteq (\mathcal{RU}^x \cup \mathcal{RL}^x) \times \mathcal{PL}^x$. That is, for each $l \in \mathcal{RL}^x$ with $(e, l) \in \text{com}_r$, the case where $e \in C^x$ can be described as before; when $e \in \mathcal{WU}^x \cup \mathcal{RU}^x$ then e denotes the (reader or writer) unlock event releasing the lock *immediately* before it is acquired by l ; when $e \in \mathcal{RL}^x$, then e denotes the event acquiring a reader lock on x *immediately* before it is also acquired by l . This is because MRSW locks allow for multiple reader locks simultaneously. Note that this is in contrast to the com_r edges above. In particular, in the above (weaker) description, when $e \in \mathcal{WU}^x$, then e denotes the last release of the writer lock on x before its acquisition by l ; i.e. e may not be *immediately* preceding l and may be interleaved by several reader (lock or unlock) events. Similarly, for each $l \in \mathcal{PL}^x$ with $(e, l) \in \text{com}_p$, when $e \in \mathcal{RU}^x$, then e denotes the reader unlock event releasing the lock *immediately* before it is acquired by l ; when $e \in \mathcal{RL}^x$, then e denotes the *very reader lock* being promoted: no other reader locks on x have been acquired between its acquisition (e) and its promotion (l). As such, in contrast to (3), we require that com be functional.

We thus denote $\langle E, \text{po}, \text{com}, \text{so}, \text{lhb} \rangle$ as *strongly RW-consistent* on x iff: (1) as above; (2) $\text{com} =$

$\text{com}_w \cup \text{com}_r \cup \text{com}_p$ with $\text{com}_r \subseteq (C^x \cup \mathcal{WU}^x \cup \mathcal{RL}^x \cup \mathcal{RU}^x) \times \mathcal{RL}^x$, com_w as above, and $\text{com}_p \subseteq (\mathcal{RL}^x \cup \mathcal{RU}^x) \times \mathcal{PL}^x$; (3) com is functional; and (4)-(6) as above.

Definition C.1 (MRSW library). The MRSW library is $L^{\text{RW}} \triangleq \langle \mathcal{M}^{\text{RW}}, \mathcal{M}_c^{\text{RW}}, \text{loc}^{\text{RW}}, \mathcal{G}_c^{\text{RW}}, \mathcal{G}_{\text{wf}}^{\text{RW}} \rangle$, where $\mathcal{G}_c^{\text{RW}} \triangleq \{G \in \mathcal{G}_{L^{\text{RW}}} \mid \forall x. G_x \text{ RW-consistent on } x\}$ and $\mathcal{G}_{\text{wf}}^{\text{RW}} \triangleq \{G \in \mathcal{G}_{L^{\text{RW}}} \mid \forall x. G_x \text{ RW-well-formed on } x\}$. Let $\mathcal{G}_c^{\text{SRW}} \triangleq \{G \in \mathcal{G}_{L^{\text{RW}}} \mid \forall x. G_x \text{ strongly RW-consistent on } x\}$; the strong MRSW library is $L^{\text{SRW}} \triangleq \langle \mathcal{M}^{\text{RW}}, \mathcal{M}_c^{\text{RW}}, \text{loc}^{\text{RW}}, \mathcal{G}_c^{\text{SRW}}, \mathcal{G}_{\text{wf}}^{\text{RW}} \rangle$.

C.2 Set Library Specification

We consider a set library with four methods: *new-set()*, for constructing a new set; *add(s, v)* for adding v to the set at s ; *rem(s, v)* for removing v from the set at s ; and *is-in(s, v)* for checking the membership of v in the set at s . An *add(s, v)* call successfully adds v to s only if it does not already contain v ; analogously, a *rem(s, v)* successfully removes v if s contains v . Similarly, the return value of *is-in(s, v)* indicates whether s contains v . As such, all three operations return a boolean reflecting their outcomes. We present a set specification in our framework below. Once again, we forgo a strong specification with a total execution order in the linearisability style, and opt instead for a weaker specification more suitable for the WMC setting.

We define the *set interface* as $\langle \mathcal{M}^{\text{Set}}, \mathcal{M}_c^{\text{Set}}, \text{loc}^{\text{Set}} \rangle$, where $\forall l \in \mathcal{M}^s. \text{loc}^{\text{MX}}(l) = \{s\}$ and

$$\mathcal{M}_c^{\text{Set}} \triangleq \bigcup_{s \in \text{Loc}} \mathcal{M}_c^s \quad \mathcal{M}_c^s \triangleq \{\text{new-set}(s)\}$$

$$\mathcal{M}^{\text{Set}} \triangleq \bigcup_{s \in \text{Loc}} \mathcal{M}^s \quad \mathcal{M}^s \triangleq \mathcal{M}_c^s \cup \{\text{add}(s, v, o), \text{rem}(s, v, o), \text{is-in}(s, v, o) \mid v \in \text{Val} \wedge o \in \{\top, \perp\}\}$$

For a set at location s , we define the following sets of events:

$$\begin{aligned} C^s &\triangleq \{e \mid \text{lab}(e) = \text{new-set}(s)\} & I^{s,v,o} &\triangleq \{e \mid \text{lab}(e) = \text{is-in}(s, v, o)\} \\ \mathcal{A}^{s,v,o} &\triangleq \{e \mid \text{lab}(e) = \text{add}(s, v, o)\} & \mathcal{R}^{s,v,o} &\triangleq \{e \mid \text{lab}(e) = \text{rem}(s, v, o)\} \end{aligned}$$

Let $\mathcal{A}^{s,o} \triangleq \bigcup_{v \in \text{Val}} \mathcal{A}^{s,v,o}$ and $\mathcal{A}^s \triangleq \mathcal{A}^{s,\top} \cup \mathcal{A}^{s,\perp}$; let us similarly define $\mathcal{R}^{s,o}, \mathcal{R}^s, I^{s,o}$ and I^s . A tuple $\langle E, \text{po}, \text{com}, \text{so}, \text{lhb} \rangle$ is *set-consistent on s* iff:

- (1) there is at most one constructor event: $E^c = \emptyset \vee \exists c \in C^s. E^c = \{c\}$;
- (2) com relates matching events: $\text{com} \triangleq \text{com}_r \cup \text{com}_i \cup \text{com}_f$, where $\text{com}_r \subseteq \bigcup_{v \in \text{Val}} \mathcal{A}^{s,v,\top} \times \mathcal{R}^{s,v,\top}$, $\text{com}_i \subseteq \bigcup_{v \in \text{Val}} \mathcal{A}^{s,v,\top} \times I^{s,v,\top}$, $\text{com}_f \subseteq \bigcup_{v \in \text{Val}} \mathcal{A}^{s,v,\top} \times \mathcal{A}^{s,v,\perp}$;
- (3) every remove, membership and failed add is matched by at most one add: com^{-1} is functional;
- (4) every add is matched by at most one remove: com_r is functional;
- (5) every unmatched remove or membership returns \perp : $(E \cap (\mathcal{R}^s \cup I^s)) \setminus \text{rng}(\text{com}) \subseteq \mathcal{R}^{s,\perp} \cup I^{s,\perp}$;
- (6) every failed add event is matched: $(E \cap \mathcal{A}^{s,\perp}) \setminus \text{rng}(\text{com}) = \emptyset$;
- (7) every matching edge is synchronising: $\text{so} = \text{com}$; and
- (8) value v cannot be added twice before being removed first; that is, for all v : $[\mathcal{A}^{s,v,\top}]; \text{lhb}; [\mathcal{A}^{s,v,\top}]; \text{lhb}; \text{com}_r^{-1}$ is irreflexive and $[\mathcal{A}^{s,v,\top} \setminus \text{rng}(\text{com}_r)]; \text{lhb}; [\mathcal{A}^{s,v,\top}] = \emptyset$;
- (9) adding a value must not fail when it is already removed: $\text{com}_r; \text{lhb}; \text{com}_f^{-1}$ is irreflexive;
- (10) removing a value must not fail when the value is yet to be removed: $\forall v. [\mathcal{R}^{s,v,\top} \cup I^{s,v,\top}]; \text{com}_r^{-1} \cup \text{com}_f^{-1}; \text{lhb}; [\mathcal{R}^{s,v,\perp}]; \text{lhb}$ is irreflexive;
- (11) membership check for a value must not fail when the value is yet to be removed: $\forall v. [\mathcal{R}^{s,v,\top} \cup I^{s,v,\top}]; \text{com}_r^{-1} \cup \text{com}_f^{-1}; \text{lhb}; [I^{s,v,\perp}]; \text{lhb}$ is irreflexive;
- (12) a remove or membership with a previous unmatched add cannot return \perp : $\forall v. [\mathcal{A}^{s,v,\top} \setminus \text{dom}(\text{com}_r)]; \text{lhb}; [\mathcal{R}^{s,v,\perp} \cup I^{s,v,\perp}] = \emptyset$;


```

1912      lock(x)  $\triangleq$ 
1913  new-mutex()  $\triangleq$       loop      unlock(x)  $\triangleq$ 
1914    let x = alloc() in x    if compare-set(x, 0, 1, acq) then    store(x, 0, rel);
1915                          break1 ()

```

Fig. 7. A simple mutex implementation using a release acquire register

- (13) successful remove events cannot match with adds which are already removed:
 $[\mathcal{A}^{s,v,\top}]; \text{lh}; [\mathcal{R}^{s,v,\top}]; \text{lh}; \text{com}_r^{-1}$ is irreflexive;
- (14) successful membership events cannot match with adds which are already removed:
 $\text{com}_i^{-1}; \text{com}_r; \text{lh}$ is irreflexive.

Intuitively, $(a, r) \in \text{com}_r$ denotes that r removes a value added by a . As such, each successful remove is matched by exactly one add, and each successful add is matched by at most one remove. Similarly, $(a, i) \in \text{com}_i$ denotes that i observes the value added by a . Each successful membership is thus matched by exactly one add. However, as membership calls leave the set unchanged, multiple membership events may be matched by the same add. Lastly, $(a, f) \in \text{com}_f$ denotes that f fails to add its value to the set as it has been previously added by a . Each failed add is hence matched by exactly one successful add, whilst each successful add may be matched by several failed adds.

Definition C.2 (Set library). The *set library* is $L^{\text{Set}} \triangleq \langle \mathcal{M}^{\text{Set}}, \mathcal{M}_c^{\text{Set}}, \text{loc}^{\text{Set}}, \mathcal{G}_c^{\text{Set}}, \mathcal{G}_{L^{\text{Set}}} \rangle$, where $\mathcal{G}_c^{\text{Set}} \triangleq \{G \in \mathcal{G}_{L^{\text{Set}}} \mid \forall s. G_s \text{ is set-consistent on } s\}$.

D A SOUND MUTEX IMPLEMENTATION

In Fig. 7 we present a simple implementation of mutex locks using release-acquire registers. As we formalise in Thm. 8, this implementation is sound with respect to the mutex library L^{MX} .

Theorem 8. *The mutex implementation in Fig. 7 is a sound implementation of L^{MX} .*

PROOF. The full proof is mechanised in the Coq proof assistant and is available as auxiliary material. \square

```

1961 new-MSRW()  $\triangleq$                                 wlock(x)  $\triangleq$ 
1962   let x = alloc() in x                                loop
1963                                                         if compare-set(x, 0, 1, acq) then
1964   rlock(x)  $\triangleq$                                        break1 ()
1965   loop
1966     let c = load(x, rlx) in
1967     if is-even(c) then
1968       if compare-set(x, c, c+2, acqrel) then
1969         break1 ()
1970   runlock(x)  $\triangleq$ 
1971   fetch-add(x, -2, rel)
1972
1973   wunlock(x)  $\triangleq$ 
1974   store(x, 0, rel);
1975
1976   plock(x)  $\triangleq$ 
1977   loop
1978     let b = 0 in
1979     if b then
1980       let c = load(x, acq) in
1981       if c == 3 then break1 ()
1982     else let c = load(x, rlx) in
1983       if is-even(c) then
1984         if compare-set(x, c, c+1, acq) then
1985           b = 1

```

Fig. 8. An implementation of strong MRSW locks using a C11 register

E SOUND MRSW LOCK IMPLEMENTATIONS

E.1 A Sound Strong MRSW Lock Implementation

We present a strong MRSW lock implementation using the C11 registers: a lock at location *x* is represented as a C11 register at *x*. The state of a lock *x* is represented by an integer value. A lock *x* may hold either:

- (1) value 0, denoting that the lock is free (not held in read or write mode); or
- (2) value 1, denoting that the lock is held (exclusively) in write mode; or
- (3) an even value $2n$ with $n > 0$, denoting that *x* is held in (shared) read mode by *n* readers; or
- (4) an odd value $2n+3$ with $n > 0$, denoting that the lock is currently being promoted, awaiting the release of *n* readers; or
- (5) value 3, denoting that the lock is successfully promoted.

As such, the implementation of *wlock*(*x*) simply spins until it can atomically update (via atomic *compare-set*) the value of *x* from zero (free) to one (acquired in write mode). Dually, the implementation of *wunlock*(*x*) simply releases the write lock by atomically assigning *x* to zero.

The implementation of *plock*(*x*) is more involved. As multiple readers may attempt to promote their reader locks simultaneously, promotion is granted on a ‘first-come-first-served’ bases. As such, the implementation of *plock*(*x*) first reads the value of *x* (the else branch). If *x* holds an odd value, then another reader is currently promoting *x* and thus promotion must be retried. On the other hand, if *x* holds an even value, then its value is atomically incremented (to an odd value) to signal the intention to promote. Moreover, *b* is set to 1 to indicate that the intention to promote has been successfully registered and promotion can enter the next waiting phase. The implementation then proceeds by spinning until all other readers have released their locks on *x* (i.e. *x* == 3), at which point *x* is successfully promoted and the implementation terminates. Note that once a reader has signalled its intention to promote *x* (by incrementing *x* to an odd value), any other such attempt to promote the lock on *x*, as well as calls to acquire it in read mode will fail thereafter until such time that *x* is released by its current promoter.

The implementation of *rlock*(*x*) is similar. It first checks whether *x* is odd (held in write mode or being promoted). If so then the implementation spins until *x* is even (free or held in read mode), at which point its value is incremented by two (to increase the number of readers by one) using the

atomic *fetch-add* operation, and x is successfully acquired in read mode. Dually, the implementation of *runlock*(x) atomically decrements the value of x by two to decrease the reader count by one.

Implementation Correctness. Let I denote the strong MRSW lock implementation in Fig. 8. To show the soundness of I , we appeal to Thm. 1 and show that I is locally sound on L^{SRW} .

Pick an arbitrary $\Lambda, f, G = \langle E, \text{po}, \text{com}, \text{so} \rangle, E'', \text{po}''$ such that G is Λ -consistent and Λ -well-formed and $\text{abs}_{L^{\text{SRW}}, I}(f, \langle E, \text{po} \rangle, \langle E'', \text{po}'' \rangle)$.

We must next find $\text{com}'', \text{so}''$ such that $\langle E'', \text{po}'', \text{com}'', \text{so}'', \text{lbh}'' \rangle \in L^{\text{SRW}}.\mathcal{G}_c$, where lbh'' is the same as lbh' in Def. 11.

For each location x , without loss of generality let us assume G contains n_x read lock calls on x , n'_x read unlock calls on x , m_x write lock calls on x , m'_x write unlock calls on x and p_x lock promotion calls on x . Let us enumerate each of read lock calls, read unlock calls, write lock calls, write unlock calls and lock promotion calls arbitrarily. Note that:

- the MRSW constructor at location x contains a single event c_x where $\text{lab}(c_x) = \text{alloc}(x, 0)$.
- For each i th read lock operation on x , the G contains the trace $\theta_i^{rl(x)} = rf^* \xrightarrow{\text{polimm}} rr \xrightarrow{\text{polimm}} rl$, where rf^* denotes the events of those iterations that failed to acquire the reader lock, $\text{lab}(rr) = \text{load}(rlx, x, rv_i)$, $\text{lab}(rl) = \text{compare-set}(\text{acqrel}, x, rv_i, rv_i+2)$, and rv_i is an even value.
- for each i th read unlock operation on x , the G contains the trace $\theta_i^{ru(x)}$ with a single event ru , where $\text{lab}(ru) = \text{fetch-add}(\text{rel}, x, v_i, v_i-2)$ for some v_i .
- for each i th write lock operation on x , the G contains the trace $\theta_i^{wl(x)} = wf^* \xrightarrow{\text{polimm}} wl$, where wf^* denotes the events of those iterations that failed to acquire the writer lock and $\text{lab}(wl) = \text{compare-set}(\text{acq}, x, 0, 1)$.
- for each i th write unlock operation on x , the G contains the trace $\theta_i^{wu(x)}$ with a single event wu , where $\text{lab}(wu) = \text{store}(\text{rel}, x, 0)$.
- for each i th read lock promotion operation on x , the G contains the trace $\theta_i^{pl(x)} = pfl^* \xrightarrow{\text{polimm}} pr \xrightarrow{\text{polimm}} pi \xrightarrow{\text{po}} pf2^* \xrightarrow{\text{polimm}} pl$, where pfl^* denotes the events of those iterations that failed to indicate lock promotion, $pf2^*$ denotes the events of those iterations that failed to promote the lock, $\text{lab}(pr) = \text{load}(rlx, x, pv_i)$, $\text{lab}(pi) = \text{compare-set}(\text{acqrel}, x, pv_i, pv_i+1)$, pv_i is an even value, and $\text{lab}(pl) = \text{load}(\text{acq}, x, 3)$.

Let $\text{imp}(\cdot) : E'' \rightarrow E$ be defined as:

$$\text{imp}(e) \triangleq \begin{cases} c_x & \exists x. e = f(c_x) \\ \theta_i^{rl(x)}.rl & \exists i, x. e = f(\theta_i^{rl(x)}.rl) \\ \theta_i^{ru(x)}.ru & \exists i, x. e = f(\theta_i^{ru(x)}.ru) \\ \theta_i^{wl(x)}.wl & \exists i, x. e = f(\theta_i^{wl(x)}.wl) \\ \theta_i^{wu(x)}.wu & \exists i, x. e = f(\theta_i^{wu(x)}.wu) \\ \theta_i^{pl(x)}.pl & \exists i, x. e = f(\theta_i^{pl(x)}.pl) \end{cases}$$

Let us define: $\text{so}'' = \text{com}''$ with com'' defined as follows:

$$\text{com}'' \triangleq \left\{ \begin{array}{l} (e_1, e'_1), \\ (e_2, e'_2), \\ (f_a(e_3), f(\theta_k^{pl(x)}.pl)), \\ (f_a(e_4), f(\theta_h^{pl(x)}.pl)) \end{array} \middle| \begin{array}{l} x \in \text{Loc} \wedge (\text{imp}(e_1), \text{imp}(e'_1)) \in \text{com} \wedge \exists i. \text{imp}(e'_1) \in \theta_i^{rl(x)} \\ \wedge (\text{imp}(e_2), \text{imp}(e'_2)) \in \text{com} \wedge \exists j. \text{imp}(e'_2) \in \theta_j^{wl(x)} \\ \wedge \exists k, e_3. (\theta_k^{pl(x)}.pi, \theta_k^{pl(x)}.pl) \in \text{com} \wedge (e_3, \theta_k^{pl(x)}.pi) \in \text{com} \\ \wedge \exists h, e_4. (e_4, \theta_h^{pl(x)}.pl) \in \text{com} \wedge e_4 \neq \theta_h^{pl(x)}.pi \end{array} \right\}$$

To show that $G' = \langle E'', \text{po}'', \text{com}'', \text{so}'', \text{lhb}'' \rangle \in L^{\text{SRW}}.\mathcal{G}_c$, we are then required to show for all $x \in \text{Loc}$, G'_x is RW-consistent on x . Pick an arbitrary $x \in \text{Loc}$ and let $G'_x = \langle E', \text{po}', \text{com}', \text{so}', \text{lhb}' \rangle$. We then need to show:

- (1) G'_x contains at most one constructor event;
- (2) $\text{com}' = \text{com}_w \cup \text{com}_r \cup \text{com}_p$ with:
 $\text{com}_w \subseteq (C^x \cup \mathcal{U}^x) \times \mathcal{WL}^x$; $\text{com}_r \subseteq (C^x \cup \mathcal{WU}^x \cup \mathcal{RL}^x \cup \mathcal{RU}^x) \times \mathcal{RL}^x$; and $\text{com}_p \subseteq (\mathcal{RL}^x \cup \mathcal{RU}^x) \times \mathcal{PL}^x$
- (3) com' is functional;
- (4) com'^{-1} is functional;
- (5) $E \cap \mathcal{L}^x = \text{rng}(\text{com}')$; and
- (6) $\text{so}' = \text{com}'$.

Parts (1), (5) and (6) follow immediately from the construction of G'_x . For part (2), pick an arbitrary $(a, b) \in \text{com}'$. From the definition of com' we then know that there exists i such that either a) $b = rl_i^x$ and $(\text{imp}(a), \text{imp}(rl_i^x)) \in \text{com}$; or b) $b = wl_i^x$ and $(\text{imp}(a), \text{imp}(wl_i^x)) \in \text{com}$; or c) $b = pl_i^x$ and there exists e such that $a = f_a(e)$, $(\theta_i^{pl(x)}.pi, \text{imp}(pl_i^x)) \in \text{com}$ and $(e, \theta_i^{pl(x)}.pi) \in \text{com}$; or d) $b = pl_i^x$ and there exists e such that $a = f_a(e)$, $(e, \text{imp}(pl_i^x)) \in \text{com}$ and $e \neq \theta_i^{pl(x)}.pi$.

In case (a), since the value read by $\text{imp}(rl_i^x)$ is even, from the implementation encapsulation (Thm. 1) we know there exists j such that $\text{imp}(a) = c_x$ or $\text{imp}(a) = \theta_j^{rl(x)}.rl$ or $\text{imp}(a) = \theta_j^{ru(x)}.ru$ or $\text{imp}(a) = \theta_j^{wu(x)}.wu$. As such, we know that either $a \in C^x \cup \mathcal{RL}^x \cup \mathcal{RU}^x \cup \mathcal{WU}^x$, as required.

Similarly in case (b), since the value read by $\text{imp}(wl_i^x)$ is zero, from the implementation encapsulation (Thm. 1) we know there exists j such that $\text{imp}(a) = c_x$ or $\text{imp}(a) = \theta_j^{ru(x)}.ru$ or $\text{imp}(a) = \theta_j^{wu(x)}.wu$. As such, we know that either $a \in C^x \cup \mathcal{RU}^x \cup \mathcal{WU}^x$, as required.

In case (c) we then know that the value read by $\theta_i^{pl(x)}.pi$ is 2; and thus from the implementation encapsulation (Thm. 1) we know e is either a read unlock event or a read lock event. That is, there exists j such that $e = \theta_j^{ru(x)}.ru$ or $e = \theta_j^{rl(x)}.rl$, as required.

In case (d), since the value read by $\text{imp}(pl_i^x)$ is 3, from the implementation encapsulation (Thm. 1) we know there exists j such that $e = \theta_j^{ru(x)}.ru$, as required.

For part (3) we proceed by contradiction. Let us assume there exists e, e_1, e_2 such that $(e, e_1), (e, e_2) \in \text{com}$. We then know there exists i such that either a) $e_1 = rl_i^x$; or b) $e_1 = wl_i^x$; or c) $e_1 = pl_i^x$. Similarly, we know there exists j such that either i) $e_2 = rl_j^x$; or ii) $e_2 = wl_j^x$; or iii) $e_2 = pl_j^x$.

In case (a-i), from the definition of com' we know $(\text{imp}(e), \text{imp}(rl_i^x)), (\text{imp}(e), rl_j^x) \in \text{com}$. However, since both rl_i^x and rl_j^x are atomic update operations, from the C11 consistency we know that there exists mo such that $(\text{imp}(e), \text{imp}(rl_i^x)), (\text{imp}(e), \text{imp}(rl_j^x)) \in \text{mo}_{\text{limm}}$, and that either $\text{imp}(rl_i^x) \xrightarrow{\text{mo}} \text{imp}(rl_j^x)$ or $\text{imp}(rl_j^x) \xrightarrow{\text{mo}} \text{imp}(rl_i^x)$. In the former case we have $\text{imp}(e) \xrightarrow{\text{mo}} \text{imp}(rl_i^x) \xrightarrow{\text{mo}} \text{imp}(rl_j^x)$ and thus $(\text{imp}(e), \text{imp}(rl_j^x)) \notin \text{mo}_{\text{limm}}$, leading to contradiction. Similarly, in the latter case we have $\text{imp}(e) \xrightarrow{\text{mo}} \text{imp}(rl_j^x) \xrightarrow{\text{mo}} \text{imp}(rl_i^x)$ and thus $(\text{imp}(e), \text{imp}(rl_i^x)) \notin \text{mo}_{\text{limm}}$, leading to contradiction.

The proof of the remaining cases (a-ii)-(a-iii), (b-i)-(b-iii) and (c-i)-(c-iii) are analogous and are omitted here.

Part (4) follows from the definition of com' and the functionality of com^{-1} for C11 registers. \square

```

2108                                     wlock(x)  $\triangleq$ 
2109                                     for i = 0 to K do
2110                                     loop
2111                                     if compare-set(x[i], 0, 1, acq) then
2112                                     break1 ()
2113 new-MSRW()  $\triangleq$ 
2114   let x = alloc(K) in x
2115   rlock(x)  $\triangleq$ 
2116   let t = get-tid() in
2117   loop
2118   if compare-set(x[t], 0, 2, acq) then
2119   break1 ()
2120   runlock(x)  $\triangleq$ 
2121   let t = get-tid() in
2122   store(x[t], 0, rel)
2123   wunlock(x)  $\triangleq$ 
2124   for i = 0 to K do
2125   store(x[i], 0, rel);
2126   plock(x)  $\triangleq$ 
2127   let t = get-tid() in
2128   let c = load(x[t], rlx) in
2129   if c == 2 then
2130   if t == 0 then store(x[t], 1, rlx)
2131   else
2132   loop
2133   if compare-set(x[0], 0, 1, acq) then
2134   store(x[t], 1, rel); break1 ()
2135   for i = 1 to K do
2136   if (i  $\neq$  t) then
2137   loop
2138   if compare-set(x[i], 0, 1, acq) then
2139   break1 ()

```

Fig. 9. An implementation of weak MRSW locks (for K threads) using a K -array of C11 registers

E.2 A Sound Weak MRSW Lock Implementation

Our second MRSW lock implementation is similarly implemented using C11 registers and is given in Fig. 9. In this implementation, a lock at location x is represented as an *ordered* map of size K at location x . The map at x contains one entry per thread (when there are K threads present) as follows. For each thread with identifier τ , the $x[\tau]$ map entry records the current locking privileges of τ on x . More concretely, when $x[\tau] = 0$, then τ does not hold the x lock; when $x[\tau] = 2$, then τ holds x in read mode; and when $x[\tau] = 1$; then *some* thread (either τ or another thread) either holds x in write mode, or it is in the process of acquiring x in write mode. The x lock is held in write mode only when all entries in x are mapped to one. As we describe shortly, for thread τ to acquire x in write mode, it must inspect each entry in x (in order), wait for it be free (zero) and then set it to one. In our implementation, we assume that the thread identifier can be obtained by calling `get-tid()`. We identify the top-most thread by $\tau = 0$; as such, the entry of top-most thread in each map is ordered before all other threads.

We proceed with a more detailed explanation of our implementation after introducing our map notation.

Map notation. We write **1** to denote a map where all entries have value 1; similarly, we write **0** to denote a map where all entries have value 0. Lastly, we write $S \subseteq x$, to denote that the values held in map x are a superset of S . The lock map x associated with location x can be in one of the following states:

- $x = \mathbf{0}$ when x is free;
- $x = \mathbf{1}$ when x is held in write mode;
- $\{2\} \subseteq x$ when x is held in read mode (by those threads τ where $x[\tau] = 2$).

When thread τ calls $rlock(x)$, it simply spins until the lock is free ($x = 0$ and thus $x[\tau] = 0$), at which point it acquires it in read mode by setting $x[\tau]$ to two. Dually, when τ calls $runlock(x)$ it simply sets $x[\tau]$ to zero.

Analogously, when τ calls $wlock(x)$, it traverses the x map in order, spinning on each entry until it is free (0) and subsequently acquiring it (by setting it to 1). Conversely, when τ calls $wunlock(x)$, it releases x by traversing x in order and setting each entry to one.

Recall that in order to promote a reader lock, the calling thread must already hold a reader lock on x . As such, the implementation of $plock(x)$ first check whether the calling thread τ currently holds a reader lock on x , i.e. $x[\tau] = 2$. If this is not the case then the implementation simply returns. To understand the remainder of the implementation, first consider the case where $plock(x)$ is called by $\tau \neq 0$, i.e. a thread other than the top-most thread. The implementation of $plock(x)$ then inspects the *first* entry in the map ($x[0]$), i.e. that of the top-most thread. If $x[0] = 1$, then x is currently being acquired by another thread; the promotion must thus be retried. If on the other hand $x[0] \neq 1$ (i.e. $x[0] = 0$ or $x[0] = 2$), the implementation spins until it is zero and atomically updates it to one, signalling its intention to promote x . This pre-empts the promotion of x by other threads: any such attempt would fail as now $x[0] = 1$. The implementation then sets its own entry ($x[\tau]$) to one, traverses the map in order, and spins on each entry until they too can be set to one. At this point the lock is successfully promoted and the implementation returns. Note that it is safe for τ to update its own entry $x[\tau]$ to one: at this point in execution no thread holds the writer lock on x , no thread can promote its lock on x , and those threads with a reader lock on x never access the $x[\tau]$ entry – the read lock calls of another thread τ' solely accesses $x[\tau']$.

Let us now consider the case when the top-most thread with $\tau = 0$ calls `can-promote` x . Since prior to a $plock(x)$ call τ owns a reader lock on x , i.e. $x[\tau] = 2$, no other thread can promote its x lock. As such, τ successfully sets $x[\tau]$ to one, signalling its intention to promote x . In other words, the promotion is skewed in favour of the top-most thread: if a thread races against the top-most thread to promote x , the top-most thread always wins. With the exception of the top-most thread, promotion is done on a ‘first-come-first-served’ basis. The rest of the implementation is then carried out as before: the map x is traversed in turn and each entry is set to one.

Implementation Correctness. Let I denote the weak MRSW lock implementation in Fig. 9. To show the soundness of I , we appeal to Thm. 1 and show that I is locally sound on L^{SRW} .

Pick an arbitrary $\Lambda, f, G = \langle E, \text{po}, \text{com}, \text{so} \rangle, E'', \text{po}''$ such that G is Λ -consistent and Λ -well-formed and $\text{abs}_{L^{RW}, I}(f, \langle E, \text{po} \rangle, \langle E'', \text{po}'' \rangle)$.

We must next find $\text{com}'', \text{so}''$ such that $\langle E'', \text{po}'', \text{com}'', \text{so}'', \text{lbh}'' \rangle \in L^{RW}.\mathcal{G}_c$, where lbh'' is the same as lbh' in Def. 11.

For each location x , without loss of generality let us assume G contains n_x read lock calls on x , n'_x read unlock calls on x , m_x write lock calls on x , m'_x write unlock calls on x and p_x lock promotion calls on x . Let us enumerate each of read lock calls, read unlock calls, write lock calls, write unlock calls and lock promotion calls arbitrarily. Note that:

- the MRSW constructor at location x contains a single event c_x where $\text{lab}(c_x) = \text{alloc}(x, 0)$.
- For each i th read lock operation on x , the G contains the trace $\theta_i^{rl(x)} = t \xrightarrow{\text{po}_{\text{limm}}} rf^* \xrightarrow{\text{po}_{\text{limm}}} rl$, where rf^* denotes the events of those iterations that failed to acquire the reader lock (failed CAS), $\text{lab}(t) = \text{get-tid}(\tau)$ for some τ , $\text{lab}(rl) = \text{compare-set}(\text{acqrel}, x[\tau], 0, 2)$.
- for each i th read unlock operation on x , the G contains the trace $\theta_i^{ru(x)} = t \xrightarrow{\text{po}_{\text{limm}}} ru$, where $\text{lab}(t) = \text{get-tid}(\tau)$ for some τ and $\text{lab}(ru) = \text{store}(\text{rel}, x[\tau], 0)$.

- for each i th write lock operation on x , the G contains the trace $\theta_i^{wl(x)} = wf_0^* \xrightarrow{po|limm} wl_0 \xrightarrow{po} \dots \xrightarrow{po} wf_K^* \xrightarrow{po|limm} wl_K$, where for each $j \in \{0 \dots K\}$, the wf_j^* denotes the events for which the CAS on $x[j]$ failed, and $lab(wl_j) = \text{compare-set}(\text{acq}, x[j], 0, 1)$.
- for each i th write unlock operation on x , the G contains the trace $\theta_i^{wu(x)} = wu_0 \xrightarrow{po} \dots \xrightarrow{po} wu_K$, where for each $j \in \{0 \dots K\}$, $lab(wu_j) = \text{store}(\text{rel}, x[j], 0)$.
- for each i th read lock promotion operation on x the G contains either the trace $\theta_i^{bpl(x)}$; or the trace $\theta_i^{hpl(x)}$; or the trace $\theta_i^{pl(x)}$.

The $\theta_i^{bpl(x)}$ trace is of the form $t \xrightarrow{po|limm} cp$ with $lab(t) = \text{get-tid}(\tau)$ for some τ , and $lab(cp) = \text{load}(\text{rlx}, x[\tau], v)$ for some $v \neq 2$.

The $\theta_i^{hpl(x)}$ is of the form: $t \xrightarrow{po|limm} cp \xrightarrow{po|limm} pi \xrightarrow{po|limm} pf_1^* \xrightarrow{po|limm} pl_1 \xrightarrow{po} \dots \xrightarrow{po} pf_K^* \xrightarrow{po|limm} pl_K$, where $lab(t) = \text{get-tid}(0)$ and $lab(pi) = \text{store}(\text{rlx}, x[0], 1)$.

The $\theta_i^{pl(x)}$ is of the form: $t \xrightarrow{po|limm} cp \xrightarrow{po|limm} pif^* \xrightarrow{po|limm} pi \xrightarrow{po|limm} pf_1^* \xrightarrow{po|limm} pl_1 \xrightarrow{po} \dots \xrightarrow{po} pf_{\tau-1}^* \xrightarrow{po|limm} pl_{\tau-1} \xrightarrow{po|limm} pf_{\tau+1}^* \xrightarrow{po|limm} pl_{\tau+1} \xrightarrow{po} \dots \xrightarrow{po} pf_K^* \xrightarrow{po|limm} pl_K$, where $lab(t) = \text{get-tid}(\tau)$ and $\tau \neq 0$. The pif^* denotes the sequence of events failing to indicate lock promotion by setting $x[0]$ to 1; and $lab(pi) = \text{compare-set}(\text{acq}, x[0], 0, 1)$.

In both cases, $lab(cp) = \text{load}(\text{rlx}, x[\tau], 2)$ and for $j \in \{1, \dots, K\}$, the pf_j^* denotes the events for which the CAS on $x[j]$ failed, and $lab(pl_j) = \text{compare-set}(\text{acq}, x[j], 0, 1)$.

Let us define: $\text{so}'' = \text{com}'$, with com'' defined as follows:

$$\begin{aligned}
 \text{com}'' \triangleq & \left\{ (f_a(a), f_a(b)) \mid (a, b) \in \text{com} \wedge \exists i, j, x, \tau. b = \theta_i^{wl(x)}.wl_\tau \wedge a = \theta_j^{ru(x)}.ru \right\} \\
 \cup & \left\{ (f_a(a), f_a(b)) \mid \begin{array}{l} (a, b) \in \text{com} \wedge \exists i, j, x. \\ b = \theta_i^{wl(x)}.wl_0 \wedge a = \theta_j^{wu(x)}.wu_0 \\ \wedge \forall k, c. 0 \leq k \leq K \wedge (c, \theta_i^{wl(x)}.wl_k) \in \text{com} \Rightarrow \neg \exists h. c = \theta_h^{ru(x)}.ru \end{array} \right\} \\
 \cup & \left\{ (f_a(a), f_a(b)) \mid \begin{array}{l} (a, b) \in \text{com} \wedge \exists i, j, x, \tau. \quad b = \theta_i^{wl(x)}.wl_0 \wedge a = c_x \\ \wedge \forall k, c. 0 \leq k \leq K \wedge (c, \theta_i^{wl(x)}.wl_k) \in \text{com} \Rightarrow \neg \exists h. c = \theta_h^{ru(x)}.ru \end{array} \right\} \\
 \cup & \left\{ (f_a(a), f_a(b)) \mid \begin{array}{l} (a, b) \in \text{com} \wedge \exists i, j, x, \tau. \\ b = \theta_i^{rl(x)}.rl \wedge (a = c_x \vee a = \theta_j^{wu(x)}.wu_\tau) \end{array} \right\} \\
 \cup & \left\{ (f_a(a), f_a(b)) \mid \begin{array}{l} \exists c, i, j, k, x, \tau. \\ (c, b) \in \text{com} \wedge b = \theta_i^{rl(x)}.rl \wedge c = \theta_j^{ru(x)}.ru \\ \wedge (a = c_x \vee a = \theta_k^{wu(x)}.wu_\tau) \wedge (a, c) \in \text{mo} \\ \wedge \forall d. a \xrightarrow{\text{mo}} d \xrightarrow{\text{mo}} c \Rightarrow \\ \neg \exists h. (d = \theta_h^{wl(x)}.wl_\tau \vee d = \theta_h^{wu(x)}.wu_\tau \vee d = \theta_h^{pl(x)}.pl_\tau \vee d = \theta_h^{hpl(x)}.pl_\tau) \end{array} \right\} \\
 \cup & \left\{ (f_a(a), f_a(b)) \mid (a, b) \in \text{com} \wedge \exists i, j, x, \tau. (b = \theta_i^{pl(x)}.pl_\tau \vee b = \theta_i^{hpl(x)}.pl_\tau) \wedge a = \theta_j^{ru(x)}.ru \right\} \\
 \cup & \left\{ (f_a(a), f_a(b)) \mid \begin{array}{l} (a, b) \in \text{com} \wedge \exists i, j, x. \\ b = \theta_i^{pl(x)}.pl_0 \wedge a = \theta_j^{wu(x)}.wu_0 \\ \wedge \forall k, c. 0 \leq k \leq K \wedge (c, \theta_i^{pl(x)}.pl_k) \in \text{com} \Rightarrow \neg \exists h. c = \theta_h^{ru(x)}.ru \end{array} \right\}
 \end{aligned}$$

$$\begin{aligned}
& \cup \left\{ (f_a(a), f_a(b)) \mid \begin{array}{l} (a, b) \in \text{com} \wedge \exists i, j, x. \\ b = \theta_i^{hp(x)}.pl_0 \wedge a = \theta_j^{wu(x)}.wu_0 \\ \wedge \forall k, c. 0 \leq k \leq K \wedge (c, \theta_i^{hp(x)}.pl_k) \in \text{com} \Rightarrow \neg \exists h. c = \theta_h^{ru(x)}.ru \end{array} \right\} \\
& \cup \left\{ (f_a(a), f_a(b)) \mid \begin{array}{l} (a, b) \in \text{com} \wedge \exists i, j, x, \tau. b = \theta_i^{pl(x)}.pl_0 \wedge a = c_x \\ \wedge \forall k, c. 0 \leq k \leq K \wedge (c, \theta_i^{pl(x)}.pl_k) \in \text{com} \Rightarrow \neg \exists h. c = \theta_h^{ru(x)}.ru \end{array} \right\} \\
& \cup \left\{ (f_a(a), f_a(b)) \mid \begin{array}{l} (a, b) \in \text{com} \wedge \exists i, j, x, \tau. b = \theta_i^{hp(x)}.pl_0 \wedge a = c_x \\ \wedge \forall k, c. 0 \leq k \leq K \wedge (c, \theta_i^{hp(x)}.pl_k) \in \text{com} \Rightarrow \neg \exists h. c = \theta_h^{ru(x)}.ru \end{array} \right\}
\end{aligned}$$

To show that $G' = \langle E'', \text{po}'', \text{com}'', \text{so}'', \text{lhb}'' \rangle \in L^{\text{RW}}. \mathcal{G}_c$, we are then required to show for all $x \in \text{Loc}$, G'_x is RW-consistent on x . Pick an arbitrary $x \in \text{Loc}$ and let $G' = \langle E', \text{po}', \text{com}', \text{so}', \text{lhb}' \rangle$. We then need to show:

- (1) G'_x contains at most one constructor event;
- (2) $\text{com}' = \text{com}_w \cup \text{com}_r \cup \text{com}_p$ with:
 $\text{com}_w \subseteq (C^x \cup \mathcal{U}^x) \times \mathcal{WL}^x$ $\text{com}_r \subseteq (C^x \cup \mathcal{WU}^x) \times \mathcal{RL}^x$ $\text{com}_p \subseteq (C^x \cup \mathcal{U}^x) \times \mathcal{PL}^x$
- (3) for all e, e_1, e_2 :
 $e_1 \neq e_2 \wedge (e, e_1), (e, e_2) \in \text{com}' \Rightarrow (e_1, e_2 \in \mathcal{RL}^x) \vee (e_1 \in \mathcal{RL}^x \wedge e_2 \in \mathcal{PL}^x) \vee (e_1 \in \mathcal{PL}^x \wedge e_2 \in \mathcal{RL}^x)$
- (4) com'^{-1} is functional;
- (5) $E \cap \mathcal{L}^x = \text{rng}(\text{com}')$; and
- (6) $\text{so}' = \text{com}'$.

Parts (1), (5) and (6) follow immediately from the construction of G'_x . For part (2), pick an arbitrary $(a, b) \in \text{com}'$. From the definition of com' we then know that there exists i, j such that either:

- i) $b = wl_i^x, a = wu_j^x \vee a = ru_j^x \vee a = \text{con}_x$; or
- ii) $b = rl_i^x, a = wu_j^x \vee a = \text{con}_x$; or
- iii) $b = pl_i^x, a = wu_j^x \vee a = ru_j^x \vee a = \text{con}_x$

In case (i), we thus have $(a, b) \in (C^x \cup \mathcal{U}^x) \times \mathcal{WL}^x$, as required. In case (ii) we have $(a, b) \in (C^x \cup \mathcal{WU}^x) \times \mathcal{RL}^x$, as required. In case (iii), we have $(a, b) \in (C^x \cup \mathcal{U}^x) \times \mathcal{WL}^x$, as required.

For part (3) we proceed by contradiction. Let us assume there exists e, e_1, e_2 such that $e_1 \neq e_2$, $(e, e_1), (e, e_2) \in \text{com}$ and either: i) $e_1, e_2 \in \mathcal{WL}^x$; or ii) $e_1 \in \mathcal{WL}^x$ and $e_2 \in \mathcal{RL}^x$; or iii) $e_1 \in \mathcal{WL}^x$ and $e_2 \in \mathcal{PL}^x$; or iv) $e_1, e_2 \in \mathcal{PL}^x$.

In case (i), we know there exists a, b_1, b_2 such that $e = f_a(a), e_1 = f_a(b_1), e_2 = f_a(b_2), (a, b_1), (a, b_2) \in \text{com}$ and b_1 and b_2 are both atomic CAS operations. As such, from the C11 consistency we know $(a, b_1), (a, b_2) \in \text{mo}_{\text{limm}}$. Moreover, from C11 consistency we have either $(b_1, b_2) \in \text{mo}$ or $(b_2, b_1) \in \text{mo}$. In the former case we then have $a \xrightarrow{\text{mo}} b_1 \xrightarrow{\text{mo}} b_2$ and thus $(a, b_2) \notin \text{mo}_{\text{limm}}$, leading to a contradiction. Similarly, in the latter case we then have $a \xrightarrow{\text{mo}} b_2 \xrightarrow{\text{mo}} b_1$ and thus $(a, b_1) \notin \text{mo}_{\text{limm}}$, leading to a contradiction.

The proof of cases (ii) and (iii) are analogous to that of (i) and are omitted here.

In case (ii) we then know there exists a, b_1, b_2, c such that $e = f_a(a), e_1 = f_a(b_1), e_2 = f_a(b_2), b_1$ and b_2 are both atomic CAS operations, $(a, b_1) \in \text{com}$, and either a) $(a, b_2) \in \text{com}$; b) $(a, c) \in \text{mo}$, $(c, b_2) \in \text{com}$ and $\neg(a \xrightarrow{\text{mo}} b_1 \xrightarrow{\text{mo}} c)$. The proof of case (a) is analogous to the proof of case (1) above. For part (b), as b_2 is an atomic CAS from the C11 consistency we have $(c, b) \in \text{mo}_{\text{limm}}$. Moreover, from C11 consistency we have either $(b_1, c) \in \text{mo}$ or $(c, b_1) \in \text{mo}$. However, as we have $\neg(a \xrightarrow{\text{mo}} b_1 \xrightarrow{\text{mo}} c)$ and $(a, b_1) \in \text{mo}$, we then have $(c, b_1) \in \text{mo}$. As such, we have $a \xrightarrow{\text{mo}} c \xrightarrow{\text{mo}} b_1$ and thus $(a, b_1) \notin \text{mo}_{\text{limm}}$, leading to a contradiction.

Part (4) follows from the definition of com' and the functionality of com^{-1} for C11 registers. \square

```

2304  new-queue()  $\triangleq$ 
2305      let  $l = \text{new-mutex}()$  in
2306      let  $q = \text{alloc}(+\infty)$  in
2307      store( $q, l, \text{rlx}$ ); store( $q+1, 2, \text{rlx}$ );  $q$ 
2308  enq( $q, v$ )  $\triangleq$ 
2309      let  $l = \text{load}(q, \text{rlx})$  in
2310      lock( $l$ ); let  $i = \text{load}(q+1, \text{rlx})$  in
2311      store( $q+i, v, \text{rlx}$ ); store( $q+1, i+1, \text{rlx}$ );
2312      unlock( $l$ )
2313
2314  deque( $q$ )  $\triangleq$ 
2315      let  $l = \text{load}(q, \text{rlx})$  in
2316      lock( $l$ ); let  $\text{range} = \text{load}(q+1, \text{rlx})$  in
2317      for  $i = 1$  to  $\text{range}$  do
2318          let  $x = \text{load}(q+i, \text{rlx})$  in
2319          if  $x \neq 0$  then
2320              unlock( $l$ ); break1  $x$ 
2321      unlock( $l$ );  $\perp$ 

```

Fig. 10. The locking queue implementation

F A SOUND STRONG QUEUE IMPLEMENTATION

In Fig. 10 we present a simple implementation of a strong queue using release-acquire registers and the mutex library. As we formalise in Thm. 9, this implementation is sound with respect to the strong queue library L^{SQ} .

Theorem 9. *The queue implementation in Fig. 7 is a sound implementation of the strong queue library L^{SQ} .*

PROOF. The full proof is mechanised in the Coq proof assistant and is available as auxiliary material. \square

G THE SOUNDNESS OF EXCHANGER IMPLEMENTATION

Let I denote the exchanger implementation in Fig. 5. To show the soundness of I , we appeal to Thm. 1 and show that I is locally sound on L^X .

Pick an arbitrary $\Lambda, f, G = \langle E, \text{po}, \text{com}, \text{so} \rangle, E', \text{po}'$ such that G is Λ -consistent and Λ -well-formed and $\text{abs}_{L^X, I}(f, \langle E, \text{po} \rangle, \langle E', \text{po}' \rangle)$. We must next find com', so' such that $\langle E', \text{po}', \text{com}', \text{so}', \text{lbh}' \rangle \in L^X \cdot \mathcal{G}_c$, where lbh' is as defined in Def. 11.

Note that each exchange operation $\text{exchange}(g, v)$ either:

- (1) offers a value at index $g+k$ (for some $k \in \mathbb{N}^+$ where k is an odd number) and returns unmatched due to a timeout; or
- (2) offers a value at index $g+k$ (for some $k \in \mathbb{N}^+$ where k is an odd number) and matches with value v' at index $g+k+1$; or
- (3) tries to offer a value at index $g+k$ (for some $k \in \mathbb{N}^+$ where i is an even number) and returns unmatched as the slot at index $g+k$ is already taken; or
- (4) offers a value at index $g+k$ (for some $k \in \mathbb{N}^+$ where k is an even number) and matches with value v' at index $g+k-1$.

Without loss of generality, let us assume e contains n exchange calls. For each i^{th} exchange operation of the form $\text{exchange}(g, v_i)$, the G_i contains a trace of one of the following forms depending which of the four categories above it falls into:

- $\theta_i^{t(g)}$ is of the form $s \xrightarrow{\text{po}|_{\text{limm}}} o \xrightarrow{\text{po}|_{\text{limm}}} t$, with $\text{lab}(s) = \text{load}(\text{rlx}, g, j_i)$, $\text{lab}(o) = \text{CAS}(\text{rel}, g+j_i, 0, v_i)$, and $\text{lab}(t) = \text{CAS}(\text{rlx}, g+j_i+1, 0, \perp)$, for some j_i and v_i where j_i is odd;
- $\theta_i^{o(g)}$ is of the form $s \xrightarrow{\text{po}|_{\text{limm}}} o \xrightarrow{\text{po}|_{\text{limm}}} a \xrightarrow{\text{po}|_{\text{limm}}} r$, with $\text{lab}(s) = \text{load}(\text{rlx}, g, j_i)$, $\text{lab}(o) = \text{CAS}(\text{rel}, g+j_i, 0, v_i)$, $\text{lab}(a) = \text{load}(\text{rlx}, g+j_i+1, v'_i)$ with $v'_i \neq 0$, $\text{lab}(r) = \text{load}(\text{acq}, g+j_i+1, v'_i)$, for some j_i, v_i and v'_i where j_i is odd;

- $\theta_i^{f(g)}$ is of the form $s \xrightarrow{\text{po|limm}} f \xrightarrow{\text{po|limm}} o \xrightarrow{\text{po|limm}} c$, with $\text{lab}(s) = \text{load}(\text{rlx}, g, j_i)$, $\text{lab}(f) = \text{load}(\text{rlx}, g+j_i, w_i)$ with $w_i \neq 0$, $\text{lab}(o) = \text{load}(\text{rlx}, g+j_i+1, w'_i)$ with $w'_i \neq 0$, and $\text{lab}(c) = \text{CAS}(\text{rlx}, g, j_i, j_i+2)$ or $\text{lab}(c_i) = \text{load}(\text{rlx}, g, u_i)$ with $u_i \neq j_i$, for some j_i and v_i where j_i is odd;
- $\theta_i^{e(g)}$ is of the form $s \xrightarrow{\text{po|limm}} f \xrightarrow{\text{po|limm}} o \xrightarrow{\text{po|limm}} c \xrightarrow{\text{po|limm}} r$, with $\text{lab}(s) = \text{load}(\text{rlx}, g, j_i)$, $\text{lab}(f) = \text{load}(\text{rlx}, g+j_i, w_i)$ with $w_i \neq 0$, $\text{lab}(o) = \text{CAS}(\text{rel}, g+j_i+1, 0, v_i)$, $\text{lab}(c) = \text{CAS}(\text{rlx}, g, j_i, j_i+2)$ or $\text{lab}(c) = \text{load}(\text{rlx}, g, u_i)$ with $u_i \neq j_i$, $\text{lab}(r) = \text{load}(\text{acq}, g+j_i, v'_i)$, for some j_i , v_i and v'_i where j_i is odd.

Let $\text{so}' = \text{com}'$ with

$$\text{com}' \triangleq \left\{ (a, b), (b, a) \mid \begin{array}{l} \exists i, j, g. f(e_i^{o(g)}.o) = a \wedge f(e_j^{e(g)}.o) = b \\ \wedge (\theta_i^{o(g)}.o, \theta_j^{e(g)}.r) \in \text{com} \wedge (\theta_j^{e(g)}.o, \theta_i^{o(g)}.r) \in \text{com} \end{array} \right\}$$

To show that $G' = \langle E', \text{po}', \text{com}', \text{so}', \text{lhb}' \rangle \in L^X \mathcal{G}_c$, we are then required to show for all $g \in \text{Loc}$, G'_g is exchanger-consistent on g . Pick an arbitrary $g \in \text{Loc}$, we then need to show:

- 1) $E'^c = \emptyset \vee \exists c \in C^g. E'^c = \{c\}$;
- 2) com' is symmetric, irreflexive and $\text{com}' \subseteq \bigcup_{v_1, v_2 \in \text{Val}} \mathcal{X}^{g, v_1, v_2} \times \mathcal{X}^{g, v_2, v_1} \setminus \text{id}$;
- 3) com' is functional;
- 4) $E' \cap \mathcal{X}^g \setminus \text{dom}(\text{com}') \subseteq \bigcup_{v \in \text{Val}} \mathcal{X}^{g, v, \perp}$; and
- 5) $\text{so}' = \text{com}'$.

Parts (1), (2), and (5) follow immediately from the construction of G_s and the consistency of the C library. The proof of parts (3) and (4) follows from the definition of com' , the consistency of G_i and the definition of the C library.

H CORRECTNESS OF THE HERLIHY-WING QUEUE IMPLEMENTATIONS

H.1 Soundness of the Strong Herlihy-Wing Queue Implementation

Let I denote the strong Herlihy-Wing implementation in Fig. 2. To show the soundness of I , we appeal to Thm. 1 and show that I is locally sound on L^{SQ} .

Pick an arbitrary $\Lambda, f, G = \langle E, \text{po}, \text{com}, \text{so} \rangle, E'', \text{po}''$ such that G is Λ -consistent and Λ -well-formed and $\text{abs}_{L^{\text{SQ}}, I}(f, \langle E, \text{po} \rangle, \langle E'', \text{po}'' \rangle)$. We must next find $\text{com}'', \text{so}''$ such that $\langle E'', \text{po}'', \text{com}'', \text{so}'', \text{lh}' \rangle \in L^{\text{SQ}}. \mathcal{G}_c$, where lh' is the same as lh' in Def. 11.

For each queue at q , let us enumerate the enqueue operations on q by their insertion index. For instance, the very first enqueue operation is that which inserts the new value at index $q+1$. That is, the i th enqueue operation is that which inserts its value at index $q+i$. Similarly, let us enumerate the dequeue operations by their removal index. That is, the j th dequeue operation is that which removes the element at index $q+j$. When program e contains n_q enqueue operations on q and m_q dequeue operations on q then:

- the constructor of the queue at location q contains a trace with a single event c_q where $\text{lab}(c_q) = \text{alloc}(q, 0)$;
- for each i th enqueue operation $\text{enq}(q, v_i)$ with $v_i \neq \perp$, G contains a trace of the form $\theta_i^{e(q)} = e_i^1 \xrightarrow{\text{po}^{\text{limm}}} e_i^2$, where $\text{lab}(e_i^1) = \text{FAA}(\text{rel}, q, i-1, 1)$ and $\text{lab}(e_i^2) = \text{store}(\text{rel}, q+i, v_i)$;
- for each j th dequeue operation, G contains a trace of the form $\theta_j^d = \theta_j^f \xrightarrow{\text{po}} d_i^1 \xrightarrow{\text{po}} f_i^1 \xrightarrow{\text{po}} \dots \xrightarrow{\text{po}} f_i^{j-1} \xrightarrow{\text{po}^{\text{limm}}} d_i^2$, such that all events of θ_j^f are read events: $\forall a \in \theta_j^f. \text{lab}(a) = \text{load}(-, -, -)-$; $\text{lab}(d_i^1) = \text{load}(\text{acq}, q, \text{len}_j)$; $\text{lab}(f_k) = \text{load}(\text{acqrel}, q+k, 0)$ for all $k \in \{1 \dots j-1\}$; and $\text{lab}(d_i^2) = \text{AX}(\text{acqrel}, q+j, w_j, 0)$, for some $w_j \neq \perp$, $\text{range } \text{len}_j \in \mathbb{N}^+$ and $1 \leq j \leq \text{len}_j$.

Let us define: $\text{imp}(\cdot) : E'' \rightarrow E$ as follows:

$$\text{imp}(e) \triangleq \begin{cases} c_q & \exists q. f(c_q) = e \\ \theta_i^{e(q)}.e_i^2 & \exists i, q. f(\theta_i^{e(q)}.e_i^2) = e \\ \theta_i^{d(q)}.d_i^2 & \exists i, q. f(\theta_i^{d(q)}.d_i^2) = e \end{cases}$$

Let $\text{so}'' = \text{com}''$ with

$$\text{com}'' \triangleq \left\{ (e, d) \mid \exists i, q. f(\theta_i^{e(q)}.e_i^2) = e \wedge f(\theta_i^{d(q)}.d_i^2) = d \wedge (\theta_i^{e(q)}.e_i^2, \theta_i^{d(q)}.d_i^2) \in \text{com} \right\}$$

Let $\text{hb} = G.\text{hb} = (\text{po} \cup \text{so})^+$. Note that from the definition of lh' , hb and the construction of so' above we know the (6) below holds. As such, from the irreflexivity of hb , we also know (7) holds.

$$\forall a, b. (a, b) \in \text{lh}' \Leftrightarrow a \neq b \wedge (\text{imp}(a), \text{imp}(b)) \in \text{hb} \quad (6)$$

$$\forall a. (a, a) \notin \text{lh}' \quad (7)$$

Moreover, it is straightforward to demonstrate that given the C11 memory model and the values written, the (8) property below holds. Consequently, since the “range” value read by each dequeue operation is greater than or equal the slot acquired by its matching enqueueing thread, the rel mode of fetch-and-add operations in enqueue and the acq mode of “range” reads in dequeue operations, thanks to the release sequences of C11 the (9) property below holds.

$$\forall a, b \in \{1 \dots n\}. a < b \Rightarrow (e_a^1, e_b^1) \in \text{mo} \quad (8)$$

$$\forall \pi. (e_\pi^2, d_\pi^2) \in \text{com} \Rightarrow (e_\pi^1, d_\pi^1) \in \text{so} \quad (9)$$

Given the encapsulation of G and the definition of I , we know that for all $\pi \in \mathbb{N}$ and all queue locations $q, G.E \cap \{e \mid \text{loc}(e) = q + \pi\} = W_\pi$, where $W_\pi \triangleq E_e \cap (\{e_\pi^2\} \cup W_\pi^f \cup W_\pi^d)$, with $W_\pi^f \triangleq \{f_k^\pi \mid 1 \leq k \leq m\}$ and $W_\pi^d \triangleq \{d_\pi^2\}$.

Given the definition of the C11 library, we then know that for each $\pi \in \mathbb{N}^+$, the W_π is totally ordered by a strict total order **mo**. Consequently, given the release-acquire (acqrel) mode of update events in $W_\pi^f \cup W_\pi^d$, it is straightforward to demonstrate that:

$$\forall \pi \in \mathbb{N}. \forall w_1, w_2 \in W_\pi^f \cup W_\pi^d. ((w_1, w_2) \in (\text{so} \cap \text{mo}) \cup (\text{so}^{-1} \cap \text{mo}^{-1})) \vee (w_1, e_\pi^2), (e_\pi^2, w_2) \in \text{mo} \vee (w_2, e_\pi^2), (e_\pi^2, w_1) \in \text{mo} \quad (10)$$

We next demonstrate that:

$$\forall a, b. a < b \Rightarrow (e_b^2, e_a^2) \notin \text{hb} \quad (11)$$

$$\forall \pi, \pi' \in \mathbb{N}^+. d_\pi^2 \xrightarrow{\text{com}^{-1}} e_\pi^2 \xrightarrow{\text{hb}} e_{\pi'}^2 \xrightarrow{\text{com}} d_{\pi'}^2 \Rightarrow d_\pi^2 \xrightarrow{\text{hb}} d_{\pi'}^2, \quad (12)$$

For part (11) we proceed by contradiction. Let us assume there exists a, b such that $a < b$ and $(e_b^2, e_a^2) \in \text{hb}$. Since G is consistent and e_b^2, e_a^2 are write events, we know that they do not have any incoming **so** edges. As such, as $\text{hb} = (\text{po} \cup \text{so})^+$, we know that there exists e such that $e_b^2 \xrightarrow{\text{hb}} e \xrightarrow{\text{po}} e_a^2$. Moreover, since $e_a^1 \xrightarrow{\text{po}|_{\text{limm}}} e_a^2$, we also know that $e \xrightarrow{\text{po}} e_a^1$. That is, we have $(e_b^2, e_a^1) \in \text{hb}$. On the other hand, from (8) we know that $(e_a^1, e_b^1) \in \text{mo}$. We then have: $e_a^1 \xrightarrow{\text{mo}} e_b^1 \xrightarrow{\text{po}} e_b^2 \xrightarrow{\text{hb}} e_a^1$. That is, we have $e_a^1 \xrightarrow{\text{mo}} e_b^1 \xrightarrow{\text{hb}} e_a^1$, contradicting the assumption that G is consistent.

For part (12), as $e_\pi^2 \xrightarrow{\text{hb}} e_{\pi'}^2$, from (11) we know that $\pi < \pi'$. Moreover, as $(e_\pi^2, d_\pi^2) \in \text{com}$, from the consistency of the C library we know that $(e_\pi^2, d_\pi^2) \in \text{mo}|_{\text{limm}}$. As such, from (10) we know that either i) $(d_\pi^2, f_{\pi'}^2) \in \text{so}$; or ii) $(f_{\pi'}^2, e_\pi^2) \in \text{mo}$.

In case (i) we have $d_\pi^2 \xrightarrow{\text{so}} f_{\pi'}^2 \xrightarrow{\text{po}} d_{\pi'}^2$; i.e. $d_\pi^2 \xrightarrow{\text{hb}} d_{\pi'}^2$, as required.

In case (ii), Since G is consistent and $e_\pi^2, e_{\pi'}^2$ are write events, we know that they do not have any incoming **so** edges. As such, as $\text{hb} = (\text{po} \cup \text{so})^+$, we know that there exists e such that $e_\pi^2 \xrightarrow{\text{hb}} e \xrightarrow{\text{po}} e_{\pi'}^2$. Moreover, since $e_{\pi'}^1 \xrightarrow{\text{po}|_{\text{limm}}} e_{\pi'}^2$, we also know that $e \xrightarrow{\text{po}} e_{\pi'}^1$. That is, we have $(e_\pi^2, e_{\pi'}^1) \in \text{hb}$. Moreover, from (9) we have $(e_{\pi'}^1, d_{\pi'}^1) \in \text{so} \subseteq \text{hb}$. As such, from the assumption of the case we have $e_\pi^2 \xrightarrow{\text{hb}} e_{\pi'}^1 \xrightarrow{\text{hb}} d_{\pi'}^1 \xrightarrow{\text{po}} f_{\pi'}^2 \xrightarrow{\text{mo}} e_\pi^2$. That is, we have $e_\pi^2 \xrightarrow{\text{hb}} f_{\pi'}^2 \xrightarrow{\text{mo}} e_\pi^2$, contradicting the assumption that G is consistent.

□

To show that $G' = (E'', \text{po}'', \text{com}'', \text{so}'', \text{hb}'') \in L^{\text{SQ}} \mathcal{G}_c$, we are then required to show that for all locations q , G'_q is queue consistent on q . Pick an arbitrary location q and let $G'_q = \langle E', \text{po}', \text{com}', \text{so}', \text{hb}' \rangle$. We then need to show:

- 1) $E'^c = \emptyset \vee \exists c \in C^q. E'^c = \{c\}$;
- 2) $\text{com}' \subseteq \bigcup_{v \in \text{Val} \setminus \{\perp\}} \mathcal{E}^{q,v} \times \mathcal{D}^{q,v}$
- 3) $\text{com}', (\text{com}')^{-1}$ are functional
- 4) $E' \cap \mathcal{D}^q \setminus \text{rng}(\text{com}') \subseteq \mathcal{D}^{q,\perp}$
- 5) $[\mathcal{E}^q \setminus \text{dom}(\text{com}')]; \text{hb}'; [\mathcal{D}^{q,\perp}] = \emptyset$
- 6) $\text{so}' = \text{com}'$
- 7) there exists a sequential enumeration S of the events in E' such that: (i) S respects hb' ; and (ii) $\text{fifo}(\epsilon, S)$ holds.

Parts (1), (2) and (6) follow simply from the construction of G' . Part (5) holds trivially as $\mathcal{D}^{q,\perp} = \emptyset$.

For part (4), we demonstrate that $E' \cap \mathcal{D}^q \setminus \text{rng}(\text{com}') = \emptyset$. We proceed by contradiction. Let us assume there exists $d \in E' \cap \mathcal{D}^q$ such that $d \notin \text{rng}(\text{com}')$. From the construction of G' we then know

there exists $\theta_j^{d(q)}$ and d_j^2 such that $d_j^2 \in \theta_j^{d(q)}$ and $\text{lab}(d_j^2) = \text{AX}(\text{acq}, q+s_j, w_j, 0)$ for some s_j, w_j such that $s_j > 0$ and $w_j \neq \perp$. From the definition of consistency for the C library we know there exists a write event a such that $(a, d_j^2) \in \text{com}$. Given the assumption of the case (A), and the shape of the θ^e and θ^d traces, we then know that there exists $\theta_i^{e(q)}$ and e_i^2 such that $e_i^2 \in \theta_i^{e(q)}$; $a = e_i^2$; and $\text{lab}(e_i^2) = \text{store}(\text{rel}, q+s_j, w_j)$. As such, from the construction of G' we know there exists $e \in \mathcal{E}^{q, w_j}$ such that $(e, d) \in \text{com}'$. This however contradicts our assumption that $d \notin \text{rng}(\text{com}')$.

For part (3) we proceed by contradiction. Let us assume that $(\text{com}')^{-1}$ is not functional and there exist $e'_1, e'_2 \in \mathcal{E}^q$ and $d' \in \mathcal{D}^q$ such that $e'_1 \neq e'_2$ and $(e'_1, d'), (e'_2, d') \in \text{com}'$. From the construction of G' we then know there exist $\theta_i^{e(q)}, \theta_k^{e(q)}, \theta_j^{d(q)}$ such that $\theta_i^{e(q)} \neq \theta_k^{e(q)}$ $(e_i^2, d_j^2), (e_k^2, d_j^2) \in \text{com}$. That is, $(e_i^2, d_j^2), (e_k^2, d_j^2) \in \text{com}_{LC}$. This however contradicts the assumption that G is consistent with respect to the C library, i.e. the assumption com_{LC}^{-1} is functional.

Let us next assume that com' is not functional and there exist $e' \in \mathcal{E}^q$ and $d'_1, d'_2 \in \mathcal{D}^q$ such that $d'_1 \neq d'_2$ and $(e', d'_1), (e', d'_2) \in \text{com}'_{\text{LSQ}}$. From the construction of G' we then know there exist $\theta_j^{d(q)}, \theta_k^{d(q)}, \theta_i^{e(q)}$ such that $\theta_j^{d(q)} \neq \theta_k^{d(q)}$ $(e_i^2, d_j^2), (e_i^2, d_k^2) \in \text{com}$. That is $(e_i^2, d_j^2), (e_i^2, d_k^2) \in \text{com}_{LC}$. As d_j^2, d_k^2 are atomic update events, from the definition of the C library and the consistency of G we know they are ordered by a total modification order mo . Without loss of generality, let us assume that $(d_j^2, d_k^2) \in \text{mo}$. From the definition of the C library and the consistency of G , we then also have $(e_i^2, d_j^2) \in \text{mo}$. As such, we have $(d_k^2, d_j^2) \in \text{com}^{-1}; \text{mo}$. Consequently, we have $d_k^2 \xrightarrow{\text{com}^{-1}; \text{mo}} d_j^2 \xrightarrow{\text{mo}} d_k^2$, contradicting the assumption that G is consistent.

For part (7), in what follows we demonstrate that for all $n \in \mathbb{N}^+$, the irreflexive($C^{n,n}$) holds for G' . The desired property of (7) then follows immediately from [Thm. 7](#).

To demonstrate that $\forall n \in \mathbb{N}^+.$ irreflexive($C^{n,n}$) holds, we proceed by induction on n .

Base case $n = 1$

We proceed by contradiction. Let us assume there exist d_1, d_2, e_1, e_2 such that $d_1 \xrightarrow{\text{com}'^{-1}} e_1 \xrightarrow{\text{hb}'} e_2 \xrightarrow{\text{com}'} d_2 \xrightarrow{\text{hb}'} d_1$. From the definition of com' , the definition of $\text{imp}(\cdot)$ and (6) we then have $\text{imp}(d_1) \xrightarrow{\text{com}^{-1}} \text{imp}(e_1) \xrightarrow{\text{hb}} \text{imp}(e_2) \xrightarrow{\text{com}} \text{imp}(d_2) \xrightarrow{\text{hb}} \text{imp}(d_1)$. From (12) we then have $\text{imp}(d_1) \xrightarrow{\text{hb}} \text{imp}(d_2) \xrightarrow{\text{hb}} \text{imp}(d_1)$. As such, from the transitivity of hb we have $\text{imp}(d_1) \xrightarrow{\text{hb}} \text{imp}(d_1)$, contradicting the assumption that G is consistent.

Inductive case $n = m+1$

$$\forall k \in \mathbb{N}^+. k \leq m \Rightarrow \text{irreflexive}(C^{k,k}) \quad (\text{I.H.})$$

We proceed by contradiction. Let us assume that there exist a $C^{n,n}$ cycle. As $n > 0$, we know there is at least one adjacent pair of $\text{com}'^{-1}; \text{hb}'$ and $\text{com}'; \text{hb}'$ edges. That is, there exist a, b such that and $a \xrightarrow{\text{com}'^{-1}; \text{hb}'; \text{com}'; \text{hb}'} b \xrightarrow{C^{m,m}} a$. As such, from (6), (12), the transitivity of hb and the functionality of $\text{imp}(\cdot)$ we have $\text{imp}(a) \xrightarrow{\text{hb}} \text{imp}(b)$ and $a \neq b$. Consequently, from (6) we have $(a, b) \in \text{hb}'$. Moreover, since by definition the $C^{m,m}$ edge ends with hb' and $a \xrightarrow{\text{hb}'} b$, from the transitivity of hb' and since $b \xrightarrow{C^{m,m}} a$ we have $b \xrightarrow{C^{m,m}} b$. This however contradicts our assumption in (I.H.).

H.2 Soundness of the Weak Herlihy-Wing Queue Implementation

Let I denote the weak Herlihy-Wing implementation in Fig. 2. To show the soundness of I , we appeal to Thm. 1 and show that I is locally sound on L^Q .

Pick an arbitrary $\Lambda, f, G = \langle E, \text{po}, \text{com}, \text{so} \rangle, E'', \text{po}'', \text{com}'', \text{so}''$ such that G is Λ -consistent and Λ -well-formed and $\text{abs}_{L^Q, I}(f, \langle E, \text{po} \rangle, \langle E'', \text{po}'' \rangle)$.

We must next find $\text{com}'', \text{so}''$ such that $\langle E'', \text{po}'', \text{com}'', \text{so}'', \text{lbh}'' \rangle \in L^{\text{SQ}}.\mathcal{G}_c$, where lbh'' is the same as lbh' in Def. 11.

In the remainder of this proof, we assume all identically-named definitions (e.g. $G', \text{com}', \text{so}'$ and $\text{imp}(\cdot)$ and so forth) are as defined for the strong Herlihy-Wing queue implementation unless otherwise stated.

Note that due to the encapsulation of G and the definition of I , we know that for all $\pi \in \mathbb{N}$:

$$G.E \cap \{e \mid \text{loc}(e) = q + \pi\} = W_\pi$$

As such, given the consistency of the C library L^C , we then know that for each $\pi \in \mathbb{N}^+$, the W_π is totally ordered by a strict total order mo .

Observe that the (6), (7) (8), (9) and (11) properties also hold for the weak implementation.

We next demonstrate that:

$$\forall \pi, \pi' \in \mathbb{N}^+. d_\pi^2 \xrightarrow{\text{com}^{-1}} e_\pi^2 \xrightarrow{\text{hb}} e_{\pi'}^2 \xrightarrow{\text{com}} d_{\pi'}^2 \Rightarrow (d_{\pi'}^2, d_\pi^2) \notin \text{hb} \quad (13)$$

We proceed by contradiction. Let us assume there exist π, π' such that $(e_\pi^2, d_\pi^2) \in \text{com}$, $(e_{\pi'}^2, e_\pi^2) \in \text{hb}$, $(e_{\pi'}^2, d_\pi^2) \in \text{com}$ and $(d_{\pi'}^2, d_\pi^2) \in \text{hb}$. As $e_\pi^2 \xrightarrow{\text{hb}} e_{\pi'}^2$, from (11) we know that $\pi < \pi'$. Moreover, as $(e_{\pi'}^2, d_\pi^2) \in \text{com}$, from the consistency of the C library we know that $(e_\pi^2, d_\pi^2) \in \text{mo}_{\text{limm}}$. As such, since the writes in W_π are totally ordered by mo (see above), we know that either i) $(d_\pi^2, f_{\pi'}^\pi) \in \text{mo}$; or ii) $(f_{\pi'}^\pi, e_\pi^2) \in \text{mo}$.

In case (i) we then have $d_\pi^2 \xrightarrow{\text{mo}} f_{\pi'}^\pi \xrightarrow{\text{po}} d_{\pi'}^2 \xrightarrow{\text{hb}} d_\pi^2$. That is, we have $d_\pi^2 \xrightarrow{\text{mo}} f_{\pi'}^\pi \xrightarrow{\text{hb}} d_{\pi'}^2$, contradicting the assumption that G is consistent.

In case (ii), since G is consistent and $e_\pi^2, e_{\pi'}^2$ are write events, we know that they do not have any incoming so edges. As such, as $\text{hb} = (\text{po} \cup \text{so})^+$, we know that there exists e such that $e_\pi^2 \xrightarrow{\text{hb}} e \xrightarrow{\text{po}} e_{\pi'}^2$. Moreover, since $e_{\pi'}^1 \xrightarrow{\text{po}_{\text{limm}}} e_{\pi'}^2$, we also know that $e \xrightarrow{\text{po}} e_{\pi'}^1$. That is, we have $(e_\pi^2, e_{\pi'}^1) \in \text{hb}$. Moreover, from (9) we have $(e_{\pi'}^1, d_{\pi'}^1) \in \text{so} \subseteq \text{hb}$. As such, from the assumption of the case we have $e_\pi^2 \xrightarrow{\text{hb}} e_{\pi'}^1 \xrightarrow{\text{hb}} d_{\pi'}^1 \xrightarrow{\text{po}} f_{\pi'}^\pi \xrightarrow{\text{mo}} e_\pi^2$. That is, we have $e_\pi^2 \xrightarrow{\text{hb}} f_{\pi'}^\pi \xrightarrow{\text{mo}} e_\pi^2$, contradicting the assumption that G is consistent.

□

To show that $G' = (E'', \text{po}'', \text{com}'', \text{so}'', \text{lbh}'') \in L^{\text{SQ}}.\mathcal{G}_c$, we are then required to show that for all locations q , G'_q is queue consistent on q . Pick an arbitrary location q and let $G'_q = \langle E', \text{po}', \text{com}', \text{so}', \text{lbh}' \rangle$. We then need to show:

- 1) $\text{com}' \subseteq \bigcup_{v \in \text{Val} \setminus \{\perp\}} \mathcal{E}^{q, v} \times \mathcal{D}^{q, v}$
- 2) $\text{com}', (\text{com}')^{-1}$ are functional
- 3) $E' \cap \mathcal{D}^q \setminus \text{rng}(\text{com}') \subseteq \mathcal{D}^{q, \perp}$
- 4) $[\mathcal{E}^q \setminus \text{dom}(\text{com}')]; \text{lbh}'; [\mathcal{D}^{q, \perp}] = \emptyset$
- 5) $\text{so}' = \text{com}'$
- 6) $\text{com}^{-1}; \text{lbh}; \text{com}; \text{lbh}$ is irreflexive.

Proof of parts (1-5) are as in the case of the strong implementation. For part (6) we proceed by contradiction. Let us assume there exist d_1, d_2, e_1, e_2 such that $d_1 \xrightarrow{\text{com}'^{-1}} e_1 \xrightarrow{\text{hb}'} e_2 \xrightarrow{\text{com}'} d_2 \xrightarrow{\text{hb}'} d_1$. From the definition of com' , the definition of $\text{imp}(\cdot)$ and (6) we then have $\text{imp}(d_1) \xrightarrow{\text{com}^{-1}} \text{imp}(e_1) \xrightarrow{\text{hb}} \text{imp}(e_2) \xrightarrow{\text{com}} \text{imp}(d_2) \xrightarrow{\text{hb}} \text{imp}(d_1)$. From (13) we then have $\text{imp}(d_1) \xrightarrow{\text{hb}} \text{imp}(d_2) \xrightarrow{\text{hb}} \text{imp}(d_1)$. As such, from the transitivity of hb we have $\text{imp}(d_1) \xrightarrow{\text{hb}} \text{imp}(d_1)$, contradicting the assumption that G is consistent.

I THE SOUNDNESS OF THE WEAK STACK IMPLEMENTATION

Let I denote the weak stack implementation in Fig. 6. To show the soundness of I , we appeal to Thm. 1 and show that I is locally sound on L^{WS} .

Pick an arbitrary $\Lambda, f, G = \langle E, \text{po}, \text{com}, \text{so} \rangle, E'', \text{po}''$ such that G is Λ -consistent and Λ -well-formed and $\text{abs}_{L^{WS}, I}(f, \langle E, \text{po} \rangle, \langle E'', \text{po}'' \rangle)$.

We must next find $\text{com}'', \text{so}''$ such that $\langle E'', \text{po}'', \text{com}'', \text{so}'', \text{lbh}'' \rangle \in L^{WS}.\mathcal{G}_c$, where lbh'' is the same as lbh' in Def. 11.

Let us assume without loss of generality that for each location s , the G contains n_s push operations and m_s pop operations. We will shortly enumerate these operations in order of their lock acquisition. Note that for each $i \in \{1 \dots n_s\}$, the i^{th} push operation $\text{try-push}(s, v_i)$ either:

- pushes v_i on the stack at s and thus G contains the events in the trace: $\theta_i^{as(s)} = l \xrightarrow{\text{po}_{\text{limm}}} r_t \xrightarrow{\text{po}_{\text{limm}}} a \xrightarrow{\text{po}_{\text{limm}}} w_t \xrightarrow{\text{po}_{\text{limm}}} u$, where $\text{lab}(l) = \text{CAS}(\text{acqrel}, s, 0, 1)$, $\text{lab}(r_t) = \text{load}(\text{rlx}, s+1, t)$ for some top value t , $\text{lab}(a) = \text{store}(\text{rlx}, s+t+1, v_i)$, $\text{lab}(w_t) = \text{store}(\text{rlx}, s+1, t+1)$, and $\text{lab}(u) = \text{store}(\text{rel}, s, 0)$; or
- fails to push v_i on the stack as it fails to acquire the lock at s , and thus G contains the single-event trace: $\theta_i^{af(s)} = f$, where $\text{lab}(f) = \text{load}(\text{acq}, s, 1)$.

Similarly, for each $i \in \{1 \dots m_s\}$, the i^{th} pop operation $\text{try-pop}(s)$ either:

- pops w_i from the stack at s and thus G contains the events in the trace: $\theta_i^{as(s)} = l \xrightarrow{\text{po}_{\text{limm}}} r_t \xrightarrow{\text{po}_{\text{limm}}} p \xrightarrow{\text{po}_{\text{limm}}} w_t \xrightarrow{\text{po}_{\text{limm}}} u$, where $\text{lab}(l) = \text{CAS}(\text{acqrel}, s, 0, 1)$, $\text{lab}(r_t) = \text{load}(\text{rlx}, s+1, t)$ for some top value t , $\text{lab}(p) = \text{load}(\text{rlx}, s+t, v_i)$, $\text{lab}(w_t) = \text{store}(\text{rlx}, s+1, t-1)$, and $\text{lab}(u) = \text{store}(\text{rel}, s, 0)$; or
- fails to pop from the stack as it fails to acquire the lock at s and thus G contains the single-event trace: $\theta_i^{rf1(s)} = f$, where $\text{lab}(f) = \text{load}(\text{acq}, s, 1)$.
- fails to pop from the stack array as it is empty, and thus G contains the events in the trace: $\theta_i^{rf2(s)} = l \xrightarrow{\text{po}_{\text{limm}}} f \xrightarrow{\text{po}_{\text{limm}}} u$, where $\text{lab}(l) = \text{CAS}(\text{acqrel}, s, 0, 1)$, $\text{lab}(f) = \text{load}(\text{rlx}, s+1, 1)$, and $\text{lab}(u) = \text{store}(\text{rel}, s, 0)$.

Moreover, the constructor of the stack at location s contains a trace of the form $\theta^{c(s)} = c_s \xrightarrow{\text{po}_{\text{limm}}} c_l \xrightarrow{\text{po}_{\text{limm}}} c_t$ where $\text{lab}(c_s) = \text{alloc}(s, 0)$, $\text{lab}(c_l) = \text{store}(\text{rel}, s, 0)$, and $\text{lab}(c_t) = \text{store}(\text{rel}, s+1, 1)$.

Let us define $\text{imp}(\cdot) : E'' \rightarrow E$ as:

$$\text{imp}(e) \triangleq \begin{cases} \theta^{c(s)}.c_t & \exists s. \theta^{c(s)}.c_t = e \\ \theta_i^{as(s)}.a & \exists i, s. f(\theta_i^{as(s)}.a) = e \\ \theta_i^{af(s)}.f & \exists i, s. f(\theta_i^{af(s)}.f) = e \\ \theta_i^{rs(s)}.r & \exists i, s. f(\theta_i^{rs(s)}.r) = e \\ \theta_i^{rf1(s)}.f & \exists i, s. f(\theta_i^{rf1(s)}.f) = e \\ \theta_i^{rf2(s)}.f & \exists i, s. f(\theta_i^{rf2(s)}.f) = e \end{cases}$$

Let $\text{so}'' = \text{com}''$ with

$$\text{com}'' \triangleq \left\{ (a, r) \mid \exists i, j, s. f(\theta_i^{as(s)}.a) = af(\theta_j^{rs(s)}.r) = r \wedge (\theta_i^{as(s)}.a, \theta_j^{rs(s)}.r) \in \text{com} \right\}$$

It is straightforward to demonstrate that given the `acqrel` mode of the lock acquisitions, the `rel` mode of lock releases, and the specification of the C library we have:

$$\begin{aligned} \forall s. \forall i, j \in \{1 \dots n+m\}. ((\theta_i^{as(s)}.u, \theta_j^{rs(s)}.l) \in \mathbf{hb} \vee (\theta_j^{rs(s)}.u, \theta_i^{as(s)}.l) \in \mathbf{hb}) \\ \wedge \forall s. \forall i, j \in \{1 \dots n+m\}. (\theta_i^{as(s)}.u, \theta_j^{as(s)}.l) \in \mathbf{hb} \vee (\theta_j^{as(s)}.u, \theta_i^{as(s)}.l) \in \mathbf{hb} \\ \wedge \forall i, j \in \{1 \dots n+m\}. ((\theta_i^{rs(s)}.u, \theta_j^{rs(s)}.l) \in \mathbf{hb} \vee (\theta_j^{rs(s)}.u, \theta_i^{rs(s)}.l) \in \mathbf{hb}) \end{aligned}$$

For each location s , let us then enumerate the *successful* push and pop operations (i.e. those with a $\theta^{as(s)}$ or $\theta^{rs(s)}$ trace) in order of their lock acquisition. That is, the *first* operation is either i) a successful push operation associated with trace $\theta_1^{as(s)}$ such that for all $i \neq 1$, $\theta_1^{as(s)}.u \xrightarrow{\mathbf{hb}} \theta_i^{as(s)}.l$ and $\theta_1^{as(s)}.u \xrightarrow{\mathbf{hb}} \theta_i^{rs(s)}.l$; or ii) a successful pop operation associated with trace $\theta_1^{rs(s)}$ such that for all $i \neq 1$, $\theta_1^{rs(s)}.u \xrightarrow{\mathbf{hb}} \theta_i^{as(s)}.l$ and $\theta_1^{rs(s)}.u \xrightarrow{\mathbf{hb}} \theta_i^{rs(s)}.l$.

Let us write $\text{isPush}(s, i)$ when the i^{th} operation on s (as ordered above) is a successful push operation with trace $\theta_i^{as(s)}$. Similarly, let us write $\text{isPop}(s, i)$ when the i^{th} operation on s (as ordered above) is a successful pop operation with trace $\theta_i^{rs(s)}$. We can then demonstrate that:

$$\begin{aligned} \forall s. \forall i, j \in \{1 \dots n_s + m_s\}. i < j \Rightarrow \\ \text{isPush}(s, i) \wedge \text{isPop}(s, j) \wedge (\theta_i^{as(s)}.u, \theta_j^{rs(s)}.l) \in \mathbf{hb} \\ \vee \text{isPush}(s, i) \wedge \text{isPush}(s, j) \wedge (\theta_i^{as(s)}.u, \theta_j^{as(s)}.l) \in \mathbf{hb} \\ \vee \text{isPop}(s, i) \wedge \text{isPop}(s, j) \wedge (\theta_i^{rs(s)}.u, \theta_j^{rs(s)}.l) \in \mathbf{hb} \end{aligned} \quad (14)$$

Let us write $\text{match}(s, i, j)$ when $\text{isPush}(s, i) \wedge \text{isPop}(s, j) \wedge (\theta_i^{as(s)}.a, \theta_j^{rs(s)}.r) \in \mathbf{com}$. Let us write $\text{top}(s, i)$ for t when either: 1) $\text{isPush}(s, i)$ and $\text{lab}(\theta_i^{as(s)}.w_t) = \text{store}(\text{rlx}, s+1, t)$; or 2) $\text{isPop}(s, i)$ and $\text{lab}(\theta_i^{rs(s)}.r_t) = \text{load}(\text{rlx}, s+1, t)$. It is then straightforward to demonstrate that for all i, j :

$$\begin{aligned} \text{isPush}(s, i) \wedge \text{isPush}(s, i+1) \Rightarrow \text{top}(s, i+1) = \text{top}(s, i)+1 \\ \text{isPush}(s, i) \wedge \text{isPop}(s, i+1) \Rightarrow \text{match}(s, i, i+1) \\ \text{match}(s, i, j) \Rightarrow \text{top}(s, j) = \text{top}(s, i) \end{aligned} \quad (15)$$

It is then straightforward to demonstrate by induction that for all i, j :

$$\begin{aligned} i < j \wedge \text{isPush}(s, i) \wedge \text{isPush}(s, j) \Rightarrow \text{top}(s, i) < \text{top}(s, j) \vee \exists k. i < k < j \wedge \text{isPop}(s, k) \wedge \text{match}(s, i, k) \\ i < j \wedge \text{isPush}(s, i) \wedge \text{isPop}(s, j) \Rightarrow \text{top}(s, i) < \text{top}(s, j) \vee \exists k. i < k \leq j \wedge \text{isPop}(s, k) \wedge \text{match}(s, i, k) \end{aligned} \quad (16)$$

Let $\mathbf{hb} = G.\mathbf{hb} = (\text{po} \cup \mathbf{so})^+$. Note that from the definition of \mathbf{hb}' , \mathbf{hb} and the construction of \mathbf{so}' above we know the (17) below holds. As such, from the irreflexivity of \mathbf{hb} , we also know (18) holds.

$$\forall a, b. (a, b) \in \mathbf{hb}' \Rightarrow (\text{im}(a), \text{im}(b)) \in \mathbf{hb} \quad (17)$$

$$\forall a. (a, a) \notin \mathbf{hb}' \quad (18)$$

To show that $G' = (E'', \text{po}'', \mathbf{com}'', \mathbf{so}'', \mathbf{hb}'') \in L^{\text{WS}}.\mathcal{G}_c$, we are then required to show that for all locations s , G'_s is weak-stack consistent on s . Pick an arbitrary location s and let $G'_s = \langle E', \text{po}', \mathbf{com}', \mathbf{so}', \mathbf{hb}' \rangle$. We then need to show:

- 1) $E'^c = \emptyset \vee \exists c \in C^q. E'^c = \{c\}$;
- 2) $\mathbf{com}' \subseteq \bigcup_{v \in \text{Val} \setminus \{\perp\}} \mathcal{A}^{s, v, \top} \times \mathcal{R}^{s, v, \top}$;
- 3) $\mathbf{com}', \mathbf{com}'^{-1}$ are functional;
- 4) $E' \cap \mathcal{R}^s \setminus \text{rng}(\mathbf{com}') \subseteq \mathcal{R}^{s, \perp, \perp}$

5) $\text{so}' = \text{com}'$; and

6) $\forall a_1, a_2, r_1, r_2. (a_1, r_1), (a_2, r_2) \in \text{com}' \wedge (a_1, a_2), (r_1, r_2) \in \text{lbh}' \Rightarrow (a_2, r_1) \notin \text{lbh}'$.

Parts (1), (2) and (5) follow immediately from the construction of G' . Part (3) follows from the definition of com' , the consistency of G and the definition of the C library and the fact that com^{-1} is functional for L^C .

For part (4), we proceed by contradiction. Let us assume there exists $r \in E' \cap \mathcal{R}^s$ such that $r \notin \text{rng}(\text{com}')$ and $r \notin \mathcal{R}^{s, \perp, \perp}$. Let $\text{lab}(r) = \text{try-pop}(s, v, -)$. From the construction of G' we then know that there exists i and r' such that $r' = \text{imp}(r)$ and either: i) $r' = \theta_i^{rs(s)}.r$ and $r = r_i^{rs(s)}$; or ii) $r' = \theta_i^{rf1(s)}.f$ and $r = r_i^{rf1(s)}$; or iii) $r' = \theta_i^{rf2(s)}.f$ and $r = r_i^{rf2(s)}$. In cases (ii) and (iii) from the construction of G' we then know $\text{lab}(r) = \text{try-pop}(s, \perp, \perp)$ and thus $r \in \mathcal{R}^{s, \perp, \perp}$, contradicting the assumption that $r \notin \mathcal{R}^{s, \perp, \perp}$. In case (i), from the shape of the $\theta_i^{rs(s)}$ we know there exists t such that $\text{lab}(\theta_i^{rs(s)}.r_t) = \text{load}(\text{rlx}, s+1, t)$, $\text{lab}(r') = \text{load}(\text{rlx}, s+t, v)$. Moreover, from the consistency of the C library we know there exists w such that $(w, r') \in \text{com}$ and $\text{lab}(w) = \text{store}(-, s+t, v)$. As such, from the well-formedness of G and the shape of the implementation traces we know that there exists j such that $w = \theta_j^{as(s)}.a$. Consequently, since $(w, r') \in \text{com}$, from the construction of G' we know that $(a_j^{as(s)}, r_i^{rs(s)}) \in \text{com}'$. Since $r = r_i^{rs(s)}$, this contradicts our assumption that $r \notin \text{rng}(\text{com}')$.

For part (6), we proceed by contradiction. Let us assume there exist a_1, a_2, r_1, r_2 such that $(a_1, r_1), (a_2, r_2) \in \text{com}'$, $(a_1, a_2), (r_1, r_2) \in \text{lbh}'$ and $(a_2, r_1) \in \text{lbh}'$.

From the construction of G' we then know there exist i, j, k, l such that $\text{imp}(a_1) = \theta_i^{as(s)}.a$, $\text{imp}(r_1) = \theta_j^{rs(s)}.r$, $\text{imp}(a_2) = \theta_k^{as(s)}.a$, $\text{imp}(r_2) = \theta_l^{rs(s)}.r$, and $(\text{imp}(a_1), \text{imp}(r_1)), (\text{imp}(a_2), \text{imp}(r_2)) \in \text{com}$. That is we have $\text{match}(s, i, j)$ and $\text{match}(s, j, k)$.

From (19) we then know $(\text{imp}(a_1), \text{imp}(a_2)), (\text{imp}(r_1), \text{imp}(r_2)), (\text{imp}(a_2), \text{imp}(r_1)) \in \text{hb}$. From (14) we then have $i < k$, $k < j$ and $j < l$, since otherwise we would get an hb cycle contradicting the assumption that G is consistent. As such, since we also have $\text{match}(s, i, j)$ and $\text{match}(s, k, l)$, and $\text{match}(s, ., .)$ is uniquely determined (due to the functionality of com and com^{-1}), from (16) we have $\text{top}(s, i) < \text{top}(s, k)$ and $\text{top}(s, k) < \text{top}(s, j)$. That is, we have $\text{top}(s, i) < \text{top}(s, j)$. On the other hand, since $\text{match}(s, i, j)$ holds, from (15) we have $\text{top}(s, i) = \text{top}(s, j)$. This however leads to a contradiction as we both have $\text{top}(s, i) < \text{top}(s, j)$ and $\text{top}(s, i) = \text{top}(s, j)$.

J THE SOUNDNESS OF THE ELIMINATION STACK IMPLEMENTATION

Let I denote the elimination stack implementation in Fig. 6. To show the soundness of I , we appeal to Thm. 1 and show that I is locally sound on L^S .

Pick an arbitrary $\Lambda, f, G = \langle E, \text{po}, \text{com}, \text{so} \rangle, E'', \text{po}''$ such that G is Λ -consistent and Λ -well-formed and $\text{abs}_{L^S, I}(f, \langle E, \text{po} \rangle, \langle E'', \text{po}'' \rangle)$.

We must next find $\text{com}'', \text{so}''$ such that $\langle E'', \text{po}'', \text{com}'', \text{so}'', \text{lhb}'' \rangle \in L^S \mathcal{G}_c$, where lhb'' is the same as lhb' in Def. 11.

Note that the constructor of the stack at s contains a trace of the following form in G : $\theta^{c(s)} = c_s \xrightarrow{\text{po}} c_{ws} \xrightarrow{\text{po}} c_{ea} \xrightarrow{\text{po}} c_{ea}^0 \xrightarrow{\text{po}} \dots \xrightarrow{\text{po}} c_{ea}^{k-1} \xrightarrow{\text{po}} c_{ws}^w \xrightarrow{\text{po}} c_{ea}^w$ where $\text{lab}(c_s) = \text{alloc}(s, 0)$, $\text{lab}(c_{ws}) = \text{alloc}(ws, 0)$, $\text{lab}(c_{ea}) = \text{alloc}(ea, 0)$, $\text{lab}(c_{ws}^w) = \text{store}(\text{rlx}, s, ws)$, $\text{lab}(c_{ea}^w) = \text{store}(\text{rlx}, s+1, ea)$, and for each $j \in \{0 \dots k-1\}$, c_{ea}^j is of the form $n_j \xrightarrow{\text{po|limm}} w_j$, where $\text{lab}(n_j) = \text{new-exchanger}(x_j)$ for some x_j , and $\text{lab}(w_j) = \text{store}(\text{rlx}, ea+j, x_j)$.

When G contains n_s push operations on the stack at s and m_s pop operations, let us enumerate them arbitrarily. Note that for each $i \in \{1 \dots n_s\}$, the i^{th} push operation $\text{push}(s, v_i)$ either:

- pushes v_i on the weak stack at s and thus G contains the events in the trace: $\theta_i^{as(s)} = r_s \xrightarrow{\text{po|limm}} r_e \xrightarrow{\text{po|limm}} f^* \xrightarrow{\text{po|limm}} a$, where $\text{lab}(r_s) = \text{load}(\text{rlx}, s, ws)$, $\text{lab}(r_e) = \text{load}(\text{rlx}, s+1, ea)$, f^* denotes the loop iterations that fail to push v_i , $\text{lab}(a) = \text{try-push}(ws, v_i, 1)$; or
- fails to push v_i on the weak stack and thus pushes it on the elimination array at $s+1$; as such G contains the events in the trace: $\theta_i^{ae(s)} = r_s \xrightarrow{\text{po|limm}} r_e \xrightarrow{\text{po|limm}} f^* \xrightarrow{\text{po|limm}} a_f \xrightarrow{\text{po}} a$, where $\text{lab}(r_s) = \text{load}(\text{rlx}, s, ws)$, $\text{lab}(r_e) = \text{load}(\text{rlx}, s+1, ea)$, f^* denotes the loop iterations that fail to push v , $\text{lab}(a_f) = \text{try-push}(ws, v_i, 0)$, $\text{lab}(a) = \text{exchange}(ea[j], v_i, \text{POP})$ for some $j \in \{0 \dots k-1\}$.

Similarly, for each $i \in \{1 \dots m_s\}$, the i^{th} pop operation $\text{pop}(s)$ either:

- pops w_i from the weak stack and thus G contains the events in the trace: $\theta_i^{rs} = r_s \xrightarrow{\text{po|limm}} r_e \xrightarrow{\text{po|limm}} f^* \xrightarrow{\text{po|limm}} r$, where $\text{lab}(r_s) = \text{load}(\text{rlx}, s, ws)$, $\text{lab}(r_e) = \text{load}(\text{rlx}, s+1, ea)$, f^* denotes loop iterations that fail to pop, and $\text{lab}(r) = \text{try-pop}(ws, w_i, 1)$; or
- fails to pop from the weak stack and thus pops w_i from the elimination array at $s+1$; as such, G contains the events in the trace: $\theta_i^{re} = r_s \xrightarrow{\text{po|limm}} r_e \xrightarrow{\text{po|limm}} f^* \xrightarrow{\text{po|limm}} r_f \xrightarrow{\text{po}} r$, where $\text{lab}(r_s) = \text{load}(\text{rlx}, s, ws)$, $\text{lab}(r_e) = \text{load}(\text{rlx}, s+1, ea)$, f^* denotes the iterations that fail to pop, $\text{lab}(r_f) = \text{try-pop}(ws, \perp, 0)$, and $\text{lab}(r) = \text{exchange}(ea[j], \text{POP}, w_i)$, for some $j \in \{0 \dots k-1\}$.

Let us define $\text{imp}(\cdot) : E'' \rightarrow E$ as:

$$\text{imp}(e) \triangleq \begin{cases} \theta^{c(s)}.c_{ea}^w & \exists s. \theta^{c(s)}.c_{ea}^w = e \\ \theta_i^{as(s)}.a & \exists i, s. f(\theta_i^{as(s)}.a) = e \\ \theta_i^{ae(s)}.a & \exists i, s. f(\theta_i^{ae(s)}.a) = e \\ \theta_i^{rs(s)}.r & \exists i, s. f(\theta_i^{rs(s)}.r) = e \\ \theta_i^{re(s)}.r & \exists i, s. f(\theta_i^{re(s)}.r) = e \end{cases}$$

Let $\text{so}'' = \text{com}''$ with

$$\text{com}'' \triangleq \left\{ (a, r) \mid \exists i, j, s. f(\theta_i^{as(s)}.a) = af(\theta_j^{rs(s)}.r) = r \wedge (\theta_i^{as(s)}.a, \theta_j^{rs(s)}.r) \in \text{com} \right\} \\ \cup \left\{ (a, r) \mid \exists i, j, s. f(\theta_i^{ae(s)}.a) = af(\theta_j^{re(s)}.r) = r \wedge (\theta_i^{ae(s)}.a, \theta_j^{re(s)}.r) \in \text{com} \right\}$$

Let $\mathbf{hb} = G.\mathbf{hb} = (\mathbf{po} \cup \mathbf{so})^+$. Note that from the definition of \mathbf{lbh}'' , \mathbf{hb} and the construction of \mathbf{so}' above we know the (19) below holds. As such, from the irreflexivity of \mathbf{hb} , we also know (20) holds. Let $\mathbf{hb}' = (\mathbf{po}' \cup \mathbf{so}')^+$. We next demonstrate that:

$$\forall a, b. (a, b) \in \mathbf{hb}' \Rightarrow (\text{im}(a), \text{im}(b)) \in \mathbf{hb} \quad (19)$$

$$\forall a. (a, a) \notin \mathbf{hb}' \quad (20)$$

To show that $G' = (E'', \mathbf{po}'', \mathbf{com}'', \mathbf{so}'', \mathbf{lbh}'') \in L^S.\mathcal{G}_c$, we are then required to show that for all locations s , G'_s is stack consistent on s . Pick an arbitrary location s and let $G'_s = \langle E', \mathbf{po}', \mathbf{com}', \mathbf{so}', \mathbf{lbh}' \rangle$. We then need to show:

- 1) $E'^c = \emptyset \vee \exists c \in C^q. E'^c = \{c\}$;
- 2) $\mathbf{com}' \subseteq \bigcup_{v \in \text{Val} \setminus \{\perp\}} \mathcal{A}^{s,v} \times \mathcal{R}^{s,v}$;
- 3) \mathbf{com}' , \mathbf{com}'^{-1} are functional;
- 4) $E' \cap \mathcal{R}^s \setminus \text{rng}(\mathbf{com}') \subseteq \mathcal{R}^{s,\perp}$
- 5) $[\mathcal{A}^s \setminus \text{dom}(\mathbf{com}')] ; \mathbf{hb}' ; [\mathcal{R}^{s,\perp}] = \emptyset$;
- 6) $\mathbf{so}' = \mathbf{com}'$; and
- 7) $\forall a_1, a_2, r_1, r_2. (a_1, r_1), (a_2, r_2) \in \mathbf{com}' \wedge (a_1, a_2), (r_1, r_2) \in \mathbf{hb}' \Rightarrow (a_2, r_1) \notin \mathbf{hb}'$.

Parts (1), (2) and (6) follow immediately from the construction of G' . Part (3) follows from the construction of G' and the fact that \mathbf{com} and \mathbf{com}^{-1} are functional for L^{ws} and L^X . Part (5) follows trivially as $\mathcal{R}^{s,\perp} = \emptyset$.

For part (4), we demonstrate that $E' \cap \mathcal{R}^s \setminus \text{rng}(\mathbf{com}') = \emptyset$. We proceed by contradiction. Let us assume there exists $r \in E' \cap \mathcal{R}^s$ such that $r \notin \text{rng}(\mathbf{com}')$. Let $\text{lab}(r) = \text{pop}(s, v)$ for some value v . From the construction of G' we then know that there exists i and r' such that $r' = \text{imp}(r)$ and either: i) $r' = \theta_i^{rs(s)}.r$; or ii) $r' = \theta_i^{re(s)}.r$.

In case (i) we then know that $\text{lab}(r') = \text{try-pop}(\text{ws}, v, 1)$. As such, from the consistency of G and the specification of the weak stack library we know there exist w' such that $\text{lab}(w') = \text{try-push}(\text{ws}, v, 1)$ and $(w', r') \in \mathbf{com}$. Moreover, given the well-formedness of G and the shape of G traces we know that there exist j such that $w' = \theta_j^{as(s)}.a$. Consequently, from the construction of G' we know there exist w such that $\text{imp}(w) = w'$ and $(w, r) \in \mathbf{com}'$, contradicting our assumption that $r \notin \text{rng}(\mathbf{com}')$.

Similarly, in case (ii) we then know that $\text{lab}(r') = \text{exchange}(\text{ea}[j], \text{POP}, v)$ for some j . As such, from the consistency of G and the specification of the exchanger library we know there exist w' such that $\text{lab}(w') = \text{exchange}(\text{ea}[j], v, \text{POP})$ and $(w', r'), (r', w') \in \mathbf{com}$. Moreover, given the well-formedness of G and the shape of G traces we know that there exist j such that $w' = \theta_j^{ae(s)}.a$. Consequently, from the construction of G' we know there exist w such that $\text{imp}(w) = w'$ and $(w, r) \in \mathbf{com}'$, contradicting our assumption that $r \notin \text{rng}(\mathbf{com}')$.

For part (7) we proceed by contradiction. Let us assume there exist a_1, a_2, r_1, r_2 such that $(a_1, r_1), (a_2, r_2) \in \mathbf{com}'$, $(a_1, a_2), (r_1, r_2) \in \mathbf{lbh}'$, and $(a_2, r_1) \in \mathbf{lbh}'$. From (19) and the construction of G' we know $(\text{imp}(a_1), \text{imp}(r_1)), (\text{imp}(a_2), \text{imp}(r_2)) \in \mathbf{com}$, $(\text{imp}(a_1), \text{imp}(a_2)), (\text{imp}(r_1), \text{imp}(r_2)) \in \mathbf{hb}$, and $(\text{imp}(a_2), \text{imp}(r_1)) \in \mathbf{hb}$. There are now three cases to consider: i) $\text{imp}(a_2) = \theta_i^{as(s)}.a$, $\text{imp}(r_2) = \theta_j^{rs(s)}.a$, $\text{imp}(a_1) = \theta_k^{as(s)}.a$, $\text{imp}(r_1) = \theta_l^{rs(s)}.a$ for some i, j, k, l ; or ii) $\text{imp}(a_2) = \theta_i^{ae(s)}.a$ and $\text{imp}(r_2) = \theta_j^{re(s)}.a$ for some i, j ; or iii) $\text{imp}(a_1) = \theta_i^{ae(s)}.a$ and $\text{imp}(r_1) = \theta_j^{re(s)}.a$ for some i, j .

Case (i) however leads to contradiction as it contradicts the L^{ws} -consistency of $(G)_{L^{\text{ws}}}$. In case (ii) from the definition of the L^X library and the symmetry of its \mathbf{com} relation we know that

($\text{imp}(r_2), \text{imp}(a_2)) \in \text{com}$. As such, we have $\text{imp}(a_2) \xrightarrow{\text{hb}} \text{imp}(r_1) \xrightarrow{\text{hb}} \text{imp}(r_2) \xrightarrow{\text{com}} \text{imp}(a_2)$. That is, $\text{imp}(a_2) \xrightarrow{\text{hb}} \text{imp}(r_2) \xrightarrow{\text{com}} \text{imp}(a_2)$, contradicting the assumption that G is consistent.

Similarly, in case (iii) from the definition of the L^X library and the symmetry of its com relation we know that $(\text{imp}(r_1), \text{imp}(a_1)) \in \text{com}$. As such, we have $\text{imp}(a_1) \xrightarrow{\text{hb}} \text{imp}(a_2) \xrightarrow{\text{hb}} \text{imp}(r_1) \xrightarrow{\text{com}} \text{imp}(a_1)$. That is, $\text{imp}(a_1) \xrightarrow{\text{hb}} \text{imp}(r_1) \xrightarrow{\text{com}} \text{imp}(a_1)$, contradicting the assumption that G is consistent.

K THE SOUNDNESS OF EXCHANGER IMPLEMENTATION

Let I denote the exchanger implementation in Fig. 5. To show the soundness of I , we appeal to Thm. 1 and show that I is locally sound on L^X .

Pick an arbitrary $\Lambda, f, G = \langle E, \text{po}, \text{com}, \text{so} \rangle, E', \text{po}'$ such that G is Λ -consistent and Λ -well-formed and $\text{abs}_{L^X, I}(f, \langle E, \text{po} \rangle, \langle E', \text{po}' \rangle)$. We must next find com', so' such that $\langle E', \text{po}', \text{com}', \text{so}', \text{lbh}' \rangle \in L^X \mathcal{G}_c$, where lbh' is as defined in Def. 11.

Note that each exchange operation $\text{exchange}(g, v)$ either:

- (1) offers a value at index $g+k$ (for some $k \in \mathbb{N}^+$ where k is an odd number) and returns unmatched due to a timeout; or
- (2) offers a value at index $g+k$ (for some $k \in \mathbb{N}^+$ where k is an odd number) and matches with value v' at index $g+k+1$; or
- (3) tries to offer a value at index $g+k$ (for some $k \in \mathbb{N}^+$ where i is an even number) and returns unmatched as the slot at index $g+k$ is already taken; or
- (4) offers a value at index $g+k$ (for some $k \in \mathbb{N}^+$ where k is an even number) and matches with value v' at index $g+k-1$.

Without loss of generality, let us assume e contains n exchange calls. For each i^{th} exchange operation of the form $\text{exchange}(g, v_i)$, the G_i contains a trace of one of the following forms depending which of the four categories above it falls into:

- $\theta_i^{t(g)}$ is of the form $s \xrightarrow{\text{polim}} o \xrightarrow{\text{polim}} t$, with $\text{lab}(s) = \text{load}(\text{rlx}, g, j_i)$, $\text{lab}(o) = \text{CAS}(\text{rel}, g+j_i, 0, v_i)$, and $\text{lab}(t) = \text{CAS}(\text{rlx}, g+j_i+1, 0, \perp)$, for some j_i and v_i where j_i is odd;
- $\theta_i^{o(g)}$ is of the form $s \xrightarrow{\text{polim}} o \xrightarrow{\text{polim}} a \xrightarrow{\text{polim}} r$, with $\text{lab}(s) = \text{load}(\text{rlx}, g, j_i)$, $\text{lab}(o) = \text{CAS}(\text{rel}, g+j_i, 0, v_i)$, $\text{lab}(a) = \text{load}(\text{rlx}, g+j_i+1, v'_i)$ with $v'_i \neq 0$, $\text{lab}(r) = \text{load}(\text{acq}, g+j_i+1, v'_i)$, for some j_i, v_i and v'_i where j_i is odd;
- $\theta_i^{f(g)}$ is of the form $s \xrightarrow{\text{polim}} f \xrightarrow{\text{polim}} o \xrightarrow{\text{polim}} c$, with $\text{lab}(s) = \text{load}(\text{rlx}, g, j_i)$, $\text{lab}(f) = \text{load}(\text{rlx}, g+j_i, w_i)$ with $w_i \neq 0$, $\text{lab}(o) = \text{load}(\text{rlx}, g+j_i+1, w'_i)$ with $w'_i \neq 0$, and $\text{lab}(c) = \text{CAS}(\text{rlx}, g, j_i, j_i+2)$ or $\text{lab}(c) = \text{load}(\text{rlx}, g, u_i)$ with $u_i \neq j_i$, for some j_i and v_i where j_i is odd;
- $\theta_i^{e(g)}$ is of the form $s \xrightarrow{\text{polim}} f \xrightarrow{\text{polim}} o \xrightarrow{\text{polim}} c \xrightarrow{\text{polim}} r$, with $\text{lab}(s) = \text{load}(\text{rlx}, g, j_i)$, $\text{lab}(f) = \text{load}(\text{rlx}, g+j_i, w_i)$ with $w_i \neq 0$, $\text{lab}(o) = \text{CAS}(\text{rel}, g+j_i+1, 0, v_i)$, $\text{lab}(c) = \text{CAS}(\text{rlx}, g, j_i, j_i+2)$ or $\text{lab}(c) = \text{load}(\text{rlx}, g, u_i)$ with $u_i \neq j_i$, $\text{lab}(r) = \text{load}(\text{acq}, g+j_i, v'_i)$, for some j_i, v_i and v'_i where j_i is odd.

Let $\text{so}' = \text{com}'$ with

$$\text{com}' \triangleq \left\{ (a, b), (b, a) \mid \begin{array}{l} \exists i, j, g. f(e_i^{o(g)}.o) = a \wedge f(e_j^{e(g)}.o) = b \\ \wedge (\theta_i^{o(g)}.o, \theta_j^{e(g)}.r) \in \text{com} \wedge (\theta_j^{e(g)}.o, \theta_i^{o(g)}.r) \in \text{com} \end{array} \right\}$$

To show that $G' = \langle E', \text{po}', \text{com}', \text{so}', \text{lbh}' \rangle \in L^X \mathcal{G}_c$, we are then required to show for all $g \in \text{Loc}$, G'_g is exchanger-consistent on g . Pick an arbitrary $g \in \text{Loc}$, we then need to show:

- 1) $E'^c = \emptyset \vee \exists c \in C^g. E'^c = \{c\}$;
- 2) com' is *symmetric, irreflexive* and $\text{com}' \subseteq \bigcup_{v_1, v_2 \in \text{Val}} X^{g, v_1, v_2} \times X^{g, v_2, v_1} \setminus \text{id}$;
- 3) com' is *functional*;
- 4) $E' \cap X^g \setminus \text{dom}(\text{com}') \subseteq \bigcup_{v \in \text{Val}} X^{g, v, \perp}$; and
- 5) $\text{so}' = \text{com}'$.

Parts (1), (2), and (5) follow immediately from the construction of G_s and the consistency of the C library. The proof of parts (3) and (4) follows from the definition of com' , the consistency of G_i and the definition of the C library.