

Later Credits: Supplementary Material

SIMON SPIES, MPI-SWS, Germany

LENNARD GÄHER, MPI-SWS, Germany

JOSEPH TASSAROTTI, New York University, USA

RALF JUNG, MIT CSAIL, USA

ROBBERT KREBBERS, Radboud University Nijmegen, The Netherlands

LARS BIRKEDAL, Aarhus University, Denmark

DEREK DREYER, MPI-SWS, Germany

In this document, we expand on several technical aspects that are not fully spelled out in the paper (*e.g.*, the invariants, the ghost state, etc. for the examples). In §1, we give an overview of the Iris rules used throughout the paper, and augment them with masks and add additional rules where appropriate. In §2, we provide additional material for the counter with a backup example. In §3, we provide additional material for the reordering refinements example. In §4, we explain how we define prepaid invariants. In §5, we expand on the *reverse refinements* that we have proven (see Section 6 in the paper). In §6, we provide additional detail of the integration of later credits into the model of Iris.

CONTENTS

Abstract	1
Contents	1
1 Iris Overview	2
2 Counter with a Backup	8
3 Reordering Refinements	12
4 Prepaid Invariants with Later Credits	16
5 Reverse Refinements	18
5.1 Logical Relations in Iris	19
5.2 Extending ReLoC with Repeatability and Later Credits	21
5.3 Memoization with Later Credits	23
6 The Model of Iris	25
6.1 Update Modalities	25
6.2 Weakest Precondition	26
6.3 Unbounded Credits are Unsound with Finite Step-indexing	28
References	29

Authors' addresses: Simon Spies, MPI-SWS, Germany, spies@mpi-sws.org; Lennard Gäher, MPI-SWS, Germany, gaeher@mpi-sws.org; Joseph Tassarotti, New York University, USA, jt4767@nyu.edu; Ralf Jung, MIT CSAIL, USA; Robbert Krebbers, Radboud University Nijmegen, The Netherlands, mail@robbertkrebbers.nl; Lars Birkedal, Aarhus University, Denmark, birkedal@cs.au.dk; Derek Dreyer, MPI-SWS, Germany, dreyer@mpi-sws.org.

The separating conjunction and magic wand

SEPWEAKEN $P * Q \vdash P$	SEPTRUE $P \vdash P * \text{True}$	SEPCOMM $P * Q \vdash Q * P$	SEPASSOC $P * (Q * R) \dashv\vdash (P * Q) * R$
SEPSPLIT $\frac{P \vdash P' \quad Q \vdash Q'}{P * Q \vdash P' * Q'}$	POINTSTOSEP $\ell \mapsto v * \ell \mapsto w \vdash \text{False}$	WANDINTRO $\frac{P * Q \vdash R}{P \vdash Q \multimap R}$	WANDELIM $\frac{P \vdash Q \multimap R}{P * Q \vdash R}$

The later modality

LATERINTRO $P \vdash \triangleright P$	LATERMONO $\frac{P \vdash Q}{\triangleright P \vdash \triangleright Q}$	LÖB $(\triangleright P \Rightarrow P) \vdash P$	LATERSEP $\triangleright (P * Q) \dashv\vdash \triangleright P * \triangleright Q$
LATEREXISTS $\frac{X \text{ non-empty}}{\triangleright (\exists x : X. P(x)) \dashv\vdash \exists x : X. \triangleright P(x)}$	LATERALL $\triangleright (\forall x : X. P(x)) \dashv\vdash \forall x : X. \triangleright P(x)$	LATERPERS $\triangleright \square P \dashv\vdash \square \triangleright P$	

The persistence modality

PERSDUP $\square P \vdash (\square P) * (\square P)$	PERSELIM $\square P \vdash P$	PERSMONO $\frac{P \vdash Q}{\square P \vdash \square Q}$	PERSANDSEP $(\square P) \wedge Q \vdash (\square P) * Q$	PERSIDEMP $\square P \vdash \square \square P$
PERSALL $\forall x : X. \square P(x) \vdash \square \forall x : X. P(x)$	PERS EXISTS $\square \exists x : X. P(x) \vdash \exists x : X. \square P(x)$			

Fig. 1. Basic Iris entailment rules

1 IRIS OVERVIEW

In the paper, we only show a selection of the proof rules that we use in Iris (with and without later credits), and we simplify some of them by omitting masks. In this section, we restate the rules with all masks, and complement them with additional rules omitted in the paper.

Basic entailment rules. In [Figure 1](#), we list some standard rules for Iris’s entailment relation $P \vdash Q$. We write $P \dashv\vdash Q$ for an entailment, which holds in both directions. These rules are used to manipulate the separating conjunction $P * Q$ and the associated notion of implication, the magic wand $P \multimap Q$. Moreover, these rules are used to work with the later modality $\triangleright P$ and the persistence modality $\square P$.

The persistence modality $\square P$ turns the notion of *duplicability* into a first-class principle in Iris. That is, $\square P$ can be duplicated, regardless of the proposition P . As a consequence, to prove $\square P$, one may only use other persistent resources (e.g., other propositions $\square Q$, invariants, etc.), but no non-persistent resources (e.g., $\ell \mapsto v$). The persistence modality “ \square ” is used, for example, in the definition of Hoare triples $\{P\} e \{v. Q\}_E$ (see [§6.2](#)) to ensure that Hoare triples are always *duplicable*. For example, if we have proven $\forall n. \{P\} f(n) \{v. Q\}$, then persistence ensures we can use the Hoare triple for multiple calls of f (e.g., in verifying $f(n) + f(m)$).

Structural rules

$$\begin{array}{c}
 \text{VALUE} \\
 \frac{}{\{\text{True}\} v \{w. w = v\}} \\
 \\
 \text{FRAME} \\
 \frac{\{P\} e \{v. Q\}_\mathcal{E}}{\{P * R\} e \{v. Q * R\}_\mathcal{E}} \\
 \\
 \text{CONSEQUENCE} \\
 \frac{P \vdash P' \quad \{P'\} e \{v. Q'\}_\mathcal{E} \quad \forall v. (Q' \vdash Q)}{\{P\} e \{v. Q\}_\mathcal{E}} \\
 \\
 \text{BIND} \\
 \frac{\{P\} e \{v. Q\}_\mathcal{E} \quad \forall v. \{Q\} K[v] \{w. R\}_\mathcal{E}}{\{P\} K[e] \{w. R\}_\mathcal{E}}
 \end{array}$$

Execution rules

$$\begin{array}{c}
 \text{PURESTEP} \\
 \frac{\{P\} e_2 \{v. Q\}_\mathcal{E} \quad e_1 \rightarrow_{\text{pure}} e_2}{\{\triangleright P\} e_1 \{v. Q\}_\mathcal{E}} \\
 \\
 \text{REF} \\
 \{\ell \mapsto ??v\} \{w. \exists \ell. w = \ell * \ell \mapsto v * \triangleright P\}_\mathcal{E} \\
 \\
 \text{LOAD} \\
 \{\ell \mapsto v * \triangleright P\} !\ell \{w. w = v * \ell \mapsto v * P\}_\mathcal{E} \\
 \\
 \text{CAS-SUCC} \\
 \frac{v \text{ comparable}}{\{\ell \mapsto v * \triangleright P\} \mathbf{CAS}(\ell, v, w) \{u. u = \mathbf{true} * \ell \mapsto w * P\}_\mathcal{E}} \\
 \\
 \text{CAS-FAIL} \\
 \frac{v \neq v' \quad (v \text{ comparable}) \vee (v' \text{ comparable})}{\{\ell \mapsto v' * \triangleright P\} \mathbf{CAS}(\ell, v, w) \{u. u = \mathbf{false} * \ell \mapsto v' * P\}_\mathcal{E}} \\
 \\
 \text{FORK} \\
 \frac{\{P\} e \{_. \text{True}\}_\top}{\{\triangleright P * \triangleright Q\} \mathbf{fork} \{e\} \{v. v = () * Q\}_\mathcal{E}} \\
 \\
 \text{STORE} \\
 \{\ell \mapsto v * \triangleright P\} \ell \leftarrow w \{u. u = () * \ell \mapsto w * P\}_\mathcal{E} \\
 \\
 \text{FAA} \\
 \{\ell \mapsto n * \triangleright P\} \mathbf{FAA}(\ell, m) \{v. v = n * \ell \mapsto (n + m) * P\}_\mathcal{E}
 \end{array}$$

Fig. 2. Hoare triple rules

Hoare triple rules. In Figure 2, we list the rules for reasoning about Iris’s Hoare triples without later credits (except for rules concerned with invariants, updates, and timelessness, which are discussed below). We write $\frac{P}{R} Q$ for Iris entailments $P * Q \vdash R$.

Compared to rules presented in the paper, we have annotated all Hoare triples with a mask and added the rules for values (*i.e.*, **VALUE**), the rule of consequence (*i.e.*, **CONSEQUENCE**), and the bind rule (*i.e.*, **BIND**). (The **BIND** rule allows us the focus on an expression e in some larger evaluation context K .) Moreover, we have added the primitive stepping rules for state manipulating operations (*i.e.*, **REF**, **STORE**, **LOAD**, **FAA**, **CAS-SUCC**, and **CAS-FAIL**) and for **fork** $\{e\}$ (*i.e.*, **FORK**).

Basic rules

$$\begin{array}{c}
 \text{UPDRETURN} \quad \text{UPDBIND} \quad \text{UPDEXEC} \quad \text{INVPERS} \\
 P \vdash \boxRightarrow^{\mathcal{E}} P \quad (\mathcal{E}_1 \boxRightarrow^{\mathcal{E}_2} P) * (P \multimap^{\mathcal{E}_2} \boxRightarrow^{\mathcal{E}_3} Q) \vdash \mathcal{E}_1 \boxRightarrow^{\mathcal{E}_3} Q \quad \frac{\{P\} e \{v. Q\}_{\mathcal{E}}}{\{\boxRightarrow^{\mathcal{E}} P\} e \{v. Q\}_{\mathcal{E}}} \quad \boxed{R}^N \vdash \square \boxed{R}^N \\
 \\
 \text{UPDINVALLOC} \quad \text{INVACC} \quad \text{UPDMASKWEAKEN} \\
 (\triangleright P) \vdash \boxRightarrow^{\mathcal{E}} \boxed{P}^N \quad \frac{\mathcal{N} \subseteq \mathcal{E}}{\boxed{R}^N \vdash \mathcal{E} \boxRightarrow^{\mathcal{E} \setminus \mathcal{N}} (\triangleright R * (\triangleright R \multimap^{\mathcal{E} \setminus \mathcal{N}} \boxRightarrow^{\mathcal{E}} \text{True}))} \quad \frac{\mathcal{E}_1 \subseteq \mathcal{E}_2 \quad P \vdash \boxRightarrow^{\mathcal{E}_1} P}{P \vdash \boxRightarrow^{\mathcal{E}_2} P} \\
 \\
 \text{MASKUPD} \\
 \frac{P \vdash \mathcal{E} \boxRightarrow^{\mathcal{E}'} P' \quad \{P'\} e \{v. Q'\}_{\mathcal{E}'} \quad Q' \vdash \mathcal{E}' \boxRightarrow^{\mathcal{E}} Q \quad e \text{ physically atomic}}{\{P\} e \{v. Q\}_{\mathcal{E}}}
 \end{array}$$

Derived rules

$$\begin{array}{c}
 \text{INVALLOC} \quad \text{INVOPENUPD} \\
 \frac{\boxed{R}^N \vdash \{P\} e \{w. Q\}_{\mathcal{E}}}{\{P * \triangleright R\} e \{w. Q\}_{\mathcal{E}}} \quad \frac{P * \triangleright R \vdash \boxRightarrow^{\mathcal{E} \setminus \mathcal{N}} Q * \triangleright R \quad \mathcal{N} \subseteq \mathcal{E}}{P * \boxed{R}^N \vdash \boxRightarrow^{\mathcal{E}} Q} \\
 \\
 \text{INVOPEN} \\
 \frac{\{ \triangleright R * P \} e \{v. \triangleright R * Q\}_{\mathcal{E} \setminus \mathcal{N}} \quad \mathcal{N} \subseteq \mathcal{E} \quad e \text{ physically atomic}}{\boxed{R}^N \vdash \{P\} e \{v. Q\}_{\mathcal{E}}}
 \end{array}$$

Fig. 3. Update and invariant rules

Update and invariant rules. In Figure 3, we list the rules for reasoning about Iris’s update modality and invariants. In general, the update modality carries two masks \mathcal{E}_1 and \mathcal{E}_2 , which indicate that when we prove $P \vdash \mathcal{E}_1 \boxRightarrow^{\mathcal{E}_2} Q$, then we get to open the invariants in \mathcal{E}_1 , we can update the ghost state, and afterwards we have to close the invariants in \mathcal{E}_2 . We write $\boxRightarrow^{\mathcal{E}} P \triangleq \mathcal{E} \boxRightarrow^{\mathcal{E}} P$ if both masks are the same. (In other Iris presentations, the mask for a non-mask-changing update is at the bottom (i.e., “ $\boxRightarrow_{\mathcal{E}}$ ”). Since this position conflicts with the “le” of our later elimination updates, we move it up.)

From the basic rules for manipulating the update modality, we have discussed **UPDRETURN**, **UPDBIND**, and **UPDEXEC** in the paper. (In the paper, we have omitted the mask in some of them if they are of no particular interest.) There are also some basic rules that we have not discussed in the paper: **INVPERS**, **UPDINVALLOC**, **INVACC**, and **MASKUPD**. The rule **INVPERS** makes sure that invariants are persistent, so we can duplicate them freely in our proofs. The other basic rules can be used to derive the rules presented in the paper (i.e., **INVALLOC**, **INVOPENUPD**, and **INVOPEN**). We include the more basic rules here to showcase the use of two distinct masks, before we return to updates with two masks in the definition of the weakest precondition (in §6.2).

$$\begin{array}{c}
 \text{HOARETIMELESS} \\
 \frac{\{P * Q\} e \{v. R\}_{\mathcal{E}} \quad \text{timeless}(Q)}{\{P * \triangleright Q\} e \{v. R\}_{\mathcal{E}}}
 \end{array}
 \qquad
 \begin{array}{c}
 \text{UPDTIMELESS} \\
 \frac{\text{timeless}(P)}{\triangleright P \vdash \Vdash^{\mathcal{E}} P}
 \end{array}
 \qquad
 \begin{array}{c}
 \text{POINTSTOTIMELESS} \\
 \text{timeless}(\ell \mapsto v)
 \end{array}$$

Fig. 4. Timelessness

Timelessness. In Figure 4, we list the rules for reasoning about timelessness. Recall from the paper that in Iris, there is a class of propositions, the so-called timeless propositions, which are largely independent of step-indexing *e.g.*, $\ell \mapsto v$ (see POINTSTOTIMELESS). What this means is that we can eliminate a later from them when we prove a Hoare triple (see HOARETIMELESS) and even when we prove an update (see UPDTIMELESS).

Later credits rules. In Figure 5, we list the rules for manipulating later credits and the later elimination update. We have discussed the *ghost theory* in the paper. CREDITSPILT allows us to combine and split credits. CREDITTIMELESS allows us to put credits into invariants without having to deal with later troubles (see Figure 4). The rules SUPPLYBOUND, SUPPLYDECR, and SUPPLYEXCL are used to prove soundness (and only there), since we define “ \Vdash_e ” in terms of \mathcal{L}_n (see §6.2).

The *update rules* for the later elimination update $\mathcal{E}_1 \Vdash_{le}^{\mathcal{E}_2} P$ are essentially¹ the same as those for the standard update $\mathcal{E}_1 \Vdash^{\mathcal{E}_2} P$, with the addition of the later elimination rule LEUPDLATER. Fittingly, we replace the standard update $\mathcal{E}_1 \Vdash^{\mathcal{E}_2} P$ with $\mathcal{E}_1 \Vdash_{le}^{\mathcal{E}_2} P$ in the model of Iris and, as a consequence, we can eliminate later modalities (if we have credits) wherever we could previously open invariants and manipulate ghost state (see §6).

The *later credit stepping rules* generate an additional credit in the postcondition, or in the case of PURESTEP in the precondition for the next step. Note that together with LEUPDLATER, these rules can be used to derive the Hoare triple rules in Figure 2.

Time receipt extension. In Figure 6, we list the rules for the time-receipt extension. The rules RECEIPTTIMELESS, RECEIPTCREDITS, and PURESTEP are discussed in the paper. The remaining rules generalize the rules from Figure 5 as one would expect.

¹There is one kind of rules that does not hold for the later elimination update: the interaction rules with Iris’s plain modality $\blacksquare P$. For more information, see §6.1.1.

Ghost theory

<p>CREDIT SPLIT</p> $\mathfrak{L}(n + m) \Leftrightarrow \mathfrak{L}n * \mathfrak{L}m$	<p>CREDIT TIMELESS</p> $\text{timeless}(\mathfrak{L}n)$	
<p>SUPPLY BOUND</p> $\mathfrak{L}_\bullet m * \mathfrak{L}_\bullet n \vdash m \geq n$	<p>SUPPLY DECR</p> $\mathfrak{L}_\bullet(n + m) * \mathfrak{L}_\bullet n \vdash \mathfrak{L}_\bullet m$	<p>SUPPLY EXCL</p> $\mathfrak{L}_\bullet m_1 * \mathfrak{L}_\bullet m_2 \vdash \text{False}$

Update rules

<p>LEUPD RETURN</p> $P \vdash \mathfrak{L}_e^{\mathcal{E}} P$	<p>LEUPD BIND</p> $(\mathcal{E}_1 \mathfrak{L}_e^{\mathcal{E}_2} P) * (P * \mathcal{E}_2 \mathfrak{L}_e^{\mathcal{E}_3} Q) \vdash \mathcal{E}_1 \mathfrak{L}_e^{\mathcal{E}_3} Q$	<p>LEUPD EXEC</p> $\frac{\{P\} e \{v. Q\}_{\mathcal{E}}}{\{\mathfrak{L}_e^{\mathcal{E}} P\} e \{v. Q\}_{\mathcal{E}}}$
<p>LEUPD LATER</p> $\mathfrak{L}1 * \triangleright P \vdash \mathfrak{L}_e^{\mathcal{E}} P$	<p>LEUPD INV ALLOC</p> $P \vdash \mathfrak{L}_e^{\mathcal{E}} \boxed{P}^{\mathcal{N}}$	<p>LEUPD MASK WEAKEN</p> $\frac{\mathcal{E}_1 \subseteq \mathcal{E}_2 \quad P \vdash \mathfrak{L}_e^{\mathcal{E}_1} P}{P \vdash \mathfrak{L}_e^{\mathcal{E}_2} P}$
<p>LEINV ACC</p> $\frac{\mathcal{N} \subseteq \mathcal{E}}{\boxed{R}^{\mathcal{N}} \vdash \mathcal{E} \mathfrak{L}_e^{\mathcal{E} \setminus \mathcal{N}} (\triangleright R * (\triangleright R * \mathcal{E} \setminus \mathcal{N} \mathfrak{L}_e^{\mathcal{E}} \text{True}))}$		
<p>LEMASK UPD</p> $\frac{P \vdash \mathcal{E} \mathfrak{L}_e^{\mathcal{E}'} P' \quad \{P'\} e \{v. Q'\}_{\mathcal{E}'} \quad Q' \vdash \mathcal{E}' \mathfrak{L}_e^{\mathcal{E}} Q \quad e \text{ physically atomic}}{\{P\} e \{v. Q\}_{\mathcal{E}}}$		

Later credit step rules

<p>PURE STEP</p> $\frac{\{P * \mathfrak{L}1\} e_2 \{v. Q\}_{\mathcal{E}} \quad e_1 \rightarrow_{\text{pure}} e_2}{\{P\} e_1 \{v. Q\}_{\mathcal{E}}}$	<p>FORK</p> $\frac{\{\triangleright P\} e \{_ . \text{True}\}_{\top}}{\{P\} \mathbf{fork} \{e\} \{v. v = () * \mathfrak{L}1\}_{\mathcal{E}}}$
<p>REF</p> $\{\text{True}\} ??v \{w. \exists \ell. w = \ell * \ell \mapsto v * \mathfrak{L}1\}_{\mathcal{E}}$	<p>STORE</p> $\{\ell \mapsto v\} \ell \leftarrow w \{u. u = () * \ell \mapsto w * \mathfrak{L}1\}_{\mathcal{E}}$
<p>LOAD</p> $\{\ell \mapsto v\} !\ell \{w. w = v * \ell \mapsto v * \mathfrak{L}1\}_{\mathcal{E}}$	<p>FAA</p> $\{\ell \mapsto n\} \mathbf{FAA}(\ell, m) \{v. v = n * \ell \mapsto (n + m) * \mathfrak{L}1\}_{\mathcal{E}}$
<p>CAS-Succ</p> $\frac{v \text{ comparable}}{\{\ell \mapsto v\} \mathbf{CAS}(\ell, v, w) \{u. u = \mathbf{true} * \ell \mapsto w * \mathfrak{L}1\}_{\mathcal{E}}}$	
<p>CAS-FAIL</p> $\frac{v \neq v' \quad (v \text{ comparable}) \vee (v' \text{ comparable})}{\{\ell \mapsto v'\} \mathbf{CAS}(\ell, v, w) \{u. u = \mathbf{false} * \ell \mapsto v' * \mathfrak{L}1\}_{\mathcal{E}}}$	

Fig. 5. Later credits rules

General rules

$$\text{RECEIPTTIMELESS} \quad \text{timeless}(\underline{\mathbb{X}}n) \quad \frac{\text{RECEIPTCREDITS} \quad \frac{\{P\} e \{v. Q\} \quad e \notin \text{Val}}{\{P * \underline{\mathbb{X}}n\} e \{v. Q * \ell n * \underline{\mathbb{X}}n\}}}{\{P * \underline{\mathbb{X}}n\} e \{v. Q * \ell n * \underline{\mathbb{X}}n\}}$$

Execution rules

$$\begin{array}{c} \text{PURESTEP} \\ \frac{\{P * \ell 1 * \underline{\mathbb{X}}1\} e_2 \{v. Q\}_{\mathcal{E}} \quad e_1 \rightarrow_{\text{pure}} e_2}{\{P\} e_1 \{v. Q\}_{\mathcal{E}}} \end{array} \quad \begin{array}{c} \text{FORK} \\ \frac{\{\triangleright P\} e \{_ . \text{True}\}_{\top}}{\{\triangleright P\} \text{fork} \{e\} \{v. v = () * \ell 1 * \underline{\mathbb{X}}1\}_{\mathcal{E}}} \end{array}$$

$$\begin{array}{c} \text{REF} \\ \{\text{True} \text{ ??} v\} \{w. \exists \ell. w = \ell * \ell \mapsto v * \ell 1 * \underline{\mathbb{X}}1\}_{\mathcal{E}} \end{array} \quad \begin{array}{c} \text{STORE} \\ \{\ell \mapsto v\} \ell \leftarrow w \{u. u = () * \ell \mapsto w * \ell 1 * \underline{\mathbb{X}}1\}_{\mathcal{E}} \end{array}$$

$$\begin{array}{c} \text{LOAD} \\ \{\ell \mapsto v\} !\ell \{w. w = v * \ell \mapsto v * \ell 1 * \underline{\mathbb{X}}1\}_{\mathcal{E}} \end{array}$$

$$\begin{array}{c} \text{FAA} \\ \{\ell \mapsto n\} \text{FAA}(\ell, m) \{v. v = n * \ell \mapsto (n + m) * \ell 1 * \underline{\mathbb{X}}1\}_{\mathcal{E}} \end{array}$$

$$\begin{array}{c} \text{CAS-Succ} \\ \frac{v \text{ comparable}}{\{\ell \mapsto v\} \text{CAS}(\ell, v, w) \{u. u = \text{true} * \ell \mapsto w * \ell 1 * \underline{\mathbb{X}}1\}_{\mathcal{E}}} \end{array}$$

$$\begin{array}{c} \text{CAS-FAIL} \\ \frac{v \neq v' \quad (v \text{ comparable}) \vee (v' \text{ comparable})}{\{\ell \mapsto v'\} \text{CAS}(\ell, v, w) \{u. u = \text{false} * \ell \mapsto v' * \ell 1 * \underline{\mathbb{X}}1\}_{\mathcal{E}}} \end{array}$$

Fig. 6. Time receipt extension

Implementation

```

new()  $\triangleq$  let (b, p) := (ref(0), ref(0)); fork {bg_thread(b, p)}; (b, p)
incr(b, p)  $\triangleq$  let n = FAA(p, 1); await_backup(b, n + 1); n
get(b, p)  $\triangleq$  let n = ! p; await_backup(b, n); n
get_backup(b, p)  $\triangleq$  ! b
    
```

Helper Functions

```

bg_thread(b, p)  $\triangleq$  let n = ! p; b  $\leftarrow$  n; bg_thread(b, p) // copy primary to backup, in a loop
await_backup(b, n)  $\triangleq$  if ! b < n then await_backup(b, n) else () // loop until ! b reaches n
    
```

Specification

```

 $\vdash$  {True} new() {c.  $\exists y$ . is_counter $^N_\gamma$ (c) * value $_\gamma$ (0)}
is_counter $^N_\gamma$ (c)  $\vdash$   $\langle n$ . value $_\gamma$ (n)  $\rangle$  incr(c)  $\langle m$ . m = n * value $_\gamma$ (n + 1)  $\rangle_N$ 
is_counter $^N_\gamma$ (c)  $\vdash$   $\langle n$ . value $_\gamma$ (n)  $\rangle$  get(c)  $\langle m$ . m = n * value $_\gamma$ (n)  $\rangle_N$ 
is_counter $^N_\gamma$ (c)  $\vdash$   $\langle n$ . value $_\gamma$ (n)  $\rangle$  get_backup(c)  $\langle m$ . m = n * value $_\gamma$ (n)  $\rangle_N$ 
    
```

Fig. 7. Counter with a backup

2 COUNTER WITH A BACKUP

In this section, we expand on the details of the counter with a backup (shown in Figure 7). We first define the counter predicates $\text{is_counter}^N_\gamma(c)$ and $\text{value}_\gamma(n)$. We will chose these predicates such that $\text{is_counter}^N_\gamma(c)$ is persistent (i.e., $\text{is_counter}^N_\gamma(c) \vdash \Box \text{is_counter}^N_\gamma(c)$), such that it can be shared between threads, and $\text{value}_\gamma(n)$ is exclusive (i.e., $\text{value}_\gamma(n) * \text{value}_\gamma(m) \vdash \text{False}$), such that owning it gives full control over the counter. After defining the predicates, we discuss their definition and how they factor into the proof of the specifications (in Figure 7).

The ghost state. To define the counter predicates, we use *ghost state*. To be precise, we will use the following kinds of ghost state:

- (1) *Monotonically growing natural numbers* from the resource algebra $\text{Auth}(\mathbb{N}, \max)$ with a fractional authoritative element. We write $\text{mono}^Y_q(n) \triangleq [\bullet q n]^Y * [\circ n]^Y$ for the authoritative element and $\text{lb}^Y(n) \triangleq [\circ n]^Y$ for the persistent lower-bound fragments. Note that $\text{mono}^Y_q(n) * \text{mono}^Y_{q'}(n') \vdash q + q' \leq 1 \wedge n = n'$.
- (2) *Exclusive tokens* from the resource algebra $\text{Ex}(1)$. We write $\text{tok}_\gamma \triangleq [\overline{\text{ex}()}]^Y$ for the exclusive token.
- (3) *Iris's ghost maps*. We write $\text{GhostMapAuth}^Y(M)$ for the authoritative element (storing the map $M : X \xrightarrow{\text{fin}} Y$) and $x \xrightarrow{Y} y$ for the fragments. We write $x \xrightarrow{Y} \Box y$ if the fragment is persistent and thus the value of x is unchangeable.
- (4) *Iris's ghost variables*. We write $\gamma \xrightarrow{q} x$ for a ghost variable γ storing x with the fractional ownership q .

The counter predicates. In the paper and in Figure 7, we use an abstract name “ γ ” to indicate that $\text{is_counter}^N_\gamma(c)$ and $\text{value}_\gamma(n)$ depend on ghost state. Formally, the “ γ ” will consist of two ghost names: γ_{back} and γ_{ex} . Besides those, we will additionally allocate several auxiliary ghost names used

$$\begin{aligned}
 I_{\text{cnt}}(b, p, \gamma_{\text{back}}, \gamma_{\text{ex}}, \gamma_{\text{prim}}, \gamma_{\text{get}}, \gamma_{\text{put}}) \triangleq & \\
 & \exists (G : \mathbb{N} \xrightarrow{\text{fin}} \text{Gname}), (P : \mathbb{N} \xrightarrow{\text{fin}} \text{Gname} \times \text{Gname}), (n_b, n_p : \mathbb{N}). b \mapsto n_b * p \mapsto n_p * n_p \geq n_b \\
 & * \text{mono}_{1/4}^{\gamma_{\text{back}}}(n_b) * \text{mono}_1^{\gamma_{\text{prim}}}(n_p) * \text{GhostMapAuth}^{\gamma_{\text{get}}}(G) * \text{GhostMapAuth}^{\gamma_{\text{put}}}(P) \\
 & * \text{gets}(\gamma_{\text{back}}, \gamma_{\text{ex}}, G, n_b) * \text{incrs}(\gamma_{\text{back}}, \gamma_{\text{ex}}, P, n_b, n_p) * \bigstar_{n \mapsto \gamma \in G} n \xrightarrow{\gamma_{\text{get}}} \square \gamma
 \end{aligned}$$

where

$$\begin{aligned}
 \text{gets}(\gamma_{\text{back}}, \gamma_{\text{ex}}, G, n_b) \triangleq & \bigstar_{k \mapsto \gamma \in G} \exists O : \text{Gname} \times \text{Gname} \xrightarrow{\text{fin}} \text{unit}. \text{GhostMapAuth}^{\gamma}(O) \\
 & * \bigstar_{(\gamma_1, \gamma_2) \mapsto () \in O} \exists R. \boxed{I_{\text{get}}(\gamma_{\text{back}}, \gamma_{\text{ex}}, \gamma_1, \gamma_2, k, R)}^{\mathcal{N}. \text{get}} * \gamma_1 \xrightarrow{1/2} (n_b < k) \\
 \text{incrs}(\gamma_{\text{back}}, \gamma_{\text{ex}}, P, n_b, n_p) \triangleq & \text{dom}(P) = \{0, \dots, n_p - 1\} \\
 & * \bigstar_{k \mapsto (\gamma_1, \gamma_2) \in P} \exists R. \boxed{I_{\text{incr}}(\gamma_{\text{back}}, \gamma_{\text{ex}}, \gamma_1, \gamma_2, k, R)}^{\mathcal{N}. \text{incr}} * \gamma_1 \xrightarrow{1/2} (n_b \leq k) \\
 I_{\text{get}}(\gamma_{\text{back}}, \gamma_{\text{ex}}, \gamma_1, \gamma_2, k, R) \triangleq & (\text{AU}(n. \text{value}_{\gamma_{\text{back}}, \gamma_{\text{ex}}}(n), m. m = n * \text{value}_{\gamma_{\text{back}}, \gamma_{\text{ex}}}(n))_R^{\mathcal{N}} * \gamma_1 \xrightarrow{1/2} \text{true} * \text{!} 1) \\
 & \vee (R[k/n] * \gamma_1 \xrightarrow{1/2} \text{false}) \\
 & \vee (\text{tok}_{\gamma_2} * \gamma_1 \xrightarrow{1/2} \text{false}) \\
 I_{\text{incr}}(\gamma_{\text{back}}, \gamma_{\text{ex}}, \gamma_1, \gamma_2, k, R) \triangleq & (\text{AU}(n. \text{value}_{\gamma_{\text{back}}, \gamma_{\text{ex}}}(n), m. m = n * \text{value}_{\gamma_{\text{back}}, \gamma_{\text{ex}}}(n + 1))_R^{\mathcal{N}} * \gamma_1 \xrightarrow{1/2} \text{true} * \text{!} 1) \\
 & \vee (R[k/n] * \gamma_1 \xrightarrow{1/2} \text{false}) \\
 & \vee (\text{tok}_{\gamma_2} * \gamma_1 \xrightarrow{1/2} \text{false})
 \end{aligned}$$

Fig. 8. The counter invariant

in the definition of the two predicates. We define

$$\text{value}_{\gamma_{\text{back}}, \gamma_{\text{ex}}}(n) \triangleq \text{mono}_{1/4}^{\gamma_{\text{back}}}(n) * \text{tok}_{\gamma_{\text{ex}}}$$

and

$$\text{is_counter}_{\gamma_{\text{back}}, \gamma_{\text{ex}}}^{\mathcal{N}}(c) \triangleq \exists b, p : \text{Loc}. \exists \gamma_{\text{prim}}, \gamma_{\text{get}}, \gamma_{\text{put}}. c = (b, p) * \boxed{I_{\text{cnt}}(b, p, \gamma_{\text{back}}, \gamma_{\text{ex}}, \gamma_{\text{prim}}, \gamma_{\text{get}}, \gamma_{\text{put}})}^{\mathcal{N}}$$

where the invariant I_{cnt} is given in Figure 8.

The counter invariant and the distribution of ghost state. The invariant I_{cnt} is used to coordinate the counter state. It contains ownership of the physical state, *i.e.*, the locations b and p , and various ghost state constructs. We share the invariant between the forked-off background thread (*i.e.*, `bg_thread`) and the clients of the counter, which will use the counter through the counter operations `incr`, `get`, and `get_backup`.

For the physical state, the invariant contains the ownership of the primary b and the backup p . They store the value of the backup n_b and the value of the primary n_p , where the primary value is always greater than the backup value. The values n_b and n_p are existentially quantified in the invariant, so they can change over time. To ensure that they only increase in value (and never

decrease), the invariant includes $\text{mono}_{1/4}^{\text{back}}(n_b)$ and $\text{mono}_1^{\text{prim}}(n_p)$. The latter expresses logical ownership of the primary, whereas the former expresses logical ownership of the backup—a quarter of the ownership. That is, quarter of the ownership of $\text{mono}^{\text{back}}(n)$ resides in the shared invariant to make sure that the counter value $\text{value}_{\gamma_{\text{back}}, \gamma_{\text{ex}}}(n)$ matches the value of the backup stored in the invariant. The predicate $\text{value}_{\gamma_{\text{back}}, \gamma_{\text{ex}}}(n)$ contains the ownership of another quarter of $\text{mono}^{\text{back}}(n_b)$. The remaining half of $\text{mono}^{\text{back}}(n_b)$ is given to the background thread. To update the value of the backup b , one needs full ownership of $\text{mono}^{\text{back}}(n)$, since only then $\text{mono}^{\text{back}}(n)$ can be increased. Through this ownership split, we ensure that one can only increase the counter if *all parts* are present—the half of the background thread, the quarter in the invariant, and the quarter in $\text{value}_{\gamma_{\text{back}}, \gamma_{\text{ex}}}(n)$.

The remaining parts of the invariant enable the helping exchange. Let us start with the machinery in place for helping `incr`. The invariant I_{cnt} contains a map P that is used to track all the completed and pending increment operations (using a ghost map $\text{GhostMapAuth}^{\text{put}}(P)$). To understand how exactly this tracking works, we take a closer look at $\text{incrs}(\gamma_{\text{back}}, \gamma_{\text{ex}}, P, n_b, n_p)$. The map P maps the numbers $0, \dots, n_p - 1$ to pairs of ghost names γ_1 and γ_2 . Each mapping $k \mapsto (\gamma_1, \gamma_2) \in P$ corresponds to one `incr` operation that either has been helped already, or is currently awaiting helping. The k -th increment is pending if $n_b \leq k$, and has been helped otherwise. For each increment, we store two things: First, we store the invariant I_{inc} , which is the central part of the helping exchange for the k -th increment. Second, we store the information about the status of the increment operation in a ghost variable, γ_1 , with half ownership (the other half is in the invariant I_{inc}). We will describe the helping exchange more detail shortly.

Let us now turn to the machinery for helping `get`. In general, the setup is very similar to the machinery for `incr`. The most important difference is that there can be *multiple* get-operations pending at a time for the value k , while there can only ever be one increment. In the invariant I_{cnt} , the map G is used to track the completed and pending get-operations (using a ghost map $\text{GhostMapAuth}^{\text{get}}(G)$). In contrast to P , G only stores a single ghost name γ , but that ghost name is unchangeable (since $\ast_{n \mapsto \gamma \in G} n \xrightarrow{\gamma_{\text{get}}} \square \gamma$). To understand why, we take a closer look at $\text{gets}(\gamma_{\text{back}}, \gamma_{\text{ex}}, G, n_b)$. For each number $k \in \text{dom } G$, we store a map O in $\text{gets}(\gamma_{\text{back}}, \gamma_{\text{ex}}, G, n_b)$ with $\text{GhostMapAuth}^{\text{get}}(O)$. This map will keep track of *multiple* get-operations for the value k . (We use a finite map O with a singleton codomain, which effectively makes O a set.) As for the increment operations, we store for each helpée an invariant and a ghost variable that tracks the status of the helpée (*i.e.*, either still pending or already completed). Another important difference to `incr` is that helpées are only pending if their value k is *strictly* greater than n_b . Thus, whenever the backup n_b is increased, we *have to* linearize all pending get-operations.

The helping exchange. Let us now turn to the helping exchange. We cover the helping exchange from *three* perspectives: an increment operation, a get operation, and the background thread.

We start with *an increment operation*. When the increment operation increases the primary p from n_p to $n_p + 1$, it puts its atomic update:

$$\mathbf{AU}_{\text{inc}} \triangleq \mathbf{AU}(n. \text{value}_{\gamma}(n), m. m = n * \text{value}_{\gamma}(n + 1))_R^N$$

into a new invariant, I_{inc} together with half the ownership of a freshly allocated ghost variable $\gamma_1 \xrightarrow{1/2} \mathbf{false}$. It also allocates a new token tok_{γ_2} , which it keeps for itself. The invariant I_{inc} and the other half of $\gamma \xrightarrow{1/2} \mathbf{false}$ go into the shared counter invariant I_{cnt} . To be precise, the increment operation extends the map P with $n_p \mapsto (\gamma_1, \gamma_2)$. Afterwards, the increment operation waits for the backup to catch up. Once the backup has caught up, the increment operation knows $n_b \geq n_p + 1$ where n_b is the new value of the backup and n_p the old value of the primary that the increment

read in the previous step. Thus, the ghost variable γ_1 must be true, and the invariant I_{inc} must be in one of the last two states. In fact, the invariant must be in the second state, because the increment operation kept tok_{γ_2} for itself. Thus, the increment operation can use tok_{γ_2} to take out R , swapping it for tok_{γ_2} .

The helping exchange for a *get operation* is very similar to an increment. Instead of extending the map P , the get-operation extends the map O for the value of the primary n_p that it has read. If n_p is not yet in P , a fresh ghost name γ is allocated for n_p . The rest of the exchange: putting the atomic update into an invariant, adding the invariant to I_{inc} , and retrieving R are analogous to an increment.

Let us now turn to the *background thread*. The background thread first reads the value of the primary n_p . Then, in a second step, it updates the value of the backup b to n_p .² When the background thread is updating b , it is helping all the increments for the values $n_b + 1, \dots, n_p$ (which would read $n_b, \dots, n_p - 1$). Moreover, the background thread is also helping all the gets for values in this range. It proceeds as follows: First, it linearizes the increment for $n_b + 1$ by (1) taking out the atomic update from I_{inc} , (2) executing the update and incrementing $\text{mono}^{\text{Yback}}(n_b)$ to $\text{mono}^{\text{Yback}}(n_b + 1)$, and (3) returning the result R . Afterwards, it linearizes all the gets for $n_b + 1$ by (1) taking out the atomic update from I_{get} , (2) executing the update, and (3) returning the result R . After both the increment for $n_b + 1$ and all the gets for $n_b + 1$ are complete, the background thread proceeds with $n_b + 2, \dots, n_p$.

Later credits. The use of *later credits* in this example is pretty straightforward: In each iteration, the background thread saves one credit $\epsilon 1$ from some pure step (e.g., the reduction of `bg_thread()`). It uses this credit to open the *outer* invariant I_{cnt} . Then for each pending helpee operation, it opens the corresponding invariant (i.e., I_{get} or I_{inc}), uses the inner credit to eliminate the guarding later from the **AU**, and executes the update. Afterwards, it has the result R and can transition to the *second* state of the invariant, which no longer requires a later credit $\epsilon 1$.

²Technically, the primary could have advanced beyond the value n_p that the background thread has read, but that does not matter for our purposes. Those waiting increments will be helped in the next iteration.

Implementation

```

promise : 1 → pr(τ)
promise() ≜ (mklock(), ??none, ??[])
resolve : pr(τ) × τ → 1
resolve((l, r, c), a) ≜ lock(l); case !r of some(b) ⇒ unlock(l); abort()
    | none ⇒ r ← some(a); let fs = !c; c ← [];
    unlock(l); app (λf. f(a)) fs
then : pr(τ) × (τ → 1) → 1
then((l, r, c), f) ≜ lock(l); case !r of some(a) ⇒ unlock(l); f(a)
    | none ⇒ c ← f :: !c; unlock(l)
    
```

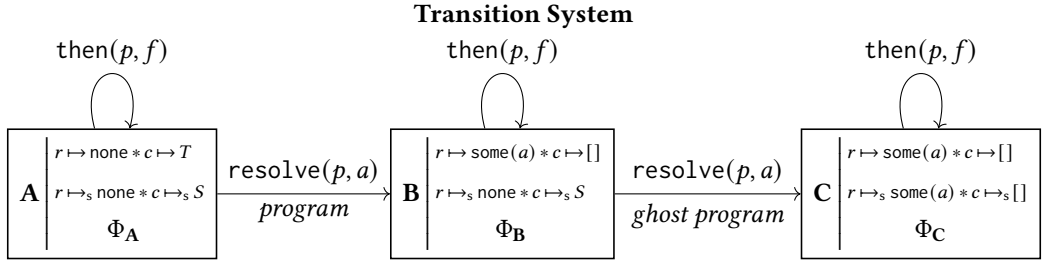


Fig. 9. Promise implementation and transition system

3 REORDERING REFINEMENTS

In this section, we expand on technical aspects of the promise reordering example that are sketched in the paper. Concretely, we elaborate on how we define the semantic interpretation of $\text{pr}(\tau)$ and the transition system backing the interpretation. Before we start, recall the implementation of promises from the paper and the transition system that we set up for each promise (depicted in Figure 9).

Conceptually, each promise is represented by an instance of this transition system. In terms of Iris propositions, this means we will use ghost state in the interpretation of $\text{pr}(\tau)$. Technically, we do not extend the syntax of ReLoC's types with $\text{pr}(\tau)$. Instead, we define the *semantic type* $\text{Pr}(A)$ for a semantic type A and prove our lemmas with respect to this semantic type.

We define $\text{Pr}(A)$ using a lock and an invariant:

$$\text{Pr}(A) \triangleq \{((l_t, r_t, c_t), (l_s, r_s, c_s)) \mid \exists \mathbf{y}. \text{Lock}(l_t, L_{r_t, c_t, \mathbf{y}_t}) * \boxed{I_{(l_t, r_t, c_t), (l_s, r_s, c_s), A, \mathbf{y}}}\}^N$$

If $\text{pr}(\tau)$ were a syntactic type, we could then define its interpretation as

$$\llbracket \text{pr}(\tau) \rrbracket \triangleq \text{Pr}(\llbracket \tau \rrbracket).$$

In our setting, we can pack up the promise implementation in an existential type

$$\exists \alpha. \{\text{mkpromise} : 1 \rightarrow \alpha; \text{resolve} : \alpha \times \tau \rightarrow_{\text{re}} 1; \text{then} : \alpha \times (\tau \rightarrow_{\text{re}} 1) \rightarrow_{\text{re}} 1\}$$

and assume that we are working in an environment where this existential type has been unpacked.³

³For the proof of this, we instantiate α precisely with $\llbracket \text{pr}(\tau) \rrbracket = \text{Pr}(\llbracket \tau \rrbracket)$.

In the rest of this section, we will explain how the lock proposition as well as the invariant are defined, and why they encode the promise transition system. For that purpose, let us fix A for the rest of the section.

To avoid any confusion with our auxiliary ghost state and clearly distinguish between the *program* and the *ghost program*, we refer to the former as the “target” and the latter as the “source”.

The ghost state. We start with the ghost state that we will use both components. The invariant is parameterized over the ghost names $\gamma = (\gamma_{\text{st}}, \gamma_s, \gamma_t, \gamma_{\text{hist}}, \gamma_{\text{cb}})$ for the ghost state that we use:

- an exclusive token $\text{tok}_t \triangleq \llbracket \bar{1} \rrbracket^{\gamma_t}$ of the fractional algebra Frac that connects the target lock and the invariant,
- a fractional token $\text{tok}_s(q) \triangleq \llbracket \bar{q} \rrbracket^{\gamma_t}$ of the fractional algebra Frac that enables the source resolve to transition to **state C** of the transition system,
- the oneshot algebra $\text{Ex}(1) + \text{Ag}(\text{List}(\text{Val} \times \text{Val}) \times \text{Val} \times \text{Val})$ that gets fired when the target resolves the promise (*i.e.*, when we transition from **state A** to **state B**). We define the elements $\text{pend} \triangleq \llbracket \text{inl}(\text{ex}(\bar{()}) \rrbracket^{\gamma_{\text{st}}}$ that gives the exclusive permission to fire the oneshot, as well as the persistent element $\text{shot}(H, v_t, v_s) \triangleq \llbracket \text{inr}(\text{ag}(H, v_t, v_s)) \rrbracket^{\gamma_{\text{st}}}$,
- an indexed list (modelled with a ghost map) tracking the callbacks in the order in which they arrived in the target, with the authoritative element $\bullet_{\text{Hist}}(H)$, stating that the full history is $H : \text{List}(\text{Val} \times \text{Val})$, and fragments $i \mapsto_{\text{Hist}} (c_t, c_s)$, stating that the i -th callback is c_t in the target and c_s in the source,
- an indexed list (with indices synchronized to the history list) supplying exclusive tokens for the state of callbacks (so the elements are in 1), with authoritative element $\bullet_{\text{Cb}}(B)$ (we do not care about the actual values in B) and exclusive tokens cb_i . Logically, the tokens cb_i will be provided to the then proof in the source to trade for a callback execution that was generated by the then proof in the target.

The invariant and the lock. With the ghost state in hand, we can turn to the definition of the invariant and the lock:

$$\begin{aligned}
 I_{(l_t, r_t, c_t), (l_s, r_s, c_s), A, \gamma} &\triangleq \exists H, B. \ \gamma = (\gamma_{\text{st}}, \gamma_s, \gamma_t, \gamma_{\text{hist}}, \gamma_{\text{cb}}) \\
 &\quad * |B| = |H| * \bullet_{\text{Hist}}(H) * \bullet_{\text{Cb}}(B) \\
 &\quad * (I_{H, \gamma_{\text{st}}, \gamma_s, \gamma_t, \gamma_{\text{hist}}, \gamma_{\text{cb}}}^A \vee I_{H, \gamma_{\text{st}}, \gamma_s, \gamma_t, \gamma_{\text{hist}}, \gamma_{\text{cb}}}^B \vee I_{H, \gamma_{\text{st}}, \gamma_s, \gamma_t, \gamma_{\text{hist}}, \gamma_{\text{cb}}}^C) \\
 L_{r_t, c_t, \gamma_t} &\triangleq \text{tok}_t * c_t \xrightarrow{1/2} _ * (r_t \xrightarrow{1/2} \text{none} \vee r_t \xrightarrow{1/2} \text{some_})
 \end{aligned}$$

Matching the transition system, the invariant has three states: I^A , I^B , and I^C . We will expand on these states shortly. Besides the states, the invariant always keeps track of the history (with $\bullet_{\text{Hist}}(H)$) and the callback list (with $\bullet_{\text{Cb}}(B)$).

Besides the invariant I , we also use a lock in the definition of $\text{Pr}(A)$. That is, each promise is protected by a *lock* in our implementation. For the target lock, we use Iris’s standard locks to guard ownership of L_{r_t, c_t, γ_t} . Here, c_t is the target location for the callback list and r_t is the target location for the resolved value. The lock owns one half of the target locations, as well as the exclusive target token. For the source lock, we proceed differently. The source lock is managed by the invariant I —the invariant ensures that the source is always locked, when the invariant is closed.

Let us now turn to the propositions I^A , I^B , and I^C , which encode the states **state A**, **state B**, and **state C**. The proposition Φ will not appear directly in each of these propositions—it is the remainder one obtains after removing the ownership of the callback list locations and the promise reference.

Invariant state A. In this state, neither the target nor source resolve has happened yet.

$$\begin{aligned}
 I_{H, \gamma_{st}, \gamma_s, \gamma_t, \gamma_{hist}, \gamma_{cb}}^A &\triangleq \exists c'_t, c'_s. c_t \xrightarrow{1/2} c'_t * c_s \mapsto_s c'_s \\
 &* \text{LinkedList}(c'_t, \text{rev}(H.1)) \\
 &* (r_t \xrightarrow{1/2} \text{none} \vee \text{tok}_t) * r_s \mapsto_s \text{none} \\
 &* l_s \mapsto_s \text{false} \text{ // the source lock is unlocked} \\
 &* \text{pend} * \text{tok}_s(1) \\
 &* \bigstar_{(v_t, v_s) \in H} (v_t, v_s) \in A \rightarrow_{re} 1 \text{ // semantic typing for all callbacks} \\
 &* \exists (m : \text{List}(\mathbb{B})) (C_s : \text{List}(\text{Val})). \text{ // subset } C_s \text{ of callbacks is also in the source} \\
 &\quad \text{LinkedList}(c'_s, \text{rev}(C_s)) * \text{indexed_subset}(m, H.2, C_s) \\
 &\quad \text{// the token is here if the source then has been executed} \\
 &* \bigstar_{i \rightarrow _ \in H} (m[i] = \text{true} * \text{tok}_i) \vee (m[i] = \text{false})
 \end{aligned}$$

Here, $\text{indexed_subset}(m, \text{full}, l)$ states that l contains exactly the elements (by index) of full for which the mask m (a list of Booleans) is true, in an arbitrary permutation. Concretely, it is defined as follows:

$$\text{indexed_subset}(m, \text{full}, l) \triangleq |m| = |\text{full}| * \text{Permutation}(l, \text{filter_mask}(m, \text{full}))$$

where

$$\begin{aligned}
 \text{filter_mask}([], _) &\triangleq [] \\
 \text{filter_mask}(\text{true} :: m', h :: l') &\triangleq h :: \text{filter_mask}(m', l') \\
 \text{filter_mask}(\text{false} :: m', h :: l') &\triangleq \text{filter_mask}(m', l') \\
 \text{filter_mask}(_, []) &\triangleq []
 \end{aligned}$$

Invariant state B. In this state, the target resolve has been initiated (but the target may still be executing the list of callbacks), while the source resolve has not happened yet. We remember the original history H at the moment where the target resolve “happened” as H_1 (*i.e.*, the moment when the reference r_t was updated), while H_2 contains the callbacks that arrived concurrently or reentrantly (and have been directly executed in the target).

$$\begin{aligned}
 I_{H, \gamma_{st}, \gamma_s, \gamma_t, \gamma_{hist}, \gamma_{cb}}^B &\triangleq \exists H_1, H_2, v_t, v_s, c'_t, c'_s. H = H_1 \# H_2 * c_t \xrightarrow{1/2} c'_t * c_s \mapsto_s c'_s \\
 &* \text{LinkedList}(c'_t, []) // \text{the target list has been emptied} \\
 &* r_t \xrightarrow{1/2} \text{some}(v_t) * r_s \mapsto_s \text{none} \\
 &* l_s \mapsto_s \text{false} // \text{the source lock is unlocked} \\
 &* \text{shot}(H_1, v_t, v_s) * \text{tok}_s(1/2) // \text{the source resolve proof has one half of the token} \\
 &* (v_t, v_s) \in A // \text{semantic typing for resolved values} \\
 &* \exists (m : \text{List}(\mathbb{B}))(C_s : \text{List}(\text{Val})). // \text{subset } C_s \text{ of callbacks is also in the source} \\
 &\quad \text{LinkedList}(c'_s, \text{rev}(C_s)) * \text{indexed_subset}(m, H.2, C_s) \\
 &* \bigstar_{i \mapsto (c_t, c_s) \in H} // \text{callback not in the source yet or the token is here} \\
 &\quad (m[i] = \text{false} \vee \text{tok}_i) \\
 &\quad // \text{we already have the source execution} \\
 &\quad // \dots \text{ or it will be produced by the target resolve} \\
 &* (c_s(v_s) \rightsquigarrow_{\text{ghost}} () \vee i < |H_1|)
 \end{aligned}$$

Invariant state state C. In this state, the resolve has also happened in the source.

$$\begin{aligned}
 I_{H, \gamma_{st}, \gamma_s, \gamma_t, \gamma_{hist}, \gamma_{cb}}^C &\triangleq \exists H_1, H_2, v_t, v_s, c'_t, c'_s. H = H_1 \# H_2 * c_t \xrightarrow{1/2} c'_t * c_s \mapsto_s c'_s \\
 &* \text{LinkedList}(c'_t, []) * \text{LinkedList}(c'_s, []) // \text{both lists have been emptied} \\
 &* r_t \xrightarrow{1/2} \text{some}(v_t) * r_s \mapsto_s \text{some}(v_s) \\
 &* l_s \mapsto_s \text{false} // \text{the source lock is unlocked} \\
 &* \text{shot}(H_1, v_t, v_s) * \text{tok}_s(1) \\
 &* (v_t, v_s) \in A // \text{semantic typing for resolved values} \\
 &* \bigstar_{i \mapsto (c_t, c_s) \in H} \text{tok}_i // \text{the callback has already been executed} \\
 &\quad \vee c_s(v_s) \rightsquigarrow_{\text{ghost}} () // \text{or we have deposited the source execution}
 \end{aligned}$$

4 PREPAID INVARIANTS WITH LATER CREDITS

As mentioned in Section 6 of the paper, with later credits we can define a new kind of invariant, a *prepaid invariant* \boxed{R}_{pre}^N , which can be opened around atomic expressions *without* a guarding later:

$$\frac{\text{INVPREOPEN} \quad \{R * P\} e \{v. R * Q\}_{\mathcal{E} \setminus \mathcal{N}} \quad \mathcal{N} \subseteq \mathcal{E} \quad e \text{ physically atomic}}{\boxed{R}_{\text{pre}}^N \vdash \{P\} e \{v. Q\}_{\mathcal{E}}}$$

The crux of prepaid invariants is that the later elimination has been prepaid—accounted for in amortized fashion by an earlier step of computation. To explain how that works and where later credits fit in, we first introduce a basic version $\boxed{R}_{\text{basic}}^N$, which requires paying a later credit $\mathcal{E}1$ to close it, and then show how to get the full version \boxed{R}_{pre}^N satisfying **INVPREOPEN**.

Basic prepaying. In the examples in the paper, we have seen *prepaid reasoning*. That is, we have seen how to obtain a credit $\mathcal{E}1$, keep it for several steps, and then spend it to eliminate the guarding later (e.g., from the contents of an invariant). For our basic prepaid invariants, we use the same idea, but *bundle a credit with an invariant*. Specifically, we define $\boxed{R}_{\text{basic}}^N \triangleq \boxed{R * \mathcal{E}1}^N$. As we explain below, we can now always use the credit in the invariant to remove the guarding later from R when we open the invariant. We obtain two proof rules for basic prepaid invariants:

$$\frac{\text{INVBASICALLOC} \quad \{P * \boxed{R}_{\text{basic}}^N\} e \{v. Q\}}{\{P * \mathcal{E}1 * \triangleright R\} e \{v. Q\}} \quad \frac{\text{INVBASICOPEN} \quad \{R * P\} e \{v. \mathcal{E}1 * R * Q\}_{\mathcal{E} \setminus \mathcal{N}} \quad \mathcal{N} \subseteq \mathcal{E} \quad e \text{ physically atomic}}{\{\boxed{R}_{\text{basic}}^N * P\} e \{v. Q\}_{\mathcal{E}}}$$

To explain the use of later credits, we sketch the proof of **INVBASICOPEN**. Since e is atomic, we can open the invariant behind $\boxed{R}_{\text{basic}}^N$ with **INVACC**. We have to prove $\{\triangleright(R * \mathcal{E}1) * P\} e \{v. \triangleright(R * \mathcal{E}1) * Q\}_{\mathcal{E} \setminus \mathcal{N}}$. The later credits $\mathcal{E}n$ are timeless (see **CREDITTIMELESS**), and hence we can use **LATERSEP** and **HOARE-TIMELESS** to eliminate the guarding later from $\mathcal{E}1$. Thus, we are left to prove

$$\{\triangleright R * \mathcal{E}1 * P\} e \{v. \triangleright(R * \mathcal{E}1) * Q\}_{\mathcal{E} \setminus \mathcal{N}}.$$

Next, we can use the later credit $\mathcal{E}1$ to eliminate the guarding later in front of R with **LEUPDLATER** and **LEUPDEXEC**. Thus, we are left with $\{R * P\} e \{v. \triangleright(R * \mathcal{E}1) * Q\}_{\mathcal{E} \setminus \mathcal{N}}$. We apply **LATERINTRO** in the postcondition, and are left with the goal $\{R * P\} e \{v. R * \mathcal{E}1 * Q\}_{\mathcal{E} \setminus \mathcal{N}}$, which is our assumption.

The basic prepaid invariants $\boxed{R}_{\text{basic}}^N$ are already quite useful: when we open these invariants, we do not have to eliminate any later modalities, since R is not guarded by a later modality. In exchange for this liberty, we have to put back one credit $\mathcal{E}1$ after executing e —a credit which we typically obtain from the step of e (e.g., with $\{\ell \mapsto v\} ! \ell \{w. w = v * \ell \mapsto v * \mathcal{E}1\}$). Unfortunately, there is a limitation. If we want to open *two* invariants $\boxed{P}_{\text{basic}}^{N_1}$ and $\boxed{Q}_{\text{basic}}^{N_2}$ while reasoning about the same single physical step of executing e , then we need to give back *two* credits, but the execution of e only generates *one* credit. So, next, we show how we extend the basic $\boxed{R}_{\text{basic}}^N$ to the full \boxed{R}_{pre}^N , which enables opening multiple and nested invariants.

Time receipt extension. The problem that prohibits opening multiple basic prepaid invariants is that we do not generate enough credits. Thus, to get prepaid invariants that satisfy **INVPREOPEN**, we need to increase the number of credits that are generated on each program step. Toward this end, we use the time receipts extension discussed in the paper (rules shown in **Figure 6**). Recall that the idea of the time receipts extension is that with each program step, we obtain a time receipt $\mathcal{E}1$ —a witness for the program step. (They were used originally to prove lower bounds on time complexity.)

Combined with later credits, this means that each execution step now produces both a later credit *and* a time credit (e.g., see [PURESTEP](#)). In addition, we can use the time receipts to get later credits with [RECEIPTCREDITS](#): if we own a receipt for n steps, then we can leverage it to generate n fresh later credits after the execution of e .

With time receipts in hand, we define $\boxed{R}_{\text{pre}}^N \triangleq \boxed{R * \pounds 1 * \pmb{\Sigma} 1}^N$ and obtain the rules:

$$\begin{array}{c} \text{INVPREALLOC} \\ \frac{\{P * \boxed{R}_{\text{pre}}^N\} e \{v. Q\}}{\{P * \pounds 1 * \pmb{\Sigma} 1 * \triangleright R\} e \{v. Q\}} \end{array} \qquad \begin{array}{c} \text{INVPREOPEN} \\ \frac{\{R * P\} e \{v. R * Q\}_{\mathcal{E} \setminus \mathcal{N}} \quad \mathcal{N} \subseteq \mathcal{E} \quad e \text{ physically atomic}}{\{\boxed{R}_{\text{pre}}^N * P\} e \{v. Q\}_{\mathcal{E}}} \end{array}$$

To understand how time receipts help us, we proceed with the proof of [INVPREOPEN](#). As in the proof of [INVBASICOOPEN](#), we open the invariants behind \boxed{R}_{pre}^N to obtain $\triangleright(R * \pounds 1 * \pmb{\Sigma} 1)$. We then eliminate the later from $\pounds 1$ and $\pmb{\Sigma} 1$, leveraging the fact that $\pmb{\Sigma} 1$ is, ironically, timeless (see [RECEIPTTIMELESS](#)). Once again, we use the later credit $\pounds 1$ to eliminate the later from $\triangleright R$, obtaining R . Then, after applying [LATERINTRO](#) in the postcondition, we are left with the goal $\{R * P * \pmb{\Sigma} 1\} e \{v. R * \pounds 1 * \pmb{\Sigma} 1 * Q\}_{\mathcal{E} \setminus \mathcal{N}}$. We use our receipt $\pmb{\Sigma} 1$ to generate a new credit with [RECEIPTCREDITS](#), and the resulting goal matches the premise of [INVPREOPEN](#).

With the time receipt extension of later credits, we have unlocked the full power of prepaid invariants. When we create one with [INVPREALLOC](#), we have to give up one credit $\pounds 1$ and one time receipt $\pmb{\Sigma} 1$. Afterwards, we can always use [INVPREOPEN](#) to open the invariants around Hoare triples without a later guarding the contents. Unlike [INVBASICOOPEN](#), we can apply [INVPREOPEN](#) multiple times if we want to open two (or more) invariants $\boxed{P}_{\text{pre}}^{N_1}$ and $\boxed{Q}_{\text{pre}}^{N_2}$. In particular, we can use [INVPREOPEN](#) to open nested invariants (e.g., $\boxed{\exists \ell. \exists n : \mathbb{N}. \ell \mapsto n}_{\text{pre}}^{N_1} * \gamma \mapsto_{\text{ghost}} \ell}_{\text{pre}}^{N_2}$ from the introduction of the paper) without any difficulty.

Limitations. Above, we have seen that prepaid invariants can be opened around Hoare triples without a guarding later (see [INVPREOPEN](#)). Naturally, this begs the question: what about opening prepaid invariants around updates (e.g., see [INVOPENUPD](#)). Sadly, even with prepaid invariants, the guarding later remains in the rule. This is not by accident: [Krebbers et al. \[2017a\]](#) have shown that an invariant opening rule without later for updates is not sound.

Since logically atomic updates are defined solely in terms of updates, we also do not inherit a later-free rule for opening invariants around atomic updates from prepaid invariants. Nevertheless, as explained in the paper, later credits still bring significant benefits to logical atomicity proofs.

```

checkCache c x  $\triangleq$  case !c of none  $\Rightarrow$  none | some(y, v)  $\Rightarrow$  if x = y then some(v) else none
memo f  $\triangleq$  let c = ??none);
       $\lambda$ x. case checkCache c x of some(v)  $\Rightarrow$ 
          | none  $\Rightarrow$  let v = f x; c  $\leftarrow$  some(x, v); v

```

Fig. 10. Implementation of a concurrent memoization function.

5 REVERSE REFINEMENTS

We show how later credits can be used to address a limitation with step-indexed logical relations described by Svendsen et al. [2016]. The issue arises when there is a function $f : \tau \rightarrow \tau$ and we want to show for all $e : \tau$ that $f(e)$ is contextually equivalent to e at type τ , written $f(e) \equiv_{\text{ctx}} e : \tau$. One strategy to show such an equivalence is to split it into proving two contextual refinements: we show $f(e) \leq_{\text{ctx}} e : \tau$ and $e \leq_{\text{ctx}} f(e) : \tau$ where \leq_{ctx} is contextual refinement. To prove these contextual refinements, we show that the expressions *logically* refine each other, according to a step-indexed logical relation. That is, we show $f(e) \leq_{\log} e : \tau$ and $e \leq_{\log} f(e) : \tau$, where \leq_{\log} is a step-indexed judgment which (by the logical relation’s soundness theorem) implies \leq_{ctx} .

Generally, when proving a logical refinement of the form $e_1 \leq_{\log} e_2 : \tau$, steps of e_1 allow elimination of later. Thus, showing $f(e) \leq_{\log} e : \tau$ is usually relatively straight-forward, as far as step-indexing goes, since evaluating $f(e)$ takes steps, which provides opportunities to eliminate later. On the other hand, showing $e \leq_{\log} f(e) : \tau$, which we call the *reverse refinement*, can be problematic. Since we want to show $e \leq_{\log} f(e) : \tau$ for all $e : \tau$, we cannot use steps taken by e to eliminate later because e could be a value that does not take any steps at all.

To address this issue with later elimination in the reverse refinement, Svendsen et al. [2016] use a *transfinite* step-indexed logical relation. Transfinite step-indexed models come with some drawbacks (discussed in the main paper), so here we show that later credits are an alternative solution for proving reverse refinements that avoids the need for transfinite step indexing. As for reordering refinements, we demonstrate this by modifying ReLoC [Frumin et al. 2018, 2021], the framework for proving contextual equivalences using step-indexed logical relations encoded in Iris. Using this new version of ReLoC we have verified the reverse refinement example considered by Svendsen et al. [2016], as well as a more complicated concurrent memoization example. We focus on the memoization example here.

The key idea is simple: rather than trying to prove $\vdash e \leq_{\log} f(e) : \tau$, we instead prove $\varepsilon n * \mathbb{X}n \vdash e \leq_{\log} f(e) : \tau$, where n is some number of additional later credits and time receipts that are needed to establish the refinement. The logical relation’s soundness theorem is extended to say that $\varepsilon n * \mathbb{X}n \vdash e_1 \leq_{\log} e_2 : \tau$ implies $e_1 \leq_{\text{ctx}} e_2 : \tau$ for any n . Taking advantage of these additional credits and receipts requires changing the interpretation of types in the logical relation, as we will see. But before we get there, we describe our memoization example in more detail.

Example: memoization of repeatable functions. Memoization is a strategy to improve performance of an algorithm by caching computed values. Figure 10 gives a simple implementation of a concurrent memoization routine that caches the most recently computed value of a function. Given a function f as input, $\text{memo } f$ first allocates a reference cell c to store cached results. Then, it returns a new function that, when applied to an argument x , first checks the contents of c to see if there is a cached value for the argument x , and returns it if so. If not, it evaluates $f x$ to get some value v , and then stores $\text{some}(x, v)$ in c .

Suppose $f : \tau_1 \rightarrow \tau_2$. When is memo $f \equiv_{\text{ctx}} f : \tau_1 \rightarrow \tau_2$? First, we need to require τ_1 to be an *equality type*, meaning that values of type τ_1 can be tested for equality (e.g. `int`, or $\tau_1 \times \tau_2$, where τ_1 and τ_2 are respectively equality types). Second, if f has observable side-effects, then this equivalence may not hold, since the memoized version will run f fewer times if there is a cache hit. However, if re-running $(f x)$ always returns the same value and has no observable side-effects after the first run, then we should expect memo $f \equiv_{\text{ctx}} f : \tau_1 \rightarrow \tau_2$. We call such functions *repeatable*.

The existing type system for the language found in ReLoC, which is a version of System F extended with recursive types, mutable references, and concurrency features, is not rich enough to state that a function is repeatable in this sense. To be able to state a contextual equivalence formally, we thus extend the type system in ReLoC with a new type $\tau_1 \rightarrow^{\text{rep}} \tau_2$ of repeatable functions. The typing rule for this type uses a new typing relation, $\Gamma \vdash^{\text{rep}} e : \tau$, which implies that e is a repeatable expression of type τ . This judgment is a restriction of the standard typing judgment \vdash that removes the rules for operations that have side effects.⁴ We then extend the standard typing judgment \vdash with two new rules for introducing and eliminating terms of type \rightarrow^{rep} :

$$\frac{\Gamma, x : \tau_1, f : \tau_1 \rightarrow^{\text{rep}} \tau_2 \vdash^{\text{rep}} e : \tau_2}{\Gamma \vdash (\mathbf{rec} f x = e) : \tau_1 \rightarrow^{\text{rep}} \tau_2} \qquad \frac{\Gamma \vdash f : \tau_1 \rightarrow^{\text{rep}} \tau_2 \quad \Gamma \vdash e : \tau_1}{\Gamma \vdash f e : \tau_2}$$

With these definitions in place, we can formally state the desired result: for all f , if $f : \tau_1 \rightarrow^{\text{rep}} \tau_2$ and τ_1 is an equality type, then memo $f \equiv_{\text{ctx}} f : \tau_1 \rightarrow \tau_2$. Proving this contextual equivalence runs into the issue with the reverse refinement described above: when trying to show $f \leq_{\text{log}} \text{memo } f$, we have the problem that we need to eliminate later, but f is an arbitrary function value that may not have any spare steps to take. Adding later credits to the interpretation of function types will allow us to work around this issue.

5.1 Logical Relations in Iris

We first recall the basics of how the step-indexed logical relation in ReLoC is defined. ReLoC uses Iris’s program logic to specify the behavior of programs. This means we need a way to do relational reasoning about pairs of programs in Iris, instead of the unary reasoning about a single program that we have seen so far. To do relational reasoning inside of Iris’s unary logic, ReLoC uses a technique from CaReSL [Turon et al. 2013], in which a second program is represented by ghost state in Iris (this technique has also been used in other formalization of logical relations in Iris [Krogh-Jespersen et al. 2017; Krebbers et al. 2017b; Tassarotti et al. 2017; Timany et al. 2018; Spies et al. 2021]). This ghost state has assertions of the form $j \Vdash e$ which mean that thread j in this ghost program is executing expression e . Similarly, there are ghost assertions of the form $\ell \mapsto_s v$ which mean that location ℓ points to v in the ghost program’s state. The ghost program is “executed” by modifying the ghost state with the update modality. For example, to perform a store of w to reference cell ℓ in the ghost program, we have the rule $j \Vdash (\ell \leftarrow w) * \ell \mapsto_s v \vdash \Vdash^{\text{N}_{\text{reloc}}} j \Vdash () * \ell \mapsto_s w$, which reflects that the store returns the unit value $()$, and the reference cell now contains the value w .

To prove a relational property about programs e_1 and e_2 , it suffices to prove a Hoare triple about e_1 in which the precondition has a ghost thread running e_2 in an arbitrary evaluation context K . We define an assertion in Iris that expresses this relational pattern. Given $P : (\text{Val} \times \text{Val}) \rightarrow \text{iProp}$ where iProp is the type of Iris assertions, we define

$$e_1 \leq e_2 : P \triangleq \forall j, K. \{j \Vdash K[e_2]\} e_1 \{v_1. \exists v_2. j \Vdash K[v_2] * P(v_1, v_2)\}$$

⁴The fragment \vdash^{rep} is somewhat limited since expressions may not contain instructions with side-effects. It is possible to bend this limitation. In the following, we develop a logical relation for \vdash^{rep} which admits additional terms that cannot be typed syntactically in the side-effect free fragment \vdash^{rep} , but are *semantically repeatable*. For example, $\lambda(). \mathbf{let} r = \text{??41}; !r + 1$ is semantically repeatable even though it has side effects from the perspective of \vdash^{rep} .

$$\begin{aligned}
 \llbracket \text{int} \rrbracket &\triangleq \lambda(v_1, v_2). \exists z \in \mathbb{Z}. v_1 = v_2 = z \\
 \llbracket \tau \times \tau' \rrbracket &\triangleq \lambda(v_1, v_2). \exists v_a, v'_a, v_b, v'_b. v_1 = (v_a, v'_a) * v_2 = (v_b, v'_b) * \llbracket \tau \rrbracket(v_a, v_b) * \llbracket \tau' \rrbracket(v'_a, v'_b) \\
 \llbracket \tau + \tau' \rrbracket &\triangleq \lambda(v_1, v_2). \exists u_1, u_2. (v_1 = \mathbf{inl}(u_1) * v_2 = \mathbf{inl}(u_2) * \llbracket \tau \rrbracket(u_1, u_2)) \vee \\
 &\quad (v_1 = \mathbf{inr}(u_1) * v_2 = \mathbf{inr}(u_2) * \llbracket \tau' \rrbracket(u_1, u_2)) \\
 \llbracket \tau \rightarrow \tau' \rrbracket &\triangleq \lambda(v_1, v_2). \forall u_1, u_2. \Box(\llbracket \tau \rrbracket(u_1, u_2) \multimap (v_1 \ u_1) \leq (v_2 \ u_2) : \llbracket \tau' \rrbracket) \\
 \llbracket \text{ref } \tau \rrbracket &\triangleq \lambda(v_1, v_2). \exists \ell_1, \ell_2. v_1 = \ell_1 * v_2 = \ell_2 * \boxed{\exists u_1, u_2. \ell_1 \mapsto u_1 * \ell_2 \mapsto_s u_2 * \llbracket \tau \rrbracket(u_1, u_2)}^{\mathcal{N}.\ell_1.\ell_2}
 \end{aligned}$$

 Fig. 11. Type interpretation $\llbracket - \rrbracket$ in ReLoC. (Polymorphic types and recursive types omitted.)

The adequacy theorem of Iris then ensures that, if $\vdash e_1 \leq e_2 : P$, and e_1 terminates with value v_1 , there exists an execution of e_2 in which it terminates with a value v_2 such that $P(v_1, v_2)$ holds.

With this method of encoding relational properties, we can now define a logical relation. To simplify the explanation here, we leave out the details of how this approach scales to polymorphic and recursive types, since the addition of later credits that we describe later does not affect that part of the logical relation. The logical relation is defined in three steps:

- (1) First, we define a type interpretation function $\llbracket - \rrbracket : \text{Type} \rightarrow (\text{Val} \times \text{Val}) \rightarrow i\text{Prop}$. That is, for each type τ , $\llbracket \tau \rrbracket$ is an Iris relation on values.
- (2) Next, this type interpretation is used to define a logical refinement relation on closed expressions $(e_1 \leq_{\log} e_2 : \tau) \triangleq (e_1 \leq e_2 : \llbracket \tau \rrbracket)$. This definition of logical refinement uses the above encoding of relational reasoning in Iris, with a postcondition that requires the values the expressions reduce to to be related according to $\llbracket \tau \rrbracket$.
- (3) Finally, the logical refinement relation is lifted from closed expressions to open expressions. Given a type context $\Gamma = x_1 : \tau_1, \dots, x_n : \tau_n$ and open terms e, e' , we define:

$$\begin{aligned}
 \Gamma \models e \leq_{\log} e' : \tau &\triangleq \forall v_1, v'_1, \dots, v_n, v'_n. \\
 &\quad \left(\bigstar_{i=1, \dots, n} \llbracket \tau_i \rrbracket(v_i, v'_i) \right) \vdash e[v_1/x_1] \cdots [v_n/x_n] \leq_{\log} e'[v'_1/x_1] \cdots [v'_n/x_n] : \tau
 \end{aligned}$$

Figure 11 gives an excerpt of the definition of $\llbracket - \rrbracket$ in ReLoC. The two most interesting cases are for $\tau \rightarrow \tau'$ and $\text{ref } \tau$. The former says that two values are related at type $\tau \rightarrow \tau'$, if whenever they are applied to values that are assumed to be related at type τ , the resulting application expressions are related at the interpretation of $\llbracket \tau' \rrbracket$. In the definition of $\llbracket \tau \rightarrow \tau' \rrbracket$, we use Iris's *persistence* modality $\Box P$, which ensures that the assertion P does not own any non-duplicable resources. We will expand on the persistence modality and its use shortly. For $\text{ref } \tau$, the relation says that the two values must be locations, and we use an Iris invariant assertion that requires the two locations to always point to values that are related at type τ . (To keep the changes to ReLoC specific to reverse refinements, we do not use a prepaid invariant here.) The invariant here is implicitly making use of Iris's step-indexing, which is what allows us to avoid the usual circularity issues that arise when trying to define logical relations for systems with higher-order mutable state [Ahmed 2004; Birkedal et al. 2011].

The logical relation has the following two key properties:

THEOREM 5.1 (SOUNDNESS). *If $\Gamma \models e_1 \leq_{\log} e_2 : \tau$ then $\Gamma \vdash e_1 \leq_{\text{ctx}} e_2 : \tau$*

THEOREM 5.2 (FUNDAMENTAL PROPERTY). *If $\Gamma \vdash e : \tau$ then $\Gamma \models e \leq_{\log} e : \tau$*

The soundness theorem is what ensures that the logical relation is useful for proving contextual equivalences, and it follows from the adequacy of Iris. Meanwhile, the fundamental property lets us

automatically deduce that a syntactically well typed term is logically related to itself. This theorem is proved by showing that the logical relation is a congruence relation wrt. all typing rules.

Because the type system here is not sub-structural, a key component of this proof is that the $\llbracket - \rrbracket$ predicate is duplicable for all types. This means that when trying to prove $\Gamma \models e_1 \leq_{\log} e_2 : \tau$, we can duplicate the assumptions about the values substituted in for the variables in Γ . This duplicability requirement is what forces us to include the \Box modality in the definition of $\llbracket \tau \rightarrow \tau' \rrbracket$ above. The modality $\Box P$ requires us to prove P without any assertions that we own exclusively (e.g., $\ell \mapsto v$). We may only use duplicable assertions (e.g., $\Box P^N$) and, as a result, $\Box P$ is also duplicable.

5.2 Extending ReLoC with Repeatability and Later Credits

To extend this logical relation to support the repeatability type judgment (\vdash^{rep}) and repeatable function type (\rightarrow^{rep}), we need an Iris assertion that captures that an expression is repeatable. The impredicative features of Iris make this relatively straightforward. First, for repeatability of ghost state programs, we define:

$$\text{repGhost}(e, v) \triangleq \Box(\forall j, K. j \Rightarrow K[e] * \Rightarrow^{\top} j \Rightarrow K[v])$$

That is, $\text{repGhost}(e, v)$ says that for any ghost thread running e in an evaluation context K , we can perform a ghost update to “execute” e to the value v . The persistence modality \Box ensures that this can be done as often as we like. For non-ghost code, we have

$$\text{replmpl}(e, v) \triangleq \{\text{True}\} e \{v'. v = v'\}$$

Just as with repGhost , if $\text{replmpl}(e, v)$ holds, then in the course of a proof we can “run” e and it can only terminate in value v . We do not need \Box here, because it is baked into Hoare triples.

Using these definitions, we define a repeatable form of $e_1 \leq e_2 : P$ as follows:

$$(e_1 \leq^{\text{rep}} e_2 : P) \triangleq (e_1 \leq e_2 : \lambda(v_1, v_2). P(v_1, v_2) * \text{replmpl}(e_1, v_1) * \text{repGhost}(e_2, v_2))$$

This requires that the results of evaluating e_1 and e_2 not only need to be related according to P , but also we must have proofs that the expressions can repeatedly run to those values. Repeatable versions of the key definitions in the logical relation are then obtained by using \leq^{rep} in place of \leq :

$$\llbracket \tau \rightarrow^{\text{rep}} \tau' \rrbracket \triangleq \lambda v_1, v_2. \forall u_1, u_2. \Box(\llbracket \tau \rrbracket(u_1, u_2) * (v_1 \ u_1) \leq^{\text{rep}} (v_2 \ u_2) : \llbracket \tau' \rrbracket)$$

$$(e_1 \leq_{\log}^{\text{rep}} e_2 : \tau) \triangleq (e_1 \leq^{\text{rep}} e_2 : \llbracket \tau \rrbracket)$$

$$\Gamma \models e \leq_{\log}^{\text{rep}} e' : \tau \triangleq \forall v_1, v'_1, \dots, v_n, v'_n.$$

$$\left(\bigstar_{i=1, \dots, n} \llbracket \tau_i \rrbracket(v_i, v'_i) \right) \vdash e[v_1/x_1] \cdots [v_n/x_n] \leq_{\log}^{\text{rep}} e'[v'_1/x_1] \cdots [v'_n/x_n] : \tau$$

In addition to soundness, the resulting logical relation has an extended form of the fundamental property, as follows:

THEOREM 5.3 (FUNDAMENTAL PROPERTY).

- (1) If $\Gamma \vdash e : \tau$ then $\Gamma \models e \leq_{\log} e : \tau$.
- (2) If $\Gamma \vdash^{\text{rep}} e : \tau$ then $\Gamma \models e \leq_{\log}^{\text{rep}} e : \tau$.

The need for later credits. Despite the addition of repeatability to the logical relation, the definition of the logical relation is still problematic if we try to prove $f \leq_{\text{ctx}} \text{memo } f : \tau_1 \rightarrow \tau_2$, the reverse refinement in our memoization example. Let us see where the issue is. We assume that we have a value $f : \tau_1 \rightarrow^{\text{rep}} \tau_2$, where τ_1 is an equality type and we want to show that $f \leq_{\log} \text{memo } f : \tau_1 \rightarrow \tau_2$. By the fundamental lemma, we know that $f \leq_{\log} f : \tau_1 \rightarrow^{\text{rep}} \tau_2$. Proceeding with the proof by unfolding the definitions and introducing universally quantified variables, we need to prove $\{j \Rightarrow K[\text{memo } f]\} f \{v. \exists v'. j \Rightarrow K[v'] * \llbracket \tau_1 \rightarrow \tau_2 \rrbracket(v, v')\}$.

By performing updates to evaluate memo f , we first allocate some ghost reference cell c for storing cached values and obtain $c \mapsto_s \text{none}$. We are left with a value f' , where

$$f' = \lambda x. \mathbf{case} \text{ checkCache } c \mathbf{ of} \text{ some}(v) \Rightarrow v \\ | \text{ none} \Rightarrow \mathbf{let} v = f \ x; c \leftarrow \text{some}(x, v); v$$

and we must prove $\llbracket \tau_1 \rightarrow \tau_2 \rrbracket (f, f')$. Our proof of this must be duplicable (due to \square), yet we will need to use the points-to fact for c when reasoning about the execution of f' , and points-to facts are not duplicable. To resolve this issue, we create the following invariant for the points-to fact of c :

$$\boxed{(\exists y, v, v'. c \mapsto_s \text{some}(y, v') * \text{replmpl}(f \ y, v) * \text{repGhost}(f \ y, v') * \llbracket \tau_1 \rrbracket (y, y) * \llbracket \tau_2 \rrbracket (v, v'))^N \\ \vee (c \mapsto_s \text{none})}$$

This invariant requires that if c contains a cached value, meaning $c \mapsto_s \text{some}(y, v')$ for some y and v' , then there is some v such that $f \ y$ repeatably runs to v on the implementation level and v' on the ghost level, where v and v' are related according to the interpretation of τ_2 .

Assuming this invariant, we must now show that for any arguments x, x' such that $\llbracket \tau_1 \rrbracket (x, x')$, we have $f \ x \leq f' \ x' : \llbracket \tau_2 \rrbracket$. Here, we use that since τ_1 is an equality type, $\llbracket \tau_1 \rrbracket (x, x')$ implies $x = x'$. Thus, unfolding the definitions, that means we must show

$$\{j \Rightarrow K[f' \ x]\} f \ x \{v. \exists v'. j \Rightarrow K[v'] * \llbracket \tau_2 \rrbracket (v, v')\}$$

To execute ghost steps of $f' \ x$ we need access to the points-to assertion for c in the invariant. To do so, we use the feature of Iris's invariants that they can be opened as part of an update:

$$\frac{\text{INVOPENUPD} \\ P * \triangleright R \vdash \equiv^{\mathcal{E} \setminus \mathcal{N}} Q * \triangleright R \quad \mathcal{N} \subseteq \mathcal{E}}{P * \boxed{R}^{\mathcal{N}} \vdash \equiv^{\mathcal{E}} Q}$$

With **INVOPENUPD**, we can open them (guarded by a later), perform some ghost updates, and then close them again—*without* taking a step. (From a concurrency perspective, using the invariant for zero steps is fine, because no other thread can observe it in between.)

In our case, once we open the invariant (and use suitable commuting rules), we have to consider two cases. If we are in the “ $c \mapsto_s \text{none}$ ” branch, there is no issue: both the real code and the ghost code will end up executing $f \ x$. We can update the source to advance to the execution of $f \ x$ and then execute $f \ x$ in both ghost code and implementation. From the definition of $\llbracket \tau_1 \rightarrow^{\text{rep}} \tau_2 \rrbracket$, after this code executes, we get $\text{replmpl}(f \ x, v)$ and $\text{repGhost}(f \ x, v')$ for the returned values, which we store in the invariant (again opening it for zero steps).

For the “ $c \mapsto_s \text{some}(y, v')$ ” branch, we obtain:

$$\exists y, v, v'. c \mapsto_s \text{some}(y, v') * \triangleright \text{replmpl}(f \ y, v) * \triangleright \text{repGhost}(f \ y, v') * \triangleright \llbracket \tau_1 \rrbracket (y, y) * \triangleright \llbracket \tau_2 \rrbracket (v, v')$$

after applying commuting rules and using timelessness of $c \mapsto_s \text{some}(y, v')$. If the cached argument y is not equal to x , the argument is similar to the “ $c \mapsto_s \text{none}$ ” case. The difficult case is when there is a cache hit (*i.e.*, $x = y$). Then, we use the $c \mapsto_s \text{some}(x, v')$ to execute the steps of $f' \ x$ in the ghost program, which will return the cached value to the point where we own $j \Rightarrow K[v']$. Turning to reasoning about $f \ x$ in the implementation (*i.e.*, the Hoare triple), we have $\triangleright \text{replmpl}(f \ x, v)$ from the invariant. Now, we would like to use this to argue that $f \ x$ must return v . But $\text{replmpl}(f \ x, v)$ is not timeless (it is a Hoare triple), so we cannot eliminate the \triangleright and our proof attempt is stuck.⁵

⁵We might try to take a step (by doing a beta reduction of $f \ x$) to remove the later, but then we would have to prove a triple about the expression that arises from taking the step instead of $f \ x$, so we would no longer be able to use $\text{replmpl}(f \ x, v)$.

Adding later credits to the logical relation. We would like to have a later credit to eliminate the later guarding $\text{repl}\text{impl}(f\ x, v)$ in the above proof. To do so, we will change the definition of $\llbracket \tau_1 \rightarrow \tau_2 \rrbracket$ and $\llbracket \tau_1 \rightarrow^{\text{rep}} \tau_2 \rrbracket$ to allow functions to *demand* later credits. This change creates a tension: since functions can now demand credits, to prove the fundamental lemma for the new definition, we need to be able to *provide* them whenever the function is applied to an argument. To resolve this tension we define *receipt pools* (using the time receipt extension depicted in Figure 6). A receipt pool, written $\text{pool}(n)$, where n is a natural number, consists of n different invariants, each containing a time receipt $\boxtimes 1$. This assertion supports the following rules:

$$\begin{array}{c} \text{POOLCREDITS} \\ \frac{\{P * \ell(n+1)\} e_2 \{v, Q\} \quad e_1 \rightarrow_{\text{pure}} e_2}{\{P * \text{pool}(n)\} e_1 \{v, Q\}} \\ \text{POOLEXTEND} \\ \boxtimes 1 * \text{pool}(n) \vdash \boxRightarrow^{\top} \text{pool}(n+1) \end{array}$$

That is, given a time receipt, we can extend an existing pool by 1. And, if we have a $\text{pool}(n)$ we can generate $n+1$ later credits after taking a step.⁶ A receipt pool is duplicable because it consists of multiple invariants, each of which is duplicable.

We then redefine the interpretation of \rightarrow to use receipt pools and later credits:

$$\begin{aligned} \llbracket \tau \rightarrow \tau' \rrbracket \triangleq & \lambda v_1, v_2. \exists n, x_1, f_1, e_1, x_2, f_2, e_2. v_1 = (\mathbf{rec}\ f_1\ x_1 = e_1) * v_2 = (\mathbf{rec}\ f_2\ x_2 = e_2) * \text{pool}(n) \\ & * \forall u_1, u_2. \square (\llbracket \tau \rrbracket (u_1, u_2) * \ell(n+1) * (e_1[u_1/x_1][v_1/f_1]) \leq (e_2[u_2/x_2][v_2/f_2]) : \llbracket \tau' \rrbracket) \end{aligned}$$

Let us break this definition down into pieces. First, we assert v_1 and v_2 are in fact functions. Then, for some existentially quantified n , there must be a $\text{pool}(n)$. Finally, given values u_1 and u_2 related according to $\llbracket \tau \rrbracket$, as well as $\ell(n+1)$, we substitute u_1 and u_2 , and the recursive definitions of the functions into the function bodies. The resulting expressions must be related according to $\llbracket \tau' \rrbracket$. That is, whereas the earlier interpretation of \rightarrow was stated in terms of the *applications* of $v_1\ u_1$ and $v_2\ u_2$, here we consider the terms after the application has been reduced by one step, performing the substitution. The interpretation of $\tau \rightarrow^{\text{rep}} \tau'$ is similar, using \leq^{rep} in place of \leq .

The fundamental property holds with this new definition. The biggest change in proving the fundamental property is the case for the function application typing rule. In that case, given $\llbracket \tau \rightarrow \tau' \rrbracket (v_1, v_2)$ and $\llbracket \tau \rrbracket (u_1, u_2)$, we must prove that $v_1\ u_1 \leq v_2\ u_2 : \llbracket \tau' \rrbracket$. (This case was trivial with the original definition of $\llbracket \tau \rightarrow \tau' \rrbracket$, since the desired conclusion was precisely the definition.) Under the new definition, we have $\text{pool}(n)$ for some n and need $\ell(n+1)$ after reducing the application by 1 step. We obtain this $\ell(n+1)$ by using the rule **POOLCREDITS** for the β -reduction step, and the rest of the case is straightforward.

In addition, we obtain a stronger version of the soundness theorem, where we may now assume $\ell n * \boxtimes n$ when proving two expressions are logically related:

THEOREM 5.4 (SOUNDNESS). *If $(\ell n * \boxtimes n \vdash \Gamma \models e_1 \leq_{\log} e_2 : \tau)$ then $\Gamma \vdash e_1 \leq_{\text{ctx}} e_2 : \tau$*

5.3 Memoization with Later Credits

Returning to our memoization example, we use this extended soundness theorem to be able to start with an initial later credit and time receipt. That is, assuming $f : \tau_1 \rightarrow^{\text{rep}} \tau_2$, where τ_1 is an equality type, we prove $\ell 1 * \boxtimes 1 \vdash f \leq_{\log} \text{memo}\ f : \tau_1 \rightarrow \tau_2$. As before, we apply the fundamental lemma to our assumption about f to get that it is logically related to itself. From the new definition of $\llbracket \tau_1 \rightarrow^{\text{rep}} \tau_2 \rrbracket$, we know that $f = (\mathbf{rec}\ f_0\ z = e)$ for some z, f_0, e , that there is $\text{pool}(n)$ for some existentially quantified n , and that f demands $\ell(n+1)$ to be executed. Using **POOLEXTEND**, we exchange our $\boxtimes 1$ for $\text{pool}(n+1)$.

⁶This rule relies on a modification of **RECEIPTCREDITS**.

As before, we execute the ghost code in memo f to allocate the reference cell c and obtain f' . Our invariant storing the points-to for c is slightly different, storing repeatability facts about the result of substituting values into the body e of f :

$$\boxed{(\exists y, v, v'. c \mapsto_s \text{some}(y, v') * \text{replImpl}(e[y/z][f/f_0], v) * \text{repGhost}(e[y/z][f/f_0], v')) * \llbracket \tau_1 \rrbracket(y, y) * \llbracket \tau_2 \rrbracket(v, v')) \vee (c \mapsto_s \text{none})}^N$$

Now, when proving $\llbracket \tau_1 \rightarrow \tau_2 \rrbracket(f, f')$ we choose the existentially quantified natural number in the interpretation of the arrow type to be $n + 1$, since we have $\text{pool}(n + 1)$. This choice means we get $\ell(n + 2)$ now when reasoning about f and f' applied to some arguments that have been substituted in. The non-cache hit cases proceed as before, except that we have to give up $\ell(n + 1)$ of our $\ell(n + 2)$ to use our assumptions about f . In the case of a cache hit, we use $\ell 1$ to remove the later from replImpl . This replImpl lets us show that the non-memoized code will return the value that is cached in the ghost code version, and thus complete the proof.

6 THE MODEL OF IRIS

In this section, we explain how the later credit mechanism is integrated into the model of Iris. Without later credits, Iris factors into two layers: *a base logic* and *a program logic*. The *base logic*, in short, is a step-indexed logic of bunched implications [O’Hearn and Pym 1999]. What that means is that (1) the base logic is step-indexed (e.g., it has the later modality $\triangleright P$), (2) it has the distinguishing connectives of separation logic (e.g., $P * Q$ and $P \multimap Q$), and (3) it has support for ghost state through updates and resources. Importantly, the base logic does not yet have any notion of programs, state, or Hoare triples. These notions are defined in the *program logic*, which is built on top of the base logic (for a discussion of the definition see [Jung et al. 2018]). The program logic defines a number of connectives that we have already encountered in terms of the base logic: updates with masks, invariants, Hoare triples, and logically atomic triples.

We insert later credits as an additional layer between the base logic and the program logic. That is, we define the later credits mechanism in terms of the base logic without changing Iris’s model of propositions, since the base logic is already expressive enough to define later credits $\mathcal{E}n$ and the later elimination update $\triangleright_e P$ (in §6.1). Equipped with the later credits mechanism, we then *redefine* the program logic of Iris. For the most part, this change involves replacing standard updates with later elimination updates. The non-trivial change to the program logic is the way we alter Hoare triples, which we will explain step-by-step (in §6.2).

6.1 Update Modalities

Technically, Iris has two kinds of update modalities: the *ghost state update* $\dot{\triangleright} P$ (notice the dot!) and the *fancy update* $\mathcal{E}_1 \dot{\triangleright} \mathcal{E}_2 P$. The former enables only manipulation of ghost state $\{\bar{r}\}^y$, whereas the latter enables manipulation of ghost state and additionally invariants \boxed{P}^N . The difference between both updates is often swept under the rug, since $\mathcal{E}_1 \dot{\triangleright} \mathcal{E}_2 P$ generalizes $\dot{\triangleright} P$, meaning $\dot{\triangleright} P \vdash \mathcal{E} P$. However, to explain how later credits fit in, we distinguish between both notions here. The update $\dot{\triangleright} P$ is a primitive of the *base logic*, whereas $\mathcal{E}_1 \dot{\triangleright} \mathcal{E}_2 P$ is a derived notion of the *program logic*.

To integrate later credits, we define two new modalities: $\dot{\triangleright}_e P$ and $\mathcal{E}_1 \dot{\triangleright}_e \mathcal{E}_2 P$. The former is defined in terms of $\dot{\triangleright} P$, and the latter is defined in terms of $\dot{\triangleright}_e P$ (analogous to how $\mathcal{E}_1 \dot{\triangleright} \mathcal{E}_2 P$ is defined in terms of $\dot{\triangleright} P$). We start with the ghost state update (in §6.1.1) and then proceed with the fancy update (in §6.1.2).

6.1.1 Ghost State Updates. The ghost state update $\dot{\triangleright} P$ is one of the central pieces for working with ghost state in Iris. It obeys the following rules:

$$\begin{array}{ccc} \text{UPDRETURN} & \text{UPDBIND} & \text{UPDGHOST} \\ P \vdash \dot{\triangleright} P & (\dot{\triangleright} P) * (P \multimap \dot{\triangleright} Q) \vdash \dot{\triangleright} Q & \frac{r \rightsquigarrow r'}{\{\bar{r}\}^y \vdash \dot{\triangleright} \{\bar{r}'\}^y} \end{array}$$

where $\{\bar{r}\}^y$ is Iris’s ghost state ownership connective, and $r \rightsquigarrow r'$ is the underlying notion of “frame preserving update” for Iris’s resources.

We use this update modality and Iris’s ghost state as the building blocks for later credits. That is, we define the ghost state connectives $\mathcal{E}n \triangleq \boxed{\bar{on}}^{\gamma_{lc}}$ and $\mathcal{E}_\bullet n \triangleq \boxed{\bullet n}^{\gamma_{lc}}$ (for some fixed ghost name γ_{lc}), where the resources are drawn from Iris’s $\text{Auth}(\mathbb{N}, +)$ resource algebra. Moreover, we define the later elimination update (for ghost state) as:

$$\dot{\triangleright}_e P \triangleq \forall n. \mathcal{E}_\bullet n \multimap \dot{\triangleright} ((\mathcal{E}_\bullet n * P) \vee (\exists m < n. \mathcal{E}_\bullet m * \triangleright \dot{\triangleright}_e P))$$

We have discussed the definition of this modality already in the paper in Section 5. In short, the update “ $\dot{\triangleright}$ ” ensures that $\dot{\triangleright}_e P$ allows ghost state updates (i.e., $\dot{\triangleright} P \vdash \dot{\triangleright}_e P$), the later “ \triangleright ” ensures

that $\dot{\Rightarrow}_{le} P$ allows later eliminations (i.e., $\mathcal{E} 1 * \triangleright P \vdash \dot{\Rightarrow}_{le} P$), the ever-decreasing credit supply “ \mathcal{E}, n ” ensures that the number of later eliminations is not unbounded, and the recursive occurrence “ $\dot{\Rightarrow}_{le} P$ ” ensures transitivity (i.e., $\dot{\Rightarrow}_{le} \dot{\Rightarrow}_{le} P \vdash \dot{\Rightarrow}_{le} P$). The recursive definition is solved using Iris’s guarded fixed points [Jung et al. 2018], a version of Banach’s fixed points. Since they are unique and the occurrence is positive, it does not matter which kind of fixed point one uses (i.e., least, greatest, or guarded).

The later elimination ghost state update “ $\dot{\Rightarrow}_{le}$ ” inherits almost all properties of the update “ $\dot{\Rightarrow}$ ”. The only rule that is lost is the elimination of the modality in front of Iris’s so-called *plain* propositions:

$$\dot{\Rightarrow} \blacksquare P \vdash P$$

This elimination rule is rarely used in Iris developments. Its main use case is in the runST work [Timany et al. 2018] (see also Section 5 in the paper).

6.1.2 Fancy Updates. Let us turn to *fancy updates* $\mathcal{E}_1 \dot{\Rightarrow} \mathcal{E}_2 P$. In Iris, these updates are built on top of ghost state updates $\dot{\Rightarrow} P$. Thus, we build them here on top of the later elimination ghost state updates $\dot{\Rightarrow}_{le} P$. As a consequence, they inherit most of the properties⁷ of $\dot{\Rightarrow} P$ and, additionally, the ability to eliminate later modalities.

Formally, Iris’s fancy update is defined as follows (the details do not matter too much for understanding how later credits factor in):

$$\mathcal{E}_1 \dot{\Rightarrow} \mathcal{E}_2 P \triangleq \text{wsat} * [\bar{\mathcal{E}}_1]^{y_{en}} * \dot{\Rightarrow} \diamond (\text{wsat} * [\bar{\mathcal{E}}_2]^{y_{en}} * P)$$

The world satisfaction *wsat* ensures that all currently enabled invariants hold, while the ghost state $[\bar{\mathcal{E}}_1]^{y_{en}}$ is linked up to that and asserts that the invariants \mathcal{E} are currently enabled.

The change in the fancy update is as simple as one would hope for:

$$\mathcal{E}_1 \dot{\Rightarrow} \mathcal{E}_2 P \triangleq \text{wsat} * [\bar{\mathcal{E}}_1]^{y_{en}} * \dot{\Rightarrow}_{le} \diamond (\text{wsat} * [\bar{\mathcal{E}}_2]^{y_{en}} * P)$$

Note that we write $\dot{\Rightarrow}^{\mathcal{E}} P \triangleq \mathcal{E} \dot{\Rightarrow} P$ (and $\dot{\Rightarrow}_{le}^{\mathcal{E}}$ respectively) if both masks are the same. (In other Iris presentations, the mask for a non-mask-changing update is at the bottom (i.e., “ $\dot{\Rightarrow}_{\mathcal{E}}$ ”). Since this position conflicts with the “le” of our later elimination updates, we move it up.)

6.2 Weakest Precondition

Recall the definition of Hoare triples from the paper:

$$\{P\} e \{v. Q\}_{\mathcal{E}} \triangleq \square (P * \text{wp}^{\mathcal{E}} e \{v. Q\})$$

In the following, we discuss the definition of the weakest precondition $\text{wp}^{\mathcal{E}} e \{v. Q\}$. We discuss multiple versions. We start with the traditional version without later credits (in §6.2.1). Then, we discuss how later credits can be integrated into this definition (in §6.2.2). Subsequently, we discuss the extension of Jourdan [2021] to the traditional weakest precondition to allow eliminating an increasing number of later per step (in §6.2.3). Finally, we discuss how later credits can be integrated with this extension (in §6.2.4).

Note that in all of the definitions, we will ignore Iris’s support for *prophecy variables*. The integration of later credits is orthogonal to this feature, so we omit them here.

⁷Analogously to the ghost state update, the later elimination update also does not enjoy some of the plain rules (see Iris Coq code).

6.2.1 Weakest Precondition without Later Credits. Compared to the simplified version of the weakest precondition shown in Section 5 of the paper, the full weakest precondition supports concurrency and uses *fancy updates* $\varepsilon_1 \Vdash^{\varepsilon_2}$ to support invariants:

$$\begin{aligned}
 \text{wp}^{\mathcal{E}} e \{v. Q\} &\triangleq \Vdash^{\mathcal{E}} Q[e/v] && \text{if } e \in \text{Val} \\
 \text{wp}^{\mathcal{E}} e \{v. Q\} &\triangleq \forall \sigma, n_t. S(\sigma, n_t) * \varepsilon \Vdash^{\emptyset} \text{red}(e, \sigma) * && \text{if } e \notin \text{Val} \\
 &(\forall e', \sigma', \vec{e}. (e, \sigma) \rightarrow (e', \sigma', \vec{e}) * \emptyset \Vdash^{\emptyset} \triangleright \emptyset \Vdash^{\mathcal{E}} \\
 &S(\sigma', |\vec{e}| + n_t) * \text{wp}^{\mathcal{E}} e' \{v. Q\} * \\
 &\text{*}_{e_f \in \vec{e}} \text{wp}^{\top} e_f \{v. \text{True}\})
 \end{aligned}$$

To support invariants, the weakest precondition is parameterized by the mask \mathcal{E} , describing the set of invariants that are currently active. In the step case, the weakest precondition allows us to open all invariants (and update ghost state) to justify the step, by showing a mask-changing update “ $\varepsilon \Vdash^{\emptyset}$ ” that deactivates all invariants. After the step, the definition requires us to establish them again with an update “ $\emptyset \Vdash^{\mathcal{E}}$ ”. The later \triangleright in between enables us to eliminate a later from our assumptions when taking a step.

To integrate concurrency, the operational semantics provides a list \vec{e} of forked-off expressions in the step case. For each of the forked-off expressions $e_f \in \vec{e}$, the definition requires us to prove a weakest precondition with a trivial postcondition. The state interpretation is additionally parameterized by the number of threads n_t .

6.2.2 Weakest Precondition with Later Credits. For the extension with later credits, two changes are necessary: First, we use the fancy updates that support later elimination “ $\varepsilon_1 \Vdash_{\text{le}}^{\varepsilon_2}$ ”. Second, the definition of the weakest precondition is changed to grant access to one later credit in the step case.

$$\begin{aligned}
 \text{wp}^{\mathcal{E}} e \{v. Q\} &\triangleq \Vdash_{\text{le}}^{\mathcal{E}} Q[e/v] && \text{if } e \in \text{Val} \\
 \text{wp}^{\mathcal{E}} e \{v. Q\} &\triangleq \forall \sigma, n_t. S(\sigma, n_t) * \varepsilon \Vdash_{\text{le}}^{\emptyset} \text{red}(e, \sigma) * && \text{if } e \notin \text{Val} \\
 &(\forall e', \sigma', \vec{e}. (e, \sigma) \rightarrow (e', \sigma', \vec{e}) * \text{\textcircled{1}} * \emptyset \Vdash_{\text{le}}^{\emptyset} \triangleright \emptyset \Vdash_{\text{le}}^{\mathcal{E}} \\
 &S(\sigma', |\vec{e}| + n_t) * \text{wp}^{\mathcal{E}} e' \{v. Q\} * \\
 &\text{*}_{e_f \in \vec{e}} \text{wp}^{\top} e_f \{v. \text{True}\})
 \end{aligned}$$

Note that we keep the later that was there previously. We do so to simplify backwards compatibility. That is, without any trouble, this definition validates the rules in [Figure 2](#) and in [Figure 5](#). Thus, adapting for example Iris’s proof mode [[Krebbers et al. 2017b](#)] is straightforward. In the adequacy proof, dealing with this additional later is simple: we know $\triangleright P \vdash \text{\textcircled{1}} * \Vdash_{\text{le}} P$. Thus, in the adequacy proof, we can replace the later with just another “ $\text{\textcircled{1}} * \Vdash_{\text{le}}$ ” and simply allocate $\text{\textcircled{2}}n$ initially.

6.2.3 Weakest Precondition with Increasing Laters without Later Credits. Recently, Iris’s weakest precondition has been extended to support an increasing number of later eliminations per step, depending on how many program steps have already happened [[Jourdan 2021](#)]. For this change, the state interpretation is extended with a parameter n_s determining the number of steps that have already been taken, which is increased with each step. This number n_s is then mapped to the the number of laters that can be eliminated in a step through a function $\Xi : \mathbb{N} \rightarrow \mathbb{N}$.⁸ To allow updating

⁸The definition of the weakest precondition adds 1 to this, to ensure that at least one later can be eliminated in each step (and to make the definition contractive).

ghost state interleaved with later elimination, the weakest precondition interleaves fancy updates and lateres (changes from §6.2.1 highlighted in blue):

$$\begin{aligned}
 \text{wp}^\varepsilon e \{v. Q\} &\triangleq \text{wp}^\varepsilon Q[e/v] && \text{if } e \in \text{Val} \\
 \text{wp}^\varepsilon e \{v. Q\} &\triangleq \forall \sigma, n_s, n_t. S(\sigma, n_s, n_t) \text{-}^* \text{wp}^\varepsilon \text{red}(e, \sigma) * && \text{if } e \notin \text{Val} \\
 &(\forall e', \sigma', \vec{e}. (e, \sigma) \rightarrow (e', \sigma', \vec{e}) \text{-}^* (\text{wp}^\varepsilon \triangleright \text{wp}^\varepsilon)^{1+\Xi(n_s)} \text{wp}^\varepsilon) && \\
 &S(\sigma', 1 + n_s, |\vec{e}| + n_t) * \text{wp}^\varepsilon e' \{v. Q\} * && \\
 &\text{wp}^\top e_f \{v. \text{True}\} && \\
 &_{e_f \in \vec{e}} &&
 \end{aligned}$$

6.2.4 Weakest Precondition with Increasing Later Credits. We describe how the definition needs to be changed to obtain a flexible number of later credits per step. Instead of putting an iterated alternation of fancy updates and lateres into the definition, we get $1 + \Xi(n_s)$ credits per step. The definition we obtain is (changes from §6.2.2 highlighted in blue):

$$\begin{aligned}
 \text{wp}^\varepsilon e \{v. Q\} &\triangleq \text{wp}_{\text{le}}^\varepsilon Q[e/v] && \text{if } e \in \text{Val} \\
 \text{wp}^\varepsilon e \{v. Q\} &\triangleq \forall \sigma, n_s, n_t. S(\sigma, n_s, n_t) \text{-}^* \text{wp}_{\text{le}}^\varepsilon \text{red}(e, \sigma) * && \text{if } e \notin \text{Val} \\
 &(\forall e', \sigma', \vec{e}. (e, \sigma) \rightarrow (e', \sigma', \vec{e}) \text{-}^* \text{le}(1 + \Xi(n_s)) \text{-}^* \text{wp}_{\text{le}}^\varepsilon \triangleright \text{wp}_{\text{le}}^\varepsilon) && \\
 &S(\sigma', 1 + n_s, |\vec{e}| + n_t) * \text{wp}^\varepsilon e' \{v. Q\} * && \\
 &\text{wp}^\top e_f \{v. \text{True}\} && \\
 &_{e_f \in \vec{e}} &&
 \end{aligned}$$

The number of steps n_s is connected to the time receipts Ξn in the state interpretation S . That is, the time receipts are elements of the resource algebra $\text{Auth}(\mathbb{N}, +)$. The authoritative element $\Xi n_s \triangleq [\bullet n_s]^{y_{\text{tr}}}$ resides in the state interpretation, and the receipts $\Xi n \triangleq [\bar{n}]^{y_{\text{tr}}}$ are the fragments.

6.3 Unbounded Credits are Unsound with Finite Step-indexing

In Section 6 of the paper, we claim that the rule **CREDITSPost** that gives us an arbitrary number of credits in the postcondition is unsound under finite step-indexing:

$$\begin{array}{c}
 \text{CREDITSPost} \\
 \frac{\{P\} e \{v. Q\} \quad e \notin \text{Val}}{\{P\} e \{v. Q * \text{le} n\}}
 \end{array}$$

To prove this, we show that $\{\text{True}\} \text{Skip } \{ _ . \text{False} \}$, which by Iris's adequacy theorem entails False at the meta-level.

PROOF. We use the well-known fact that $\vdash \exists n. \triangleright^n \text{False}$ holds in Iris (the proof goes by Löb induction). Thus, we show $\triangleright^n \text{False} \vdash \{\text{True}\} \text{Skip } \{ _ . \text{False} \}$ for some n .

Moreover, we claim that $(\triangleright^n \text{False}) * \text{le} n \vdash \text{wp}_{\text{le}}^\varepsilon \text{False}$: the proof is by induction on n . In the base case, we are done by assumption. In the inductive step, we eliminate one later, using one of the credits we have, by the rule **LEUPDLATER**, before using the inductive hypothesis:

$$\begin{array}{c}
 \text{LEUPDLATER} \\
 \text{le} 1 * \triangleright P \vdash \text{wp}_{\text{le}}^\varepsilon P
 \end{array}$$

Thus, it suffices to show that $\triangleright^n \text{False} \vdash \{\text{True}\} \text{Skip } \{ _ . (\triangleright^n \text{False}) * \text{le} n \}$ by the rule of consequence. We frame $\triangleright^n \text{False}$ and are left to prove $\vdash \{\text{True}\} \text{Skip } \{ _ . \text{le} n \}$. We apply **CREDITSPost** and it remains to show $\vdash \{\text{True}\} \text{Skip } \{ _ . \text{True} \}$. This holds trivially by executing the **Skip**. \square

REFERENCES

- Amal Ahmed. 2004. *Semantics of types for mutable state*. Ph. D. Dissertation. Princeton University.
- Lars Birkedal, Bernhard Reus, Jan Schwinghammer, Kristian Støvring, Jacob Thamsborg, and Hongseok Yang. 2011. Step-indexed Kripke models over recursive worlds. In *POPL*. 119–132. <https://doi.org/10.1145/1926385.1926401>
- Dan Frumin, Robbert Krebbers, and Lars Birkedal. 2018. ReLoC: A mechanised relational logic for fine-grained concurrency. In *LICS*. 442–451. <https://doi.org/10.1145/3209108.3209174>
- Dan Frumin, Robbert Krebbers, and Lars Birkedal. 2021. ReLoC Reloaded: A Mechanized Relational Logic for Fine-Grained Concurrency and Logical Atomicity. *LMCS* 17, 3 (2021). [https://doi.org/10.46298/lmcs-17\(3:9\)2021](https://doi.org/10.46298/lmcs-17(3:9)2021)
- Jacques-Henri Jourdan. 2021. Flexible number of logical steps per physical step. https://gitlab.mpi-sws.org/iris/iris/-/merge_requests/595 Iris merge request 595.
- Ralf Jung, Robbert Krebbers, Jacques-Henri Jourdan, Ales Bizjak, Lars Birkedal, and Derek Dreyer. 2018. Iris from the ground up: A modular foundation for higher-order concurrent separation logic. *JFP* 28 (2018), e20. <https://doi.org/10.1017/S0956796818000151>
- Robbert Krebbers, Ralf Jung, Ales Bizjak, Jacques-Henri Jourdan, Derek Dreyer, and Lars Birkedal. 2017a. The essence of higher-order concurrent separation logic. In *ESOP (LNCS, Vol. 10201)*. 696–723. https://doi.org/10.1007/978-3-662-54434-1_26
- Robbert Krebbers, Amin Timany, and Lars Birkedal. 2017b. Interactive proofs in higher-order concurrent separation logic. In *POPL*. 205–217. <https://doi.org/10.1145/3093333.3009855>
- Morten Krogh-Jespersen, Kasper Svendsen, and Lars Birkedal. 2017. A relational model of types-and-effects in higher-order concurrent separation logic. In *POPL*. 218–231. <https://doi.org/10.1145/3093333.3009877>
- Peter W. O’Hearn and David J. Pym. 1999. The Logic of Bunched Implications. *Bulletin of Symbolic Logic* 5, 2 (June 1999), 215–244. <https://doi.org/10.2307/421090>
- Simon Spies, Lennard Gäher, Daniel Gratzer, Joseph Tassarotti, Robbert Krebbers, Derek Dreyer, and Lars Birkedal. 2021. Transfinite Iris: Resolving an existential dilemma of step-indexed separation logic. In *PLDI*. 80–95. <https://doi.org/10.1145/3453483.3454031>
- Kasper Svendsen, Filip Sieczkowski, and Lars Birkedal. 2016. Transfinite step-indexing: Decoupling concrete and logical steps. In *ESOP (LNCS, Vol. 9632)*. 727–751. https://doi.org/10.1007/978-3-662-49498-1_28
- Joseph Tassarotti, Ralf Jung, and Robert Harper. 2017. A higher-order logic for concurrent termination-preserving refinement. In *ESOP (LNCS, Vol. 10201)*. 909–936. https://doi.org/10.1007/978-3-662-54434-1_34
- Amin Timany, Léo Stefanescu, Morten Krogh-Jespersen, and Lars Birkedal. 2018. A logical relation for monadic encapsulation of state: proving contextual equivalences in the presence of runST. *PACMPL* 2, POPL (2018), 64:1–64:28. <https://doi.org/10.1145/3158152>
- Aaron Turon, Derek Dreyer, and Lars Birkedal. 2013. Unifying refinement and Hoare-style reasoning in a logic for higher-order concurrency. In *ICFP*. 377–390. <https://doi.org/10.1145/2500365.2500600>