

# A separation logic for a promising semantics (Technical appendix)

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This is the technical appendix accompanying the article “A separation logic for a promising semantics”. It contains a soundness proof for the SLR logic. The programming language and logic presented in this appendix extends the programming language and logic presented in the paper with plain accesses. The programming language has further been extended with compare-and-swap operations and the logic has been proven sound under the additional memory reduction rules required to support compare-and-swap.

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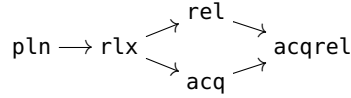
# 1 Operational semantics

**Programming Language** Let  $Loc$  be the set of memory locations,  $Val$  be the set of values,  $Tid$  be the set of thread identifiers, and  $Reg$  be the set of register identifiers.

We assume  $Loc$  is the union of two disjoint sets of plain and non-plain memory locations,  $PLoc$  and  $NPLoc$ , respectively. Throughout the rest of the document we implicitly assume that all plain accesses are performed on plain locations and all non-plain accesses on non-plain locations.

$$\begin{array}{l}
 e \in Expr ::= r \mid e_1 + e_2 \mid \dots \\
 s \in Stm ::= r = e \\
 \quad \mid [e]_{\text{pln}} := r \mid [e]_{\text{rlx}} := r \mid [e]_{\text{rel}} := r \\
 \quad \mid r := [e]_{\text{pln}} \mid r := [e]_{\text{rlx}} \mid r := [e]_{\text{acq}} \\
 \quad \mid r_1, r_2 = \text{cas}_{\text{rlx}}(e, r_3, r_4) \\
 \quad \mid r_1, r_2 = \text{cas}_{\text{rel}}(e, r_3, r_4) \\
 \quad \mid r_1, r_2 = \text{cas}_{\text{acq}}(e, r_3, r_4) \\
 \quad \mid r_1, r_2 = \text{cas}_{\text{acqrel}}(e, r_3, r_4) \\
 \quad \mid \text{if } e \text{ then } s_1 \text{ else } s_2 \\
 \quad \mid \text{while } e \text{ do } s \\
 \quad \mid s_1; s_2 \mid \text{skip}
 \end{array}$$

The modes for accesses are partially ordered by  $\sqsubset$  as follows:



*Thread store*

$$\mu \in TStore = Reg \rightarrow Val$$

A *thread store*,  $\mu$ , is a function assigning a value to every register identifiers.

*Thread local state*

$$\sigma \in TLState = TStore \times Stm$$

A *thread local state*,  $\sigma = (\mu, s)$ , consists of a thread store and a statement (to be executed by the thread).

*Memory actions*

$$a \in MO$$

Transitions are labelled with *memory actions*:

$$a \in MO ::= \tau \mid W_o x v \mid R_o x v \mid U_o x v_1 v_2$$

$\tau$ , which stands for a silent action (as opposed to a proper memory action), is elided.

Thread-local state reduction

$$\sigma \xrightarrow{a} \sigma'$$

$$\begin{array}{c} \frac{\llbracket e \rrbracket(\mu) = \top}{(\mu, \mathbf{if } e \mathbf{ then } s_1 \mathbf{ else } s_2) \longrightarrow (\mu, s_1)} \quad \frac{\llbracket e \rrbracket(\mu) = \perp}{(\mu, \mathbf{if } e \mathbf{ then } s_1 \mathbf{ else } s_2) \longrightarrow (\mu, s_2)} \quad \frac{\llbracket e \rrbracket(\mu) = \perp}{(\mu, \mathbf{while } e \mathbf{ do } s) \longrightarrow (\mu, \mathbf{skip})} \\ \\ \frac{\llbracket e \rrbracket(\mu) = \top}{(\mu, \mathbf{while } e \mathbf{ do } s) \longrightarrow (\mu, s; \mathbf{while } e \mathbf{ do } s)} \quad \frac{}{(\mu, \mathbf{skip}; s) \longrightarrow (\mu, s)} \quad \frac{(\mu, s_1) \xrightarrow{a} (\mu', s'_1)}{(\mu, s_1; s_2) \xrightarrow{a} (\mu', s'_1; s_2)} \\ \\ \frac{\llbracket e \rrbracket(\mu) = v}{(\mu, r = e) \longrightarrow (\mu[r \mapsto v], \mathbf{skip})} \quad (\mu, [x]_o := e) \xrightarrow{W_o x \mu(e)} (\mu, \mathbf{skip}) \quad (\mu, r := [x]_o) \xrightarrow{R_o x v} (\mu[r \mapsto v], \mathbf{skip}) \\ \\ o = \mathbf{rlx} \vee o = \mathbf{rel} \Rightarrow o' = \mathbf{rlx} \quad o = \mathbf{acq} \vee o = \mathbf{acqrel} \Rightarrow o' = \mathbf{acq} \\ \\ \frac{}{(\mu, r_3, r_4 = \mathbf{cas}_o(x, e_1, e_2)) \xrightarrow{R_{o'} x v} (\mu[r_3 \mapsto 0, r_4 \mapsto v], \mathbf{skip})} \\ \\ (\mu, r_3, r_4 = \mathbf{cas}_o(x, e_1, e_2)) \xrightarrow{U_o x \mu(e_1) \mu(e_2)} (\mu[r_3 \mapsto 1, r_4 \mapsto v_1], \mathbf{skip}) \end{array}$$

Timestamp

$$t \in \mathit{Time}$$

$\mathit{Time}$  is an infinite set of *timestamps*, densely totally ordered by  $\leq$ , with 0 being the minimum element.

Timemap

$$T \in \mathit{Timemap} = \mathit{Loc} \rightarrow \mathit{Time}$$

A *timemap* is a function  $T : \mathit{Loc} \rightarrow \mathit{Time}$ . The order  $\leq$  is extended pointwise to timemaps.

View

$$V \in \mathit{View} \subseteq \mathit{Timemap} \times \mathit{Timemap}$$

A *view* is a pair  $V = \langle T_{\mathbf{pln}}, T_{\mathbf{rlx}} \rangle$  of timemaps satisfying  $T_{\mathbf{pln}} \leq T_{\mathbf{rlx}}$ . We denote by  $V.\mathbf{pln}$  and  $V.\mathbf{rlx}$  the components of  $V$ .

**Additional notations for timemaps and views**  $\perp$  and  $\sqcup$  denote the natural bottom elements and join operations for timemaps and for views (pointwise extensions of the initial timestamp 0 and the  $\sqcup$ —i.e., max—operation on timestamps);  $\{x@t\}$  denotes the timemap assigning  $t$  to  $x$  and 0 to other locations. We write  $T_\perp$  for the least timemap that maps every location to 0 and  $V_\perp$  for the view  $\langle T_\perp, T_\perp \rangle$ .

Message

$$m \in \mathit{Msg} \subseteq \mathit{Loc} \times \mathit{Val} \times \mathit{Time} \times \mathit{Time} \times \mathit{View} \times \mathit{Tid} \times \mathit{Mod}$$

A *message* is a tuple  $m = \langle x :_i^o v, R@(\mathbf{f}, \mathbf{t}) \rangle$ , where  $x \in \mathit{Loc}$ ,  $v \in \mathit{Val}$ ,  $\mathbf{f}, \mathbf{t} \in \mathit{Time}$ ,  $R \in \mathit{View}$ ,  $i \in \mathit{Tid}$ , and  $o \in \mathit{Mod}$ , such that (i)  $\mathbf{f} < \mathbf{t}$  or  $\mathbf{f} = \mathbf{t} = 0$ ; (ii)  $R.\mathbf{rlx}(x) = R.\mathbf{pln}(x) = \mathbf{t}$ ; and (iii)  $o = \mathbf{pln}$  iff  $x \in \mathit{PLoc}$ . We denote by  $m.\mathbf{loc}$ ,  $m.\mathbf{val}$ ,  $m.\mathbf{from}$ ,  $m.\mathbf{time}$ ,  $m.\mathbf{view}$ , and  $m.\mathbf{mod}$  the components of  $m$ . Two messages  $m = \langle x :_i^o v, R@(\mathbf{f}, \mathbf{t}) \rangle$  and  $m' = \langle x' :_j^{o'} v', R'@(\mathbf{f}', \mathbf{t}') \rangle$  are called *disjoint*, denoted  $m \# m'$ , if either  $x \neq x'$ ,  $\mathbf{t} \leq \mathbf{f}' < \mathbf{t}$ , or  $\mathbf{t}' \leq \mathbf{f} < \mathbf{t}$ .

Memory

$$M \in \mathit{Mem} \subseteq \mathcal{P}_{\mathbf{fin}}(\mathit{Msg})$$

A *memory* is a (nonempty) pairwise disjoint finite set of messages. A memory  $M$  supports the following insertions of a message  $m = \langle x :_i^o v, R@(\mathbf{f}, \mathbf{t}) \rangle$ :

- The *additive insertion*, denoted by  $M \stackrel{\oplus}{\leftarrow} m$ , is only defined if  $\{m\} \# M$ , in which case it is given by  $\{m\} \cup M$ .
- The *splitting insertion*, denoted by  $M \stackrel{\oplus}{\leftarrow} m$ , is only defined if there exists  $m' = \langle x :_i^o v', R'@(\mathbf{f}, \mathbf{t}') \rangle$  with  $\mathbf{t} < \mathbf{t}'$  in  $M$ , in which case it is given by  $M \setminus \{m'\} \cup \{m, \langle x :_i^o v', R'@(\mathbf{t}, \mathbf{t}') \rangle\}$ .

- The *lowering insertion*, denoted by  $M \stackrel{\downarrow}{\hookrightarrow} m$ , is only defined if there exists  $m' = \langle x \stackrel{?}{:}_i v, R' @ (f, t) \rangle$  with  $R \leq R'$  in  $M$ , in which case it is given by  $M \setminus \{m'\} \cup \{m\}$ .

Notation for restricting a memory:

$$\begin{aligned}
M(i) &\stackrel{def}{=} \{m \in M \mid m.\text{tid} = i\} \\
M(x) &\stackrel{def}{=} \{m \in M \mid m.\text{loc} = x\} \\
M(i, x) &\stackrel{def}{=} M(i) \cap M(x) \\
M(\text{rel}) &\stackrel{def}{=} \{m \in M \mid m.\text{mod} = \text{rel}\} \\
M(\text{rlx}) &\stackrel{def}{=} \{m \in M \mid m.\text{mod} = \text{rlx}\} \\
M(\text{rel}, x) &\stackrel{def}{=} M(\text{rel}) \cap M(x) \\
M(\text{rlx}, x) &\stackrel{def}{=} M(\text{rlx}) \cap M(x)
\end{aligned}$$

Memory reduction

$$\langle M, P \rangle \xrightarrow{m} \langle M', P' \rangle$$

MEMORY: NEW

$$\frac{}{\langle M, P \rangle \xrightarrow{m} \langle M \stackrel{\downarrow}{\hookrightarrow} m, P \rangle}$$

MEMORY: FULFILL

$$\frac{\stackrel{\downarrow}{\hookrightarrow} \in \{\stackrel{s}{\hookrightarrow}, \stackrel{\downarrow}{\hookrightarrow}\} \quad P' = P \stackrel{\downarrow}{\hookrightarrow} m \quad M' = M \stackrel{\downarrow}{\hookrightarrow} m}{\langle M, P \rangle \xrightarrow{m} \langle M', P' \setminus \{m\} \rangle}$$

**Closed memory** Given a timemap  $T$  and a memory  $M$ , we write  $T \in M$  if, for every  $x \in \text{Loc}$ , we have  $T(x) = m.\text{time}$  for some  $m \in M$  with  $m.\text{loc} = x$ . For a view  $V$ , we write  $V \in M$  if  $T \in M$  for each component timemap  $T$  of  $V$ . A memory  $M$  is *closed* if  $m.\text{view} \in M$  for every  $m \in M$ .

**Future memory** For memories  $M, M'$ , we write  $M \rightarrow M'$  if  $M' \in \{M \stackrel{\downarrow}{\hookrightarrow} m, M \stackrel{s}{\hookrightarrow} m, M \stackrel{\downarrow}{\hookrightarrow} m\}$  for some message  $m$ , and  $M'$  is closed. We say that  $M'$  is a *future memory* of  $M$  w.r.t. a memory  $P$ , if  $P \subseteq M'$  and  $M \rightarrow^* M'$ .

Global Configuration

$$gconf \in GConf = Mem \times Mem$$

A *global configuration* is a tuple  $gconf = \langle M, P \rangle$ , where  $M$  is a memory and  $P \subseteq M$  is a memory called *promise memory*. We denote by  $gconf.M$  and  $gconf.P$  the components of  $gconf$ . We write  $gconf_{\perp}$  to denote the empty global configuration,  $\langle \emptyset, \emptyset \rangle$ .

Thread state

$$TS \in TLState \times View$$

A *thread state* is a pair  $TS = \langle \sigma, V \rangle$ , where  $\sigma$  is a thread local state and  $V$  is a view. We denote by  $TS.\sigma$  and  $TS.V$  the components of  $TS$ .

Thread configuration

$$\langle TS, \langle M, P \rangle \rangle$$

A *thread configuration* is a tuple  $\langle TS, \langle M, P \rangle \rangle$ , where  $TS$  is a thread state, and  $\langle M, P \rangle$  is a global configuration.

Transition mode

$$\beta \in TM$$

$$\beta \in TM ::= NP \mid \text{promise}$$

Given a  $\beta$ -labelled relation  $\rightarrow$ ,  $a \rightarrow b$  stands for  $\exists \beta. a \stackrel{\beta}{\rightarrow} b$ .

Thread configuration reduction

$$\langle TS, \langle M, P \rangle \rangle \xrightarrow{\beta}_i \langle TS', \langle M', P' \rangle \rangle$$

$$\begin{array}{c} \text{THREAD: READ} \\ \sigma \xrightarrow{R_o x v} \sigma' \quad m = \langle x :_j^{o_r} v, R@(\_, t) \rangle \in M \\ o = \text{pln} \implies V.\text{pln}(x) \leq t \wedge V' = V \sqcup [\text{pln} : \perp, \text{rlx} : \{x@t\}] \\ o = \text{rlx} \implies V.\text{rlx}(x) \leq t \wedge V' = V \sqcup [\text{pln} : \{x@t\}, \text{rlx} : \{x@t\}] \\ o = \text{acq} \implies V.\text{rlx}(x) \leq t \wedge V' = V \sqcup [\text{pln} : \{x@t\}, \text{rlx} : \{x@t\}] \sqcup R \end{array}$$

$$\frac{\text{THREAD: SILENT} \quad \sigma \longrightarrow \sigma'}{\langle \langle \sigma, V \rangle, \langle M, P \rangle \rangle \xrightarrow{\text{NP}}_i \langle \langle \sigma', V \rangle, \langle M, P \rangle \rangle} \quad \frac{}{\langle \langle \sigma, V \rangle, \langle M, P \rangle \rangle \xrightarrow{\text{NP}}_i \langle \langle \sigma', V' \rangle, \langle M, P \rangle \rangle}$$

$$\begin{array}{c} \text{THREAD: WRITE} \\ \sigma \xrightarrow{W_o x v} \sigma' \quad \langle M, P \rangle \xrightarrow{\langle x :_i^{o_v}, R@(\_, t) \rangle} \langle M', P' \rangle \quad V.\text{rlx}(x) < t \\ V' = V \sqcup [\text{pln} : \{x@t\}, \text{rlx} : \{x@t\}] \\ o \sqsubseteq \text{rlx} \implies R = [\text{pln} : \{x@t\}, \text{rlx} : \{x@t\}] \\ o = \text{rel} \implies R = V' \wedge P(i, x) = \emptyset \end{array}$$

$$\frac{}{\langle \langle \sigma, V \rangle, \langle M, P \rangle \rangle \xrightarrow{\text{NP}}_i \langle \langle \sigma', V' \rangle, \langle M', P' \rangle \rangle}$$

$$\begin{array}{c} \text{THREAD: UPDATE} \\ \sigma \xrightarrow{U_o x v_r} \sigma' \quad m = \langle x :_j^{o_r} v_r, R_r@(\_, f) \rangle \in M \\ \langle M, P \rangle \xrightarrow{\langle x :_i^{o_v}, R_w@(\_, t) \rangle} \langle M', P' \rangle \quad V.\text{rlx}(x) \leq f \\ o \sqsubseteq \text{rel} \implies V' = V \sqcup [\text{pln} : \{x@t\}, \text{rlx} : \{x@t\}] \\ o \sqsupseteq \text{acq} \implies V' = V \sqcup [\text{pln} : \{x@t\}, \text{rlx} : \{x@t\}] \sqcup R_r \\ o \sqsubseteq \text{acq} \implies R_w = R_r \sqcup [\text{pln} : \{x@t\}, \text{rlx} : \{x@t\}] \\ o \sqsupseteq \text{rel} \implies R_w = V \sqcup [\text{pln} : \{x@t\}, \text{rlx} : \{x@t\}] \sqcup R_r \\ o \sqsupseteq \text{rel} \implies P(i, x) = \emptyset \end{array}$$

$$\frac{}{\langle \langle \sigma, V \rangle, \langle M, P \rangle \rangle \xrightarrow{\text{NP}}_i \langle \langle \sigma', V' \rangle, \langle M', P' \rangle \rangle}$$

$$\begin{array}{c} \text{THREAD: PROMISE} \\ m = \langle x :_i^o \_, R@(\_, t) \rangle \quad \leftarrow \in \{ \leftarrow^{\Delta}, \leftarrow^{\text{S}}, \leftarrow^{\perp} \} \quad P' = P \leftarrow m \quad M' = M \leftarrow m \quad R = [\text{pln} : \{x@t\}, \text{rlx} : \{x@t\}] \end{array}$$

$$\frac{}{\langle TS, \langle M, P \rangle \rangle \xrightarrow{\text{promise}}_i \langle TS, \langle M', P' \rangle \rangle}$$

**Consistency** A thread configuration  $\langle TS, \langle M, P \rangle \rangle$  is called *consistent* w.r.t.  $i \in \text{ThreadId}$  if for every future memory  $M_{\text{future}}$  of  $M$  w.r.t.  $P(i)$  we have  $\langle TS, \langle M_{\text{future}}, P \rangle \rangle \xrightarrow{\text{NP}^*}_i \langle TS', \langle M', P' \rangle \rangle$  for some  $TS', M', P'$  such that  $P'(i) = \emptyset$ .

Thread certified reduction

$$\langle TS, \langle M, P \rangle \rangle \xRightarrow{\beta}_i \langle TS', \langle M', P' \rangle \rangle$$

$$\frac{\langle TS, \langle M, P \rangle \rangle \xrightarrow{\text{promise}}_i \langle TS, \langle M', P' \rangle \rangle \quad \langle TS, \langle M', P' \rangle \rangle \text{ is consistent w.r.t. } i}{\langle TS, \langle M, P \rangle \rangle \xrightarrow{\text{promise}}_i \langle TS, \langle M', P' \rangle \rangle} \quad \frac{\langle TS, \langle M, P \rangle \rangle \xrightarrow{\text{NP}^+}_i \langle TS', \langle M', P' \rangle \rangle \quad \langle TS', \langle M', P' \rangle \rangle \text{ is consistent w.r.t. } i}{\langle TS, \langle M, P \rangle \rangle \xrightarrow{\text{NP}}_i \langle TS', \langle M', P' \rangle \rangle}$$

Machine state

$$\mathbf{MS} = \langle TS, \langle M, P \rangle \rangle$$

A *machine state* is a tuple  $\mathbf{MS} = \langle TS, \langle M, P \rangle \rangle$ , where  $TS$  is a function assigning a thread state to every thread, and  $\langle M, P \rangle$  is a global configuration. The initial state  $\mathbf{MS}^0$  (for a given program) consists of the function  $TS^0$  mapping each thread  $i$  to its initial state  $\langle \sigma_i^0, \perp \rangle$  (where  $\sigma_i^0$  is the thread's initial local state, and  $\perp$  is the zero view (all timestamps in timemaps are 0), the empty set of promises; the initial memory  $M^0$  consisting of one message  $\langle x :_0^{\text{rlx}} 0, \perp@(\_, 0) \rangle$  for each location  $x$ .

Machine reduction

$$\boxed{\langle \mathcal{TS}, \langle M, P \rangle \rangle \xRightarrow{\beta}_i \langle \mathcal{TS}', \langle M', P' \rangle \rangle}$$

$$\frac{\langle \mathcal{TS}(i), \langle M, P \rangle \rangle \xRightarrow{\text{promise}}_i \langle \mathcal{TS}(i), \langle M', P' \rangle \rangle}{\langle \mathcal{TS}, \langle M, P \rangle \rangle \xRightarrow{\text{promise}}_i \langle \mathcal{TS}, \langle M', P' \rangle \rangle} \quad \frac{\langle \mathcal{TS}(i), \langle M, P \rangle \rangle \xRightarrow{\text{NP}}_i \langle \mathcal{TS}', \langle M', P' \rangle \rangle}{\langle \mathcal{TS}, \langle M, P \rangle \rangle \xRightarrow{\text{NP}}_i \langle \mathcal{TS}[i \mapsto \mathcal{TS}'], \langle M', P' \rangle \rangle}$$

Promise certifications satisfy the following basic property: making a promise and then certifying it leads to a state that is reachable without performing any promises.

**Lemma 1.** Whenever  $\langle \langle \sigma, V \rangle, \langle M, P \rangle \rangle \xrightarrow{\text{promise}}_i \langle \langle \sigma, V \rangle, \langle M', P' \rangle \rangle \xrightarrow{\text{NP}^*}_i \langle \langle \sigma', V' \rangle, \langle M'', P'' \rangle \rangle$  and  $P''(i) = \emptyset$ , then  $\langle \langle \sigma, V \rangle, \langle M, P \rangle \rangle \xrightarrow{\text{NP}^*}_i \langle \langle \sigma', V' \rangle, \langle M'', P'' \rangle \rangle$ .

## 1.1 Simplified operational semantics

Memory reduction

$$\boxed{\langle M, P \rangle \xrightarrow{m} \langle M', P' \rangle}$$

$$\frac{}{\langle M, P \rangle \xrightarrow{m} \langle M \dot{\leftrightarrow} m, P \rangle} \quad \frac{m \in P}{\langle M, P \rangle \xrightarrow{m} \langle M, P \setminus \{m\} \rangle}$$

THREAD: SILENT

$$\frac{\sigma \longrightarrow \sigma'}{\langle \langle \sigma, V \rangle, \langle M, P \rangle \rangle \xrightarrow{\text{NP}}_i \langle \langle \sigma', V \rangle, \langle M, P \rangle \rangle}$$

THREAD: WRITE

$$\frac{\sigma \xrightarrow{W_o x v} \sigma' \quad \langle M, P \rangle \xrightarrow{\langle x :_i^o v, R @ t \rangle} \langle M', P' \rangle \quad V(x) < t \quad V' = V[x \mapsto t] \quad o = \text{rlx} \implies R = \{x @ t\} \quad o = \text{rel} \implies R = V' \wedge P(i, x) = \emptyset}{\langle \langle \sigma, V \rangle, \langle M, P \rangle \rangle \xrightarrow{\text{NP}}_i \langle \langle \sigma', V' \rangle, \langle M', P' \rangle \rangle}$$

THREAD: READ

$$\frac{\sigma \xrightarrow{R_o x v} \sigma' \quad \langle x :_j^o v, R @ t \rangle \in M \quad V(x) \leq t \quad o = \text{rlx} \implies V' = V[x \mapsto t] \quad o = \text{acq} \implies V' = V[x \mapsto t] \sqcup R}{\langle \langle \sigma, V \rangle, \langle M, P \rangle \rangle \xrightarrow{\text{NP}}_i \langle \langle \sigma', V' \rangle, \langle M, P \rangle \rangle}$$

THREAD: PROMISE

$$\frac{m = \langle x :_i^{\text{rlx}} v, R @ t \rangle \quad M' = M \dot{\leftrightarrow} m \quad P' = P \dot{\leftrightarrow} m}{\langle \mathcal{TS}, \langle M, P \rangle \rangle \xrightarrow{\text{prom}}_i \langle \mathcal{TS}, \langle M', P' \rangle \rangle}$$

## 2 Program logic

The SLR assertion language is generated by the following grammar.

$$\begin{aligned}
P, Q \in \text{Assn} & ::= \perp \mid \top \mid P \vee Q \mid P \wedge Q \mid P \Rightarrow Q \mid \forall x. P \mid \exists x. P \mid N_1 = N_2 \mid \phi(N) \\
& \mid \top \mid P * Q \mid \text{Rel}(\ell, \phi) \mid \text{Acq}(\ell, \phi) \mid \text{O}(\ell, v, t) \mid \text{W}^\pi(\ell, X) \mid \ell \stackrel{\pi}{\mapsto} v \mid \nabla P \mid \text{pure}(P) \\
\phi \in \text{Pred} & ::= \lambda x. P
\end{aligned}$$

Here  $M, \ell, v, t, \pi$  and  $X$  all range over a simply-typed term language which we assume includes booleans, locations, values and expressions of the programming language, fractional permissions, and timestamps, and is closed under paring, finite sets and sequences. By convention we assume that  $\ell, v, t, \pi$  and  $X$  range over terms of type location, value, timestamp, permission and sets of pairs of values and timestamps, respectively.

### 2.1 Assertion logic

The judgments of the assertion logic include an entailment judgment,  $P \vdash Q$ , a view shift judgment,  $\vdash P \Rightarrow Q$ . The entailment judgment includes all the usual rules of first-order separation logic, which we elide. In addition it includes a number of axioms about SLR assertions, which are given below.

$$\begin{aligned}
& \text{Acq}(x, \lambda v. \phi_1(v) * \phi_2(v)) \Leftrightarrow \text{Acq}(x, \phi_1) * \text{Acq}(x, \phi_2) \\
& \text{Rel}(x, \phi) \Leftrightarrow \text{Rel}(x, \phi) * \text{Rel}(x, \phi) \\
& \text{W}^\pi(x, X) * (v, t) \in X \Rightarrow \text{W}^\pi(x, X) * \text{O}(x, v, t) \\
& \text{W}^1(x, X) * \text{O}(x, a, t) \Rightarrow \text{W}^1(x, X) * \text{O}(x, a, t) * (a, t) \in X \\
& \text{W}^\pi(x, X) * (v, t) \in X * (v', t') \in X * v \neq v' \Rightarrow \text{W}^\pi(x, X) * t \neq t' \\
& \text{W}^\pi(x, X) * (\_, t) \in X * (\_, t') \in X \Rightarrow \text{W}^\pi(x, X) * t < t' \vee t = t' \vee t' < t \\
& \text{W}_{\pi_1 + \pi_2}(x, X_1 \cup X_2) \Leftrightarrow \text{W}_{\pi_1}(x, X_1) * \text{W}_{\pi_2}(x, X_2) \\
& x \stackrel{\pi_1 + \pi_2}{\mapsto} v \Leftrightarrow x \stackrel{\pi_1}{\mapsto} v * x \stackrel{\pi_2}{\mapsto} v \\
& x \stackrel{\pi_1}{\mapsto} v_1 * x \stackrel{\pi_2}{\mapsto} v_2 \Rightarrow x \stackrel{\pi_1}{\mapsto} v_1 * x \stackrel{\pi_2}{\mapsto} v_2 * v_1 = v_2
\end{aligned}$$

For all assertions  $P$  expressible in the first-order logic fragment of our assertion logic, we have  $\vdash \text{pure}(P)$ .

### 2.2 Specification logic

The judgment of the specification logic is  $\vdash \{P\} s \{Q\}$ .

$$\begin{array}{c}
\frac{\vdash P \Rightarrow P' \quad \vdash \{P'\} s \{Q'\} \quad \vdash Q' \Rightarrow Q}{\vdash \{P\} s \{Q\}} \qquad \frac{\vdash \{P\} s \{Q\} \quad \text{mod}(s) \cap \text{FRV}(R) = \emptyset}{\vdash \{P * R\} s \{Q * R\}} \\
\frac{\{P\} s_1 \{Q\} \quad \{Q\} s_2 \{R\}}{\{P\} s_1; s_2 \{R\}} \qquad \frac{\{P \wedge e = \top\} s \{P\}}{\{P\} \text{ while } e \text{ do } s \{P \wedge e = \perp\}} \\
\frac{\{P \wedge e = \top\} s_1 \{Q\} \quad \{P \wedge e = \perp\} s_2 \{Q\}}{\{P\} \text{ if } e \text{ then } s_1 \text{ else } s_2 \{Q\}} \qquad \frac{\{P\} s \{Q\}}{\{\exists x. P\} s \{\exists x. Q\}}
\end{array}$$



$$\frac{\vdash P \Rightarrow Q}{\vdash P \Rightarrow Q} \quad \frac{\vdash P \Rightarrow Q}{\vdash P * R \Rightarrow Q * R} \quad \frac{\vdash P \Rightarrow Q \quad \vdash Q \Rightarrow R}{\vdash P \Rightarrow R}$$

### Plain accesses

$$\begin{aligned} & \vdash \left\{ x = e * x \overset{1}{\mapsto} \_ \right\} [e]_{\text{pln}} := a \left\{ x \overset{1}{\mapsto} a \right\} \\ & \vdash \left\{ x = e * x \overset{\pi}{\mapsto} v \right\} a := [e]_{\text{pln}} \left\{ x \overset{\pi}{\mapsto} v * a = v \right\} \end{aligned}$$

where  $x$  is a specification variable.

### Release and acquire accesses

$$\begin{aligned} & \vdash \left\{ x = e * \text{Acq}(x, \phi) * \text{O}(x, \_, t) \right\} \\ & \quad a := [e]_{\text{acq}} \\ & \quad \left\{ \exists t' \geq t. \text{Acq}(x, \phi[a \mapsto \top]) * \phi(a) * \text{O}(x, a, t') \right\} \\ \\ & \vdash \left\{ x = e * \text{Acq}(x, \phi) * \text{W}^1(x, X) \right\} \\ & \quad a := [e]_{\text{acq}} \\ & \quad \left\{ \text{Acq}(x, \phi[a \mapsto \top]) * \phi(a) * \text{W}^1(x, X) * \text{O}(x, a, \text{snd}(\max(X))) * a = \text{fst}(\max(X)) \right\} \\ \\ & \vdash \left\{ x = e_1 * v = e_2 * \text{Rel}(x, \phi) * \text{W}^\pi(x, X) * \phi(v) \right\} \\ & \quad [e_1]_{\text{rel}} := e_2 \\ & \quad \left\{ \exists t. \text{W}^\pi(x, X \cup \{(v, t)\}) * \text{snd}(\max(X)) < t \right\} \end{aligned}$$

where  $x, t, v, \pi$  and  $X$  are specification variables.

### Relaxed accesses

$$\begin{aligned} & \vdash \left\{ x = e * \text{Acq}(x, \phi) * \text{O}(x, \_, t) \right\} \\ & \quad a := [e]_{\text{rlx}} \\ & \quad \left\{ \exists t' \geq t. \nabla(\phi(a)) * \text{O}(x, a, t') \right\} \\ \\ & \vdash \left\{ x = e * \text{Acq}(x, \phi) * \text{W}^1(x, X) \right\} \\ & \quad a := [e]_{\text{rlx}} \\ & \quad \left\{ \text{Acq}(x, \phi) * \text{W}^1(x, X) * (\nabla\phi(a)) * \text{O}(x, a, \text{snd}(\max(X))) * a = \text{fst}(\max(X)) \right\} \\ \\ & \vdash \left\{ x = e_1 * v = e_2 * \text{Rel}(x, \phi) * \text{W}^\pi(x, X) * \phi(v) * \text{pure}(\phi(v)) \right\} \\ & \quad [e_1]_{\text{rlx}} := e_2 \\ & \quad \left\{ \exists t. \text{W}^\pi(x, X \cup \{(v, t)\}) * \text{snd}(\max(X)) < t \right\} \end{aligned}$$

where  $x, t, v, \pi$  and  $X$  are specification variables.

### 3 Semantics of the program logic

#### 3.1 Semantic domains

$$\begin{aligned}
PredId &\stackrel{def}{=} \mathcal{N} \\
Perm &\stackrel{def}{=} \{x \in \mathbb{Q} \mid 0 \leq x \leq 1\} \\
Perm^+ &\stackrel{def}{=} \{x \in \mathbb{Q} \mid 0 < x \leq 1\} \\
PlnPerm &\stackrel{def}{=} PLoc \rightarrow 1 + (Perm^+ \times Val \times Time) \\
WrPerm &\stackrel{def}{=} NPLoc \rightarrow \{(\pi, X) \in Perm \times \mathcal{P}(Val \times Time) \mid \pi = 0 \Rightarrow X = \emptyset\} \\
AcqPerm &\stackrel{def}{=} NPLoc \rightarrow \mathcal{P}(PredId) \\
r \in \mathcal{M} &\stackrel{def}{=} PlnPerm \times WrPerm \times AcqPerm \\
u \in MsgRes &\stackrel{def}{=} Msg \rightarrow_{fin} (PredId \rightarrow_{fin} \mathcal{M}) \\
\mathcal{W} \in World &\stackrel{def}{=} (PLoc \rightarrow Pred) \times (PredId \rightarrow_{fin} Pred) \\
p \in Prop &\stackrel{def}{=} World \rightarrow_{mon} \mathcal{P}^\uparrow(GConf \times View \times \mathcal{M}) \\
TRes &\stackrel{def}{=} TId \rightarrow \mathcal{M}
\end{aligned}$$

We use  $r.pln$ ,  $r.wr$  and  $r.acq$  to refer to the first, second and third projection of a resource  $r$ , respectively.

*Resource monoid*

$\bullet : \mathcal{M} \times \mathcal{M} \rightarrow \mathcal{M}$ $\varepsilon : \mathcal{M}$
---

The set of resources,  $\mathcal{M}$  is a partial commutative monoid with the following composition operator and monoid unit.

$$\begin{aligned}
r_1 \bullet r_2 &\stackrel{def}{=} (r_1.pln \bullet_{pln} r_2.pln, r_1.wr \bullet_{wr} r_2.wr, r_1.acq \bullet_{acq} r_2.acq) \\
\varepsilon &\stackrel{def}{=} (\lambda_. inj_1(*), \lambda_. (0, \emptyset), \lambda_. \emptyset)
\end{aligned}$$

where

$$\begin{aligned}
h_1 \bullet_{pln} h_2 &\stackrel{def}{=} \begin{cases} \lambda x. h_1(x) \bullet_{pl} h_2(x) & \text{if } h_1(x) \# h_2(x) \text{ for all } x \in Loc \\ \text{undefined} & \text{otherwise} \end{cases} \\
inj_1(*) \bullet_{pl} x &\stackrel{def}{=} x \\
x \bullet_{pl} inj_1(*) &\stackrel{def}{=} x \\
inj_2(\pi_1, v_1) \bullet_{pl} inj_2(\pi_2, v_2) &\stackrel{def}{=} \begin{cases} inj_2(\pi_1 + \pi_2, v_1) & \text{if } \pi_1 + \pi_2 \leq 0 \text{ and } v_1 = v_2 \\ \perp & \text{otherwise} \end{cases} \\
f_1 \bullet_{wr} f_2 &\stackrel{def}{=} \begin{cases} \lambda x. f_1(x) \bullet_{wr} f_2(x) & \text{if } f_1(x) \# f_2(x) \text{ for all } x \\ \text{undefined} & \text{otherwise} \end{cases} \\
(\pi_1, X_1) \bullet (\pi_2, X_2) &\stackrel{def}{=} \begin{cases} (\pi_1 + \pi_2, X_1 \cup X_2) & \text{if } \pi_1 + \pi_2 \leq 0 \\ \text{undefined} & \text{otherwise} \end{cases} \\
g_1 \bullet_{acq} g_2 &\stackrel{def}{=} \begin{cases} \lambda x. g_1(x) \cup g_2(x) & \text{if } \forall x. g_1(x) \cap g_2(x) = \emptyset \\ \text{undefined} & \text{otherwise} \end{cases}
\end{aligned}$$

The monotonicity of propositions is with respect to the following ordering on worlds, and subset inclusion on  $\mathcal{P}^\uparrow(GConf \times View \times \mathcal{M})$ .

World ordering

$$\leq : \mathcal{P}(\text{World} \times \text{World})$$

$$\begin{aligned} \mathcal{W}_1 \leq \mathcal{W}_2 &\stackrel{\text{def}}{=} \mathcal{W}_1.\text{rel} = \mathcal{W}_2.\text{rel} \wedge \text{dom}(\mathcal{W}_1.\text{acq}) \subseteq \text{dom}(\mathcal{W}_2.\text{acq}) \wedge \\ &\forall \iota \in \text{dom}(\mathcal{W}_1.\text{acq}). \mathcal{W}_1.\text{acq}(\iota) = \mathcal{W}_2.\text{acq}(\iota) \end{aligned}$$

The upwards-closure on  $GConf \times View \times \mathcal{M}$  is with respect to the point-wise extension of the following orders on resources and global configurations and the previously defined ordering on views.

Resource ordering

$$\leq : \mathcal{P}(\mathcal{M} \times \mathcal{M})$$

$$r_1 \leq r_2 \stackrel{\text{def}}{=} \exists r_3. r_2 = r_1 \bullet r_3$$

Configuration ordering

$$\leq : \mathcal{P}(GConf \times GConf)$$

$$gconf_1 \leq gconf_2 \stackrel{\text{def}}{=} gconf'.M \text{ is a future memory of } gconf.M \text{ w.r.t. } gconf'.P$$

**Lemma 2.**

$$gconf.M \text{ is closed} \wedge (\Sigma, gconf) \implies (\Sigma', gconf') \implies gconf \leq gconf'$$

Well-formed configuration

$$\text{wf}_{\text{conf}}$$

$$\text{wf}_{\text{conf}}(i, gconf, V) \stackrel{\text{def}}{=} \forall m \in gconf.P. m.\text{tid} = i \implies V.\text{rlx}(m.\text{loc}) < m.\text{time}$$

**Lemma 3.**

$$\langle \langle \sigma, V \rangle, \langle M, P \rangle \rangle \xrightarrow{i}^{\text{NP}^*} \langle \langle \sigma', V' \rangle, \langle M', P' \rangle \rangle \wedge P'(i) = \emptyset \implies \text{wf}_{\text{conf}}(i, \langle M, P \rangle, V)$$

Read assertion

$$O$$

$$O(\ell, v, t) \stackrel{\text{def}}{=} \lambda \mathcal{W}. \{(gconf, V, r) \mid \exists m \in gconf.M. m.\text{loc} = \ell \wedge m.\text{val} = v \wedge m.\text{time} = t \wedge t \leq V.\text{rlx}(\ell)\}$$

Write assertion

$$W$$

$$W_\pi(\ell, X) \stackrel{\text{def}}{=} \lambda \mathcal{W}. \{(gconf, V, r) \mid \exists \pi' \geq \pi. r.\text{wr}(\ell) = \text{inj}_2(\pi', X) \wedge \text{snd}(\max(X)) \leq V.\text{rlx}(\ell)\}$$

Acq assertion

$$\text{acq}$$

$$\text{acq}(\ell, \phi) \stackrel{\text{def}}{=} \lambda \mathcal{W}. \{(gconf, V, r) \mid \exists \iota \in \text{dom}(\mathcal{W}.\text{acq}) \cap r.\text{acq}(\ell). \mathcal{W}.\text{acq}(\iota) = \phi\}$$

Rel assertion

$$\text{rel}$$

$$\text{rel}(\ell, \phi) \stackrel{\text{def}}{=} \lambda \mathcal{W}. \{(gconf, V, r) \mid \vdash \forall v. \phi(v) \implies \mathcal{W}.\text{rel}(\ell)(v)\}$$

$$\ell \xrightarrow{\pi} v \stackrel{\text{def}}{=} \lambda \mathcal{W}. \{(gconf, V, r) \mid \exists \pi' \geq \pi. \exists t. r.\text{pln}(\ell) = \text{inj}_2(\pi', v, t) \wedge t \leq V.\text{pln}(x)\}$$

Assertion interpretation

$$\llbracket - \rrbracket_{\mu}^{\equiv} : \text{Assn} \times \text{TStore} \times \text{Env} \rightarrow \text{Prop}$$

$$\begin{aligned} \llbracket \perp \rrbracket_{\mu}^{\eta}(\mathcal{W}) &\stackrel{\text{def}}{=} \emptyset \\ \llbracket \top \rrbracket_{\mu}^{\eta}(\mathcal{W}) &\stackrel{\text{def}}{=} \text{GConf} \times \text{View} \times \mathcal{M} \\ \llbracket P \wedge Q \rrbracket_{\mu}^{\eta}(\mathcal{W}) &\stackrel{\text{def}}{=} \llbracket P \rrbracket_{\mu}^{\eta} \cap \llbracket Q \rrbracket_{\mu}^{\eta}(\mathcal{W}) \\ \llbracket P \vee Q \rrbracket_{\mu}^{\eta}(\mathcal{W}) &\stackrel{\text{def}}{=} \llbracket P \rrbracket_{\mu}^{\eta} \cup \llbracket Q \rrbracket_{\mu}^{\eta}(\mathcal{W}) \\ \llbracket P \Rightarrow Q \rrbracket_{\mu}^{\eta}(\mathcal{W}) &\stackrel{\text{def}}{=} \{(gconf, V, r) \mid \forall gconf' \geq gconf. \forall V' \geq V. \forall r' \geq r. \forall \mathcal{W}' \geq \mathcal{W}. \\ &\quad (gconf', V', r') \in \llbracket P \rrbracket_{\mu}^{\eta}(\mathcal{W}') \Rightarrow (gconf', V', r') \in \llbracket Q \rrbracket_{\mu}^{\eta}(\mathcal{W}')\} \\ \llbracket P * Q \rrbracket_{\mu}^{\eta}(\mathcal{W}) &\stackrel{\text{def}}{=} \{(gconf, V, r_1 \bullet r_2) \mid (gconf, V, r_1) \in \llbracket P \rrbracket_{\mu}^{\eta}(\mathcal{W}) \wedge (gconf, V, r_2) \in \llbracket Q \rrbracket_{\mu}^{\eta}(\mathcal{W})\} \\ \llbracket \forall x. P \rrbracket_{\mu}^{\eta}(\mathcal{W}) &\stackrel{\text{def}}{=} \bigcap_{v \in \text{Val}} \llbracket P \rrbracket_{\mu}^{\eta[x \mapsto v]}(\mathcal{W}) \\ \llbracket \exists x. P \rrbracket_{\mu}^{\eta}(\mathcal{W}) &\stackrel{\text{def}}{=} \bigcup_{v \in \text{Val}} \llbracket P \rrbracket_{\mu}^{\eta[x \mapsto v]}(\mathcal{W}) \\ \llbracket N_1 = N_2 \rrbracket_{\mu}^{\eta}(\mathcal{W}) &\stackrel{\text{def}}{=} \{x \mid \llbracket N_1 \rrbracket_{\mu}^{\eta} = \llbracket N_2 \rrbracket_{\mu}^{\eta}\} \\ \llbracket (\lambda x. P)(N) \rrbracket_{\mu}^{\eta}(\mathcal{W}) &\stackrel{\text{def}}{=} \llbracket P \rrbracket_{\mu}^{\eta[x \mapsto \llbracket N \rrbracket_{\mu}^{\eta}]}(\mathcal{W}) \\ \\ \llbracket \text{O}(\ell, v, t) \rrbracket_{\mu}^{\eta}(\mathcal{W}) &\stackrel{\text{def}}{=} \text{O}(\llbracket \ell \rrbracket_{\mu}^{\eta}, \llbracket v \rrbracket_{\mu}^{\eta}, \llbracket t \rrbracket_{\mu}^{\eta})(\mathcal{W}) \\ \llbracket \text{W}_{\pi}(\ell, X) \rrbracket_{\mu}^{\eta}(\mathcal{W}) &\stackrel{\text{def}}{=} \text{W}_{\llbracket \pi \rrbracket_{\mu}^{\eta}}(\llbracket \ell \rrbracket_{\mu}^{\eta}, \llbracket X \rrbracket_{\mu}^{\eta})(\mathcal{W}) \\ \llbracket \text{Acq}(\ell, \phi) \rrbracket_{\mu}^{\eta}(\mathcal{W}) &\stackrel{\text{def}}{=} \text{acq}(\llbracket \ell \rrbracket_{\mu}^{\eta}, \phi)(\mathcal{W}) \\ \llbracket \text{Rel}(\ell, \phi) \rrbracket_{\mu}^{\eta}(\mathcal{W}) &\stackrel{\text{def}}{=} \text{rel}(\llbracket \ell \rrbracket_{\mu}^{\eta}, \phi)(\mathcal{W}) \\ \llbracket \ell \xrightarrow{\pi} v \rrbracket_{\mu}^{\eta}(\mathcal{W}) &\stackrel{\text{def}}{=} (\llbracket \ell \rrbracket_{\mu}^{\eta} \xrightarrow{\llbracket \pi \rrbracket_{\mu}^{\eta}} \llbracket v \rrbracket_{\mu}^{\eta})(\mathcal{W}) \\ \llbracket \nabla P \rrbracket_{\mu}^{\eta}(\mathcal{W}) &\stackrel{\text{def}}{=} \{(gconf, V, r) \mid \llbracket P \rrbracket_{\mu}^{\eta}(\mathcal{W}) \neq \emptyset\} \\ \llbracket \text{pure}(P) \rrbracket_{\mu}^{\eta}(\mathcal{W}) &\stackrel{\text{def}}{=} \{x \mid \forall \mathcal{W}' \geq \mathcal{W}. \forall x_1, x_2. x_1 \in \llbracket P \rrbracket_{\mu}^{\eta}(\mathcal{W}') \Rightarrow x_2 \in \llbracket P \rrbracket_{\mu}^{\eta}(\mathcal{W}')\} \end{aligned}$$

### 3.2 Acquirable resources

*Acquirable messages*

$$\boxed{\text{canAcq} : \text{AcqPerm} \times \text{MsgRes} \rightarrow \mathcal{P}(\text{Msg} \times \text{PredId})}$$

The  $\text{canAcq}(A, u)$  function recursively computes the set of messages whose resources we are currently allowed to acquire from message resource assignment  $u$ .

$$\text{canAcq}(A, u) \stackrel{\text{def}}{=} \begin{cases} I \cup \text{canAcq}(\prod_{(m, \iota) \in I} u(m)(\iota). \text{acq}, u[I \mapsto \perp]) & \text{if } I = \text{canAcq}_0(A, u) \neq \emptyset \\ \emptyset & \text{otherwise} \end{cases}$$

$$\text{canAcq}_0(A, u) \stackrel{\text{def}}{=} \{(m, \iota) \mid m \in \text{dom}(u) \wedge \iota \in A(m.\text{loc}) \cap \text{dom}(u(m))\}$$

The  $\text{canAcq}(A, u)$  function is defined by recursion using  $\sum_{m \in \text{dom}(u)} |\text{dom}(u(m))|$  as the size function.

*Acquirable resources*

$$\boxed{\text{canAcq} : \mathcal{M} \times \text{MsgRes} \rightarrow \mathcal{M}}$$

The  $\text{canAcq}(r, u)$  function computes the composition of the resources we are currently allowed to acquire from message resource assignment  $u$ .

$$\text{canAcq}(r, u) \stackrel{\text{def}}{=} \prod_{(m, \iota) \in \text{canAcq}(r.\text{acq}, u)} u(m)(\iota)$$

Throughout the rest of the document whenever we write  $\text{canAcq}(r, u)$  we will implicitly assume that  $r \bullet \prod_{m \in \text{dom}(u)} \prod_{\iota \in \text{dom}(u(m))} u(m)(\iota)$  is defined and whenever we write  $\text{canAcq}(A, u)$  that  $A \bullet (\prod_{m \in \text{dom}(u)} \prod_{\iota \in \text{dom}(u(m))} u(m)(\iota)). \text{acq}$  is defined.

**Lemma 4.**  $\text{canAcq}_0(A \bullet B, u) = \text{canAcq}_0(A, u) \cup \text{canAcq}_0(B, u)$

**Lemma 5.**  $\text{canAcq}(A \bullet B, u) = \text{canAcq}(A, u) \cup \text{canAcq}(B, u)$

**Lemma 6.** For  $m \in \text{dom}(u)$  and  $\iota \in A(m.\text{loc}) \cap \text{dom}(u(m))$ , we have  $\text{canAcq}(A, u) = \text{canAcq}(A', u')$  where  $A' = A \bullet u(m)(\iota).A$  and  $u' = u[m, \iota \mapsto \varepsilon]$ .

*Proof.*

$$\begin{aligned} \text{canAcq}(A', u') &= \text{canAcq}(A, u') \cup \text{canAcq}(u(m)(\iota).A, u') \\ &= (\text{canAcq}(A, u) \setminus \text{canAcq}(u(m)(\iota).A, u)) \cup \text{canAcq}(u(m)(\iota).A, u) \\ &= \text{canAcq}(A, u) \end{aligned}$$

□

**Lemma 7.**

$$\begin{aligned} m \in \text{dom}(u) \wedge \iota \in r.\text{acq}(m.\text{loc}) \cap \text{dom}(u(m)) &\Rightarrow \\ \text{canAcq}(r, u) &= u(m)(\iota) \bullet \text{canAcq}(r \bullet u(m)(\iota), u[m, \iota \mapsto \varepsilon]) \end{aligned}$$

*Proof.* By Lemma 6

$$\begin{aligned} &u(m)(\iota) \bullet \text{canAcq}(r \bullet u(m)(\iota), u[m, \iota \mapsto \varepsilon]) \\ &= u(m)(\iota) \bullet \prod_{(m', \iota') \in \text{canAcq}((r \bullet u(m)(\iota)). \text{acq}, u[m, \iota \mapsto \varepsilon])} u[m, \iota \mapsto \varepsilon](m')(\iota') \\ &= u(m)(\iota) \bullet \prod_{(m', \iota') \in \text{canAcq}(r.\text{acq}, u)} u[m, \iota \mapsto \varepsilon](m')(\iota') \\ &= u(m)(\iota) \bullet \prod_{(m', \iota') \in (\text{canAcq}(r.\text{acq}, u) \setminus \{(m, \iota)\})} u(m')(\iota') \\ &= \prod_{(m', \iota') \in \text{canAcq}(r.\text{acq}, u)} u(m')(\iota') \\ &= \text{canAcq}(r, u) \end{aligned}$$

□

**Lemma 8.**

$$m \in \text{dom}(u) \wedge \iota \in A(m.\text{loc}) \cap \text{dom}(u(m)) \Rightarrow \\ \text{canAcq}(A, u) \setminus \{(m, \iota)\} = \text{canAcq}(A', u')$$

where  $A' = A \bullet u(m)(\iota).\text{acq}$  and  $u' = u[m, \iota \mapsto \perp]$ .

*Proof.*

$$\begin{aligned} \text{canAcq}(A', u') &= \text{canAcq}(A, u') \cup \text{canAcq}(u(m)(\iota).\text{acq}, u') \\ &= (\text{canAcq}(A, u) \setminus (\{(m, \iota)\} \cup \text{canAcq}(u(m)(\iota).\text{acq}, u))) \cup \text{canAcq}(u(m)(\iota).\text{acq}, u) \\ &= \text{canAcq}(A, u) \setminus \{(m, \iota)\} \end{aligned}$$

□

**Lemma 9.**

$$\begin{aligned} \iota \in A(x) \wedge \iota' \text{ fresh for } A \text{ and } u \Rightarrow \\ \text{canAcq}_0(A', u') &= (\text{canAcq}_0(A, u) \setminus \{(m, \iota) \mid m.\text{loc} = x\}) \cup \\ &\quad \{(m, \iota') \mid m.\text{loc} = x \wedge \iota \in \text{dom}(u(m))\} \end{aligned}$$

where  $u' = u[\iota \rightsquigarrow_x \iota']$  and  $A' = A[x \mapsto A(x)[\iota \rightsquigarrow \iota']]$ .

**Lemma 10.**

$$\begin{aligned} \iota \in A(x) \wedge \iota' \text{ fresh for } A \text{ and } u \Rightarrow \\ \text{canAcq}(A', u') &= (\text{canAcq}(A, u) \setminus \{(m, \iota) \mid m.\text{loc} = x\}) \cup \\ &\quad \{(m, \iota') \mid m.\text{loc} = x \wedge \iota \in \text{dom}(u(m))\} \end{aligned}$$

where  $u' = u[\iota \rightsquigarrow_x \iota']$  and  $A' = A[x \mapsto A(x)[\iota \rightsquigarrow \iota']]$ .

**Lemma 11.**

$$\begin{aligned} \iota \notin r.\text{acq}(x) \wedge \iota' \text{ fresh for } u \text{ and } r \wedge \\ (\forall m \in \text{dom}(u). \forall \iota'' \in \text{dom}(u(m)). \iota \notin u(m)(\iota'').A(x)) \Rightarrow \\ \text{canAcq}(r[\iota \rightsquigarrow_x \iota'], u[\iota \rightsquigarrow_x \iota']) = \text{canAcq}(r, u) \end{aligned}$$

*Proof.* It follows from the  $\iota \notin r.\text{acq}(x)$  assumption that  $r = r[\iota \rightsquigarrow_x \iota']$ . Furthermore, since  $\iota \notin u(m)(\iota'').A(x)$  for any  $m \in \text{dom}(m)$  and  $\iota'' \in \text{dom}(u(m))$  it follows that the acquirable resources should be independent of the current resources associated with the predicate named  $\iota$ , since these resources currently cannot be acquired. □

*Predicate renaming*

$(-)[\equiv_{\rightsquigarrow_x}] : \text{MsgRes} \times \text{PredId} \times \text{PredId} \rightarrow \text{MsgRes}$ $(-)[\equiv_{\rightsquigarrow_x}] : \mathcal{M} \times \text{PredId} \times \text{PredId} \rightarrow \mathcal{M}$
--

$$u[\iota \rightsquigarrow_x \iota'] \stackrel{\text{def}}{=} \lambda m. \begin{cases} \lambda \iota''. \begin{cases} u(m)(\iota) & \text{if } \iota'' = \iota' \text{ and } m.\text{loc} = x \\ \perp & \text{if } \iota'' = \iota \text{ and } m.\text{loc} = x \\ u(m)(\iota'') & \text{otherwise} \end{cases} & \text{if } m \in \text{dom}(u) \\ \perp & \text{otherwise} \end{cases}$$

$$X[\iota \rightsquigarrow \iota'] \stackrel{\text{def}}{=} \text{if } \iota \in X \text{ then } (X \setminus \{\iota\}) \cup \{\iota'\} \text{ else } X$$

$$r[\iota \rightsquigarrow_x \iota'] \stackrel{\text{def}}{=} r[A(x) \mapsto A(x)[\iota \rightsquigarrow_x \iota']$$

$$r_F[\iota \rightsquigarrow_x \iota'] \stackrel{\text{def}}{=} \lambda t \in \text{TIId}. r_F(t)[\iota \rightsquigarrow_x \iota']$$

**Lemma 12.**

$$\begin{aligned} & \iota \in A(x) \wedge \iota' \text{ fresh for } A \text{ and } u \Rightarrow \\ & \text{canAcq}(A', u') = (\text{canAcq}(A, u) \setminus \{(m, \iota) \mid m \in \text{dom}(u) \wedge m.\text{loc} = x\}) \cup \\ & \quad \{(m, \iota') \mid (m, \iota) \in \text{canAcq}(A, u) \wedge m.\text{loc} = x\} \end{aligned}$$

where  $u' = u[\iota \rightsquigarrow_x \iota']$  and  $A' = A[x \mapsto A(x)[\iota \rightsquigarrow \iota']$ .

**Lemma 13.**

$$\begin{aligned} & \iota \in r.\text{acq}(x) \wedge \iota' \text{ fresh for } u \text{ and } r \Rightarrow \\ & \text{canAcq}(r, u) = \text{canAcq}(r[\iota \rightsquigarrow_x \iota'], u[\iota \rightsquigarrow_x \iota']) \end{aligned}$$

*Proof.* Follows from Lemma 12. □

**Lemma 14.**

$$\begin{aligned} & \text{dom}(u) = \text{dom}(u') = M(\text{rel}) \wedge x \in \text{dom}(r.\text{acq}) \wedge \iota \in r.\text{acq}(x) \wedge \\ & r' = r[A(x) \mapsto (r.\text{acq}(x) \setminus \{\iota\}) \cup \{\iota_1, \iota_2\}] \wedge \\ & u' = u[m \in M(\text{rel}, x) \mapsto u(m)[\iota \mapsto \perp, \iota_1 \mapsto r_m^1, \iota_2 \mapsto r_m^2]] \\ & \forall m \in M(\text{rel}, x). u(m)(\iota) = r_m^1 \bullet r_m^2 \\ & \quad \iota \in \text{dom}(u(m)) \wedge \iota_1, \iota_2 \notin \text{dom}(u(m)) \cup \text{dom}(u'(m)) \wedge \\ & \Rightarrow \text{canAcq}(r, u) = \text{canAcq}(r', u') \end{aligned}$$

Write restrictions

$$\begin{aligned} \text{writeAllowed} &: \mathcal{P}(\mathcal{M} \times \text{Msg}) \\ \text{writesAllowed} &: \mathcal{P}(\mathcal{M} \times \mathcal{P}(\text{Msg})) \end{aligned}$$

$$\begin{aligned} \text{writeAllowed}(r, m) &\stackrel{\text{def}}{=} (\text{pln}(m) \wedge r.\text{pln}(m.\text{loc}) = (1, \_)) \vee \\ &\quad (\text{npln}(m) \wedge \pi_1(r.\text{wr}(m.\text{loc})) > 0) \\ \text{writesAllowed}(r, M) &\stackrel{\text{def}}{=} \forall m \in M. \text{writeAllowed}(r, m) \end{aligned}$$

Ownership ordering

$$\leq_o : \mathcal{P}(\mathcal{M} \times \mathcal{M})$$

$$\begin{aligned} r_1 \leq_o r_2 &\stackrel{\text{def}}{=} \forall x. \pi_1(r_1.\text{wr}(x)) \leq \pi_1(r_2.\text{wr}(x)) \wedge \\ &\quad \forall x. r_2.\text{pln}(x) = \text{inj}_1(*) \Rightarrow r_1.\text{pln}(x) = \text{inj}_1(*) \wedge \\ &\quad \forall x. r_1.\text{pln}(x) \neq \text{inj}_1(*) \Rightarrow \pi_1(r_1.\text{pln}(x)) \leq \pi_1(r_2.\text{pln}(x)) \end{aligned}$$

**Lemma 15.**

$$r_1 \leq_o r'_1 \wedge r_2 \leq_o r'_2 \Rightarrow r_1 \bullet r_2 \leq_o r'_1 \bullet r'_2$$

**Lemma 16.**

$$\text{writeAllowed}(r_1, m) \wedge r_1 \leq_o r_2 \Rightarrow \text{writeAllowed}(r_2, m)$$

**Lemma 17.**

$$\begin{aligned} \iota \notin \text{dom}(u(m)) \wedge \text{def}(r, u[m \mapsto u(m)[\iota \mapsto r_2]]) \\ \Rightarrow \text{canAcq}(r_1, u[m \mapsto u(m)[\iota \mapsto r_2]]) \leq_o \text{canAcq}(r_1 \bullet r_2, u) \end{aligned}$$

World erasure

$$|-| : \text{World} \rightarrow \text{World}$$

$$|\mathcal{W}| \stackrel{\text{def}}{=} (\pi_1(\mathcal{W}), [])$$

Erasure

$$[-_1, =_2, =_3]_{=4} : \mathcal{M} \times \text{MsgRes} \times \text{World} \times \mathcal{P}(\text{TId}) \rightarrow \mathcal{P}(\text{GConf})$$

$$\begin{aligned} [r, u, \mathcal{W}]_T &\stackrel{\text{def}}{=} \{(M, P) \mid \text{closed}(M) \wedge \\ &\quad \text{let } t = \Pi_{t \in \text{TId}} r(t) \bullet \Pi_{m \in M} \Pi_{\iota \in \text{dom}(u(m))} u(m)(\iota) \text{ in} \\ &\quad \text{wf}(\mathcal{W}, u, t.A) \wedge \\ &\quad \forall x \in \text{locs}(\text{pln}(M)). t.\text{pln}(x) \neq \text{inj}_1(*) \Rightarrow \\ &\quad \quad \text{last}(M(x) \setminus P).\text{val} = t.\text{pln}(x).\text{val} \wedge \\ &\quad \quad \text{last}(M(x) \setminus P).\text{time} = t.\text{pln}(x).\text{time} \wedge \\ &\quad \forall x \in \text{locs}(\text{npln}(M)). \\ &\quad \quad \{(m.\text{val}, m.\text{time}) \mid m \in M(x) \setminus P\} = \text{snd}(t.\text{wr}(x)) \wedge \\ &\quad \forall M(\text{rlx}). \\ &\quad \quad (g\text{conf}_\perp, V_\perp, \varepsilon) \in \llbracket \mathcal{W}.\text{rel}(m.\text{loc})(m.\text{val}) \rrbracket_\perp^{\square}(|\mathcal{W}|) \wedge \\ &\quad \forall m \in M(\text{rel}). t.\text{acq}(m.\text{loc}) = \text{dom}(u(m)) \wedge \forall \iota \in \text{dom}(u(m)). \\ &\quad \quad (g\text{conf}, m.\text{view}, u(m)(\iota)) \in \llbracket \mathcal{W}.\text{acq}(\iota)(m.\text{val}) \rrbracket_\perp^{\square}(\mathcal{W}) \wedge \\ &\quad \text{validPromises}_T(r, u, P)\} \end{aligned}$$



where

$$\begin{aligned} \text{wf}(\mathcal{W}, u, A) &\stackrel{\text{def}}{=} \text{dom}(u) = M(\text{rel}) \wedge \\ &\quad \{\iota \mid m \in \text{dom}(u) \wedge \iota \in \text{dom}(u(m))\} \subseteq \text{dom}(\mathcal{W}.\text{acq}) \wedge \\ &\quad \forall x \in \text{NPLoc}. \forall v. \vdash \mathcal{W}.\text{rel}(x)(v) \Rightarrow \otimes_{\iota \in A(x)} \mathcal{W}.\text{acq}(\iota)(v) \end{aligned}$$

*Valid promises*

$\begin{aligned} \text{validPromises}_-(=) &: \mathcal{P}(\mathcal{P}(TId) \times TRes \times MsgRes \times Mem) \\ \text{validPromise}(-) &: \mathcal{P}(\mathcal{M} \times MsgRes \times Msg) \end{aligned}$
--

$$\begin{aligned} \text{validPromises}_T(r, u, P) &\stackrel{\text{def}}{=} \forall m \in P. m.\text{tid} \notin T \Rightarrow \text{validPromise}(r, u, m) \\ \text{validPromise}(r, u, m) &\stackrel{\text{def}}{=} \text{writeAllowed}(r(m.\text{tid}) \bullet \text{canAcq}(r(m.\text{tid}), u), m) \end{aligned}$$

*Non-promising safety assertion*

$\text{npsafe}_-(=) : \omega^2 \times TLState \times Prop \rightarrow Prop$
---

$$\begin{aligned} \text{npsafe}_{(0,m)}(\sigma, B)(\mathcal{W}) &\stackrel{\text{def}}{=} GConf \times View \times \mathcal{M} \\ \text{npsafe}_{(n+1,0)}(\sigma, B)(\mathcal{W}) &\stackrel{\text{def}}{=} \{(gconf, V, r) \mid \forall m. (gconf, V, r) \in \text{npsafe}_{(n,m)}(\sigma, B)(\mathcal{W})\} \\ \text{npsafe}_{(n+1,m+1)}(\sigma, B)(\mathcal{W}) &\stackrel{\text{def}}{=} \{(gconf_1, V_1, r_1) \mid \forall (gconf, V, r) \geq (gconf_1, V_1, r_1). \\ &\quad (\sigma.s = \text{skip} \Rightarrow (gconf, V, r) \in \text{vs}(B(\sigma.\mu))(\mathcal{W})) \\ &\quad \wedge (\forall r_F, f, \sigma', gconf', V', u, i, \mathcal{W}' \geq \mathcal{W}. gconf \in \lfloor r_F[i \mapsto r \bullet f], u, \mathcal{W}' \rfloor_{\{i\}} \wedge \\ &\quad \quad \langle \langle \sigma, V \rangle, gconf \rangle \xrightarrow{\text{NP}}_i \langle \langle \sigma', V' \rangle, gconf' \rangle \wedge \text{wf}_{\text{conf}}(i, gconf, V) \wedge \text{wf}_{\text{conf}}(i, gconf', V') \Rightarrow \\ &\quad \quad \exists r', u', \mathcal{W}'' \geq \mathcal{W}'. gconf' \in \lfloor r_F[i \mapsto r' \bullet f], u', \mathcal{W}'' \rfloor_{\{i\}} \wedge \\ &\quad \quad (gconf', V', r') \in \text{npsafe}_{(n+1,m)}(\sigma', B)(\mathcal{W}'') \wedge \\ &\quad \quad r' \bullet \text{canAcq}(r', u') \leq_o r \bullet \text{canAcq}(r, u) \wedge \\ &\quad \quad \text{writesAllowed}(r, \text{written}(gconf, gconf')))\} \end{aligned}$$

**Lemma 18** (npsafe is downwards-closed.).

$$\begin{aligned} \forall n, n', m, m', \sigma, B, \mathcal{W}. \\ (n', m') \leq (n, m) \wedge \text{npsafe}_{(n,m)}(\sigma, B)(\mathcal{W}) \subseteq \text{npsafe}_{(n',m')}(\sigma, B)(\mathcal{W}) \end{aligned}$$

*Written messages*

$\text{written} : GConf \times GConf \rightarrow Mem$
---

$$\text{written}(gconf, gconf') \stackrel{\text{def}}{=} \{m \in gconf'.M \mid (m \notin gconf.M \vee m \in gconf.P) \wedge m \notin gconf'.P\}$$

*View-shift assertion*

$\text{vs}(-) : Prop \rightarrow Prop$
--

$$\begin{aligned} \text{vs}(B)(\mathcal{W}) &\stackrel{\text{def}}{=} \{(gconf_1, V_1, r_1) \mid \forall (gconf, V, r) \geq (gconf_1, V_1, r_1). \forall i, f, u, T, \mathcal{W}' \geq \mathcal{W}. \\ &\quad gconf \in \lfloor r_F[i \mapsto r \bullet f], u, \mathcal{W}' \rfloor_T \wedge \text{wf}_{\text{conf}}(i, gconf, V) \Rightarrow \\ &\quad \exists r', u', \mathcal{W}'' \geq \mathcal{W}'. \\ &\quad (gconf, V, r') \in B(\mathcal{W}'') \wedge gconf \in \lfloor r_F[i \mapsto r' \bullet f], u', \mathcal{W}'' \rfloor_T \wedge \\ &\quad r' \leq_o r \wedge r' \bullet \text{canAcq}(r', u') \leq_o r \bullet \text{canAcq}(r, u)\} \end{aligned}$$

Promising safety assertion

$$\text{safe}_-(=) : \omega \times TLState \times Prop \rightarrow Prop$$

$$\begin{aligned} \text{safe}_0(\sigma, B)(\mathcal{W}) &\stackrel{\text{def}}{=} GConf \times View \times \mathcal{M} \\ \text{safe}_{n+1}(\sigma, B)(\mathcal{W}) &\stackrel{\text{def}}{=} \{(gconf_1, V_1, r_1) \mid \forall (gconf, V, r) \geq (gconf_1, V_1, r_1). \\ &\quad (\sigma.s = \mathbf{skip} \Rightarrow (gconf, V, r) \in vs(B(\sigma.\mu))(\mathcal{W})) \\ &\quad \wedge (\forall r_F, \sigma', gconf', V', u, \mathcal{W}', i. gconf' \in [r_F[i \mapsto r], u, \mathcal{W}]_{\emptyset} \wedge \\ &\quad \quad \langle \langle \sigma, V \rangle, gconf \rangle \Longrightarrow_i \langle \langle \sigma', V' \rangle, gconf' \rangle \wedge n > 0 \\ &\quad \Rightarrow \exists r', u', \mathcal{W}'' \geq \mathcal{W}'. \\ &\quad \quad gconf' \in [r_F[i \mapsto r'], u', \mathcal{W}'' ]_{\emptyset} \wedge \\ &\quad \quad (gconf', V', r') \in \text{safe}_n(\sigma', B)(\mathcal{W}'')\}\} \end{aligned}$$

**Lemma 19** (safe is downwards-closed.).

$$\begin{aligned} \forall n, n', \sigma, B, \mathcal{W}. \\ n' \leq n \wedge \text{safe}_n(\sigma, B)(\mathcal{W}) \subseteq \text{safe}_{n'}(\sigma, B)(\mathcal{W}) \end{aligned}$$

Interpretation of triples

$$\models_{np} \{A\} s \{B\}, \models_p \{A\} s \{B\}, \models A \Rightarrow B$$

$$\begin{aligned} \models_{np} \{A\} s \{B\} &\triangleq \forall n, m, \mu, \mathcal{W}. A(\mu)(\mathcal{W}) \subseteq \text{npsafe}_{(n,m)}((\mu, s), B)(\mathcal{W}) \\ \models_p \{A\} s \{B\} &\triangleq \forall n, \mu, \mathcal{W}. A(\mu)(\mathcal{W}) \subseteq \text{safe}_n((\mu, s), B)(\mathcal{W}). \\ \models A \Rightarrow B &\stackrel{\text{def}}{=} \forall \mu, \mathcal{W}. A(\mu)(\mathcal{W}) \subseteq vs(B(\mu))(\mathcal{W}) \end{aligned}$$

$$\begin{aligned} \llbracket \vdash \{P\} s \{Q\} \rrbracket &\stackrel{\text{def}}{=} \forall \eta. \models_p \{ \lambda \mu. \lambda \mathcal{W}. \llbracket P \rrbracket_{\mu}^{\eta}(\mathcal{W}) \} s \{ \lambda \mu. \lambda \mathcal{W}. \llbracket Q \rrbracket_{\mu}^{\eta}(\mathcal{W}) \} \\ \llbracket \vdash \{P\} s \{Q\} \rrbracket^{\text{np}} &\stackrel{\text{def}}{=} \forall \eta. \models_{np} \{ \lambda \mu. \lambda \mathcal{W}. \llbracket P \rrbracket_{\mu}^{\eta}(\mathcal{W}) \} s \{ \lambda \mu. \lambda \mathcal{W}. \llbracket Q \rrbracket_{\mu}^{\eta}(\mathcal{W}) \} \\ \llbracket \vdash P \Rightarrow Q \rrbracket &\stackrel{\text{def}}{=} \forall \eta. \models (\lambda \mu. \lambda \mathcal{W}. \llbracket P \rrbracket_{\mu}^{\eta}(\mathcal{W})) \Rightarrow (\lambda \mu. \lambda \mathcal{W}. \llbracket Q \rrbracket_{\mu}^{\eta}(\mathcal{W})) \end{aligned}$$

### 3.3 Non-promising safety implies promising safety

**Lemma 20.**

$$\begin{aligned} \langle \langle \sigma_1, V_1 \rangle, gconf_1 \rangle &\xrightarrow{\text{NP}^*}_i \langle \langle \sigma_2, V_2 \rangle, gconf_2 \rangle \xrightarrow{\text{NP}^*}_i \langle \langle \sigma_3, V_3 \rangle, gconf_3 \rangle \wedge gconf_1.P \subseteq gconf_1.M \\ \Rightarrow \text{written}(gconf_1, gconf_3) &\subseteq \text{written}(gconf_1, gconf_2) \cup \text{written}(gconf_2, gconf_3) \end{aligned}$$

**Lemma 21.**

$$\begin{aligned} gconf \in [r_F[i \mapsto r \bullet f], u, \mathcal{W}]_{\{i\}} \wedge (gconf, V, r) \in \text{npsafe}_{(n+1,k)}(\sigma, B)(\mathcal{W}) \wedge \\ m \in \text{written}(gconf, gconf') \wedge \langle \langle \sigma, V \rangle, gconf \rangle &\xrightarrow{\text{NP}^k}_i \langle \langle \sigma', V' \rangle, gconf' \rangle \\ \Rightarrow \text{writeAllowed}(r \bullet \text{canAcq}(r, u), m) \end{aligned}$$

*Proof.* By induction on  $k$ .

- Case  $k = 0$ : by assumption  $gconf'.M \neq gconf.M$  or  $gconf'.P \neq gconf.P$  which contradicts  $gconf = gconf'$ .

- Case  $k > 0$ : then there exists  $\sigma''$ ,  $V''$ , and  $gconf''$  such that

$$\langle \langle \sigma, V \rangle, gconf \rangle \xrightarrow{i}^{\text{NP}} \langle \langle \sigma'', V'' \rangle, gconf'' \rangle \xrightarrow{i}^{\text{NP}^{k-1}} \langle \langle \sigma', V' \rangle, gconf' \rangle$$

From the npsafe assumption there exists  $r''$ ,  $u''$ , and  $\mathcal{W}'' \geq \mathcal{W}$  such that

$$gconf'' \in [r_F[i \mapsto r'' \bullet f], u'', \mathcal{W}'']_{\{i\}} \quad (gconf'', V'', r'') \in \text{npsafe}_{(n+1, k-1)}(\sigma'', B)(\mathcal{W}'')$$

$r'' \bullet \text{canAcq}(r'', u'') \leq_o r \bullet \text{canAcq}(r, u)$ ,  $\text{writesAllowed}(r, \text{written}(gconf, gconf''))$ .

- Case  $m \in \text{written}(gconf, gconf'')$ : Then it follows from the

$$\text{writesAllowed}(r, \text{written}(gconf, gconf''))$$

assumption that  $\text{writeAllowed}(r, m)$  and thus, by upwards-closure that  $\text{writeAllowed}(r \bullet \text{canAcq}(r, u), m)$ .

- Case  $m \notin \text{written}(gconf, gconf'')$ : Then by Lemma 20,  $m \in \text{written}(gconf'', gconf')$ . By Lemma 16 it suffices to prove that

$$\text{writeAllowed}(r'' \bullet \text{canAcq}(r'', u''), m)$$

which follows from the induction hypothesis. □

**Lemma 22.**

$$pln(m) \Rightarrow \forall x \in PLoc. \text{last}(M(x) \setminus P) = \text{last}((M \leftarrow m)(x) \setminus (P \leftarrow m))$$

where  $\leftarrow \in \{\leftarrow^{\Delta}, \leftarrow^{\Sigma}, \leftarrow^{\cup}\}$ .

*Proof.*

- Case  $\leftarrow^{\Delta}$ : then  $M' = M \leftarrow m = M \cup \{m\}$ ,  $P' = P \leftarrow m = P \cup \{m\}$ ,  $\{m\} \# M$  and  $\{m\} \# P$ . Hence,  $\text{last}(M'|_x \setminus P') = \text{last}(M|_x \setminus M)$ .
- Case  $\leftarrow^{\Sigma}$ : then there exists  $m', m''$  such that  $M' = M \leftarrow m = (M \setminus \{m'\}) \cup \{m, m''\}$ ,  $P' = P \leftarrow m = (P \setminus \{m'\}) \cup \{m, m''\}$ , where  $m' \in M \cap P$  and  $m.\text{loc} = m'.\text{loc} = m''.\text{loc}$ . Hence,  $\text{last}(M'|_x \setminus P') = \text{last}(M|_x \setminus M)$ .
- Case  $\leftarrow^{\cup}$ : then there exists an  $m'$  such that  $M' = M \leftarrow m = (M \setminus \{m'\}) \cup \{m\}$ ,  $P' = P \leftarrow m = (P \setminus \{m'\}) \cup \{m\}$ , where  $m' \in M \cap P$  and  $m.\text{loc} = m'.\text{loc}$ . Hence,  $\text{last}(M'|_x \setminus P') = \text{last}(M|_x \setminus M)$ . □

**Lemma 23.**

$$(M, P) \in [r_F, u, \mathcal{W}]_{\emptyset} \wedge pln(m) \Rightarrow (M \leftarrow m, P \leftarrow m) \in [r_F, u, \mathcal{W}]_{\emptyset}$$

where  $\leftarrow \in \{\leftarrow^{\Delta}, \leftarrow^{\Sigma}, \leftarrow^{\cup}\}$ .

*Proof.* Follows from 22. □

**Lemma 24.**

$$\begin{aligned} (M, P) \in [r_F, u, \mathcal{W}]_{\emptyset} \wedge m.\text{mod} = \text{rlx} \wedge \text{validPromise}(r_F, u, m) \wedge \\ (gconf_{\perp}, V_{\perp}, \varepsilon) \in \llbracket \mathcal{W}.\text{rel}(m.\text{loc})(m.\text{val}) \rrbracket_{\emptyset}(|\mathcal{W}|) \\ \Rightarrow (M', P') \in [r_F, u, \mathcal{W}]_{\emptyset} \end{aligned}$$

where  $\leftarrow \in \{\leftarrow^{\Delta}, \leftarrow^{\Sigma}, \leftarrow^{\cup}\}$ ,  $M' = M \leftarrow m$  and  $P' = P \leftarrow m$ .

*Proof.* By case-analysis of  $\leftrightarrow$ .

- Case  $\xleftrightarrow{\Delta}$ : then  $M' = M \leftrightarrow m = M \cup \{m\}$ ,  $P' = P \leftrightarrow m = P \cup \{m\}$ ,  $\{m\} \# M$  and  $\{m\} \# P$ . From the validPromise assumption it follows that  $\text{validPromises}_\emptyset(r_F, u, P')$ . It follows by upwards-closure in the observations component that all existing relaxed messages satisfy the required predicate. It thus remains to prove that newly promised message does as well and that the corresponding predicate is pure, which follows directly from the assumptions.
- Case  $\xleftrightarrow{\Sigma}$ : then there exists  $m', m''$  such that  $M' = M \leftrightarrow m = (M \setminus \{m'\}) \cup \{m, m''\}$ ,  $P' = P \leftrightarrow m = (P \setminus \{m'\}) \cup \{m, m''\}$ , where  $m' \in M \cap P$ ,  $m.\text{loc} = m'.\text{loc} = m''.\text{loc}$ ,  $m'.\text{val} = m''.\text{val}$ ,  $m'.\text{view} = m''.\text{view}$ ,  $m.\text{tid} = m'.\text{tid} = m''.\text{tid}$ , and  $m.\text{mod} = m'.\text{mod} = m''.\text{mod} = \text{rlx}$ .

It follows from the erasure assumption that

$$(gconf_\perp, V_\perp, \varepsilon) \in \llbracket \mathcal{W}.\text{rel}(m'.\text{loc})(m'.\text{val}) \rrbracket(|\mathcal{W}|)$$

from which it follows that

$$(gconf_\perp, V_\perp, \varepsilon) \in \llbracket \mathcal{W}.\text{rel}(m''.\text{loc})(m''.\text{val}) \rrbracket(|\mathcal{W}|)$$

It follows that all the assertions associated with relaxed messages hold.

It thus remains to prove that all outstanding promises are valid. Validity for  $m$  follows from the lemma assumptions and validity of  $m''$  follows from validity of  $m'$  which follows from the erasure assumption.

- Case  $\xleftrightarrow{\cup}$ : then there exists  $m'$  such that  $M' = M \leftrightarrow m = (M \setminus \{m'\}) \cup \{m\}$ ,  $P' = P \leftrightarrow m = (P \setminus \{m'\}) \cup \{m\}$ , where  $m' \in M \cap P$ ,  $m.\text{loc} = m'.\text{loc}$ ,  $m.\text{val} = m'.\text{val}$ ,  $m.\text{view} = m'.\text{view}$  and  $m.\text{tid} = m'.\text{tid}$ .

It follows directly from the lemma assumptions that the  $m$  promise is valid and that the assertion associated with  $m$  holds. □

**Lemma 25** (Non-promising safety implies safety).

$$\forall n, \sigma, B, \mathcal{W}. \text{npsafe}_{(n+1,0)}(\sigma, B)(\mathcal{W}) \subseteq \text{safe}_n(\sigma, B)(\mathcal{W})$$

*Proof.* By induction on  $n$ . The  $n = 0$  case holds by definition. Suppose that the claim holds for  $n$ , and let  $\sigma, r$  such that  $(gconf, V, r) \in \text{npsafe}_{(n+2,0)}(\sigma, B)(\mathcal{W})$ . We show that  $(gconf, V, r) \in \text{safe}_{n+1}(\sigma, B)(\mathcal{W})$  holds. The first conjunct holds since  $(gconf, V, r) \in \text{npsafe}_{n+1,1}(\sigma, B)(\mathcal{W})$ .

For the second conjunct, assume  $gconf' \in [r_F[i \mapsto r], u, \mathcal{W}]_\emptyset$ ,  $gconf \leq gconf'$  and

$$\langle \langle \sigma, V \rangle, gconf' \rangle \xrightarrow{\beta}_i \langle \langle \sigma', V' \rangle, gconf'' \rangle$$

- Case  $\beta = \text{promise}$ : then there exists an  $m$  such that  $\sigma' = \sigma$ ,  $V' = V$ ,  $\langle \langle \sigma', V' \rangle, gconf'' \rangle$  is consistent w.r.t.  $i$  and

$$gconf''.M = gconf'.M \leftrightarrow m \qquad gconf''.P = gconf'.P \leftrightarrow m \qquad m.\text{tid} = i$$

$\leftrightarrow \in \{ \xleftrightarrow{\Delta}, \xleftrightarrow{\Sigma}, \xleftrightarrow{\cup} \}$ .

Hence, by the definition of consistency there exists  $\sigma'', V''$ , and  $gconf'''$  such that  $gconf'''.P(i) = \emptyset$  and

$$\langle \langle \sigma', V' \rangle, gconf'' \rangle \xrightarrow{\text{NP}^*}_i \langle \langle \sigma'', V'' \rangle, gconf''' \rangle$$

It follows by Corollary 1 that there exists a  $k$  such that

$$\langle \langle \sigma, V \rangle, gconf' \rangle \xrightarrow{\text{NP}^k}_i \langle \langle \sigma'', V'' \rangle, gconf''' \rangle$$

Since  $m \in gconf'' . M$ ,  $gconf''' . P(i) = \emptyset$  and  $m.tid = i$  it follows that  $m \in \text{written}(gconf', gconf''')$ .

By downwards-closure in the step-index and upwards-closure in  $gconf$  it follows that

$$(gconf', V, r) \in \text{npsafe}_{(n+1, k+1)}(\sigma, B)(\mathcal{W})$$

and by promise weakening it follows that  $gconf' \in [r_F[i \mapsto r], u, \mathcal{W}]_{\{i\}}$ . Hence, by Lemma 21,  $\text{writeAllowed}(r \bullet \text{canAcq}(r, u), m)$ .

Pick  $u' = u$ ,  $\mathcal{W}' = \mathcal{W}$  and  $r' = r$ . It remains to prove that

$$gconf'' \in [r_F[i \mapsto r], u, \mathcal{W}]_{\emptyset} \quad (gconf'', V, r) \in \text{safe}_n(\sigma, B)(\mathcal{W})$$

The second proof obligation follows from the induction hypothesis.

- Case  $m.\text{mod} = \text{rlx}$ : we proceed using Lemma 24, which requires us to prove that the promise is allowed:  $\text{validPromise}(r_F[i \mapsto r], u, m)$  and that the assertion associated with the write holds:  $(gconf_{\perp}, V_{\perp}, \varepsilon) \in \llbracket \mathcal{W}.\text{rel}(m.\text{loc})(m.\text{val}) \rrbracket_{\emptyset}(|\mathcal{W}|)$ . We have already shown that the promise is allowed.

Applying the  $\text{npsafe}$  assumption  $k$ -times it follows that there exists  $r', u' \mathcal{W}' \geq \mathcal{W}$  such that  $gconf'' \in [r_F[i \mapsto r'], u', \mathcal{W}']_{\{i\}}$ . Thus,

$$(gconf_{\perp}, V_{\perp}, \varepsilon) \in \llbracket \mathcal{W}'.\text{rel}(m.\text{loc})(m.\text{val}) \rrbracket_{\emptyset}(|\mathcal{W}'|)$$

By world ordering it follows that  $\mathcal{W}.\text{rel}(\text{loc}) = \mathcal{W}'.\text{rel}(\text{loc})$  and  $|\mathcal{W}| = |\mathcal{W}'|$  and thus

$$(gconf_{\perp}, V_{\perp}, \varepsilon) \in \llbracket \mathcal{W}.\text{rel}(m.\text{loc})(m.\text{val}) \rrbracket_{\emptyset}(|\mathcal{W}|)$$

as required.

- Case  $m.\text{mod} = \text{pln}$ : by Lemma 23 and the fact that  $\text{writeAllowed}(r \bullet \text{canAcq}(r, u), m)$ .
- Case  $m.\text{mod} = \text{rel}$ : contradiction, as release write promises cannot be fulfilled.
- Case  $\beta = \text{NP}$ : then there exists a  $gconf''''$  such that  $\langle \langle \sigma', V' \rangle, gconf'''' \rangle$  is consistent w.r.t.  $i$ , such that  $gconf'' . M = gconf'''' . M$ ,  $gconf'' . P = gconf'''' . P$ , and

$$\langle \langle \sigma, V \rangle, gconf' \rangle \xrightarrow{i}^{\text{NP}^+} \langle \langle \sigma', V' \rangle, gconf'''' \rangle$$

By the definition of consistency it follows that there exists  $\sigma'', V'', gconf''''', k_1$ , and  $k_2$  such that  $gconf'''' . P(i) = \emptyset$  and

$$\langle \langle \sigma, V \rangle, gconf' \rangle \xrightarrow{i}^{\text{NP}^{k_1+1}} \langle \langle \sigma', V' \rangle, gconf'''' \rangle \xrightarrow{i}^{\text{NP}^{k_2}} \langle \langle \sigma'', V'' \rangle, gconf'''' \rangle$$

By downwards-closure of  $\text{npsafe}$  it follows that

$$(gconf', V, r) \in \text{npsafe}_{(n+1, k_1+k_2+2)}(\sigma, B)(\mathcal{W})$$

It follows by  $k_1 + 1$  applications of  $\text{npsafe}$  that there exists  $r', u'$  and  $\mathcal{W}' \geq \mathcal{W}$  such that

$$gconf'''' \in [r_F[i \mapsto r'], u', \mathcal{W}']_{\{i\}} \quad (gconf''''', V', r') \in \text{npsafe}_{(n+1, k_2+1)}(\sigma', B)(\mathcal{W}')$$

By downwards-closure of  $\text{npsafe}$  and the induction hypothesis, it thus follows that  $(gconf''''', V', r') \in \text{safe}_n(\sigma', B)$ .

It thus remains to show that  $\text{promisesAllowed}_{\emptyset}(r_F[i \mapsto r'], u', gconf'''' . P)$ . Let  $m \in gconf'''' . P$ .

- Case  $m.\text{tid} = i$ : then we have to prove that

$$\text{validPromise}(r', u', m)$$

This follows from Lemma 21, since  $m \in \text{written}(gconf''', gconf'''' )$ .

- Case  $m.\text{tid} \neq i$ : then the conclusion follows from

$$\text{promisesAllowed}_{\{i\}}(r_F[i \mapsto r'], u', gconf'''.P).$$

□

### 3.4 Adequacy of promising safety

**Lemma 26.**

$$\begin{aligned} \forall I \in \mathcal{P}_{fin}(TId). \forall r, V, B. \\ gconf \in [r_F[i \in I \mapsto r_i], u, \mathcal{W}]_{\emptyset} \wedge \\ \forall i \in I. (gconf, V_i, r_i) \in vs(B_i)(\mathcal{W}) \\ \Rightarrow \exists r', u', \mathcal{W}' \geq \mathcal{W}. \\ gconf \in [r_F[i \in I \mapsto r'_i], u', \mathcal{W}']_{\emptyset} \wedge \\ \forall i \in I. (gconf, V_i, r'_i) \in B_i(\mathcal{W}') \end{aligned}$$

*Proof.* By induction on  $|I|$ .

- Case  $|I| = 0$ : follows trivially by picking  $r' = r$ ,  $u' = u$  and  $\mathcal{W}' = \mathcal{W}$ .
- Case  $|I| > 0$ : pick a  $j \in I$ . From the assumption that  $(gconf, V_j, r_j) \in vs(B_j)(\mathcal{W})$  it follows that there exists  $r'_j$ ,  $u'$  and  $\mathcal{W}' \geq \mathcal{W}$  such that

$$gconf \in [(r_F[j \mapsto r'_j])[i \in I \setminus \{j\} \mapsto r_i], u', \mathcal{W}']_{\emptyset} \quad (gconf, V_j, r'_j) \in B_j(\mathcal{W}')$$

Furthermore, by upwards-closure in the world component we have that

$$\forall i \in I \setminus \{j\}. (gconf, V_i, r_i) \in vs(B_i)(\mathcal{W}')$$

From the induction hypothesis it thus follows that there exists  $r''$ ,  $u''$  and  $\mathcal{W}'' \geq \mathcal{W}$  such that

$$gconf \in [(r_F[j \mapsto r'_j])[i \in I \setminus \{j\} \mapsto r''_i], u'', \mathcal{W}'']_{\emptyset}$$

and

$$\forall i \in I \setminus \{j\}. (gconf, V_i, r''_i) \in B_i(\mathcal{W}'')$$

The conclusion now follows by picking  $u' = u''$ ,  $\mathcal{W}' = \mathcal{W}''$ , and

$$r' = [j \mapsto r'_j, i \in I \setminus \{j\} \mapsto r''_i]$$

since  $(gconf, V_k, r'_j) \in B_j(\mathcal{W}'')$  by upwards-closure.

□

**Lemma 27** (Adequacy). If

1.  $(gconf, \Sigma(i).V, r(i)) \in \text{safe}_{n+1}(\Sigma(i).\sigma, B_i)(\mathcal{W})$  for all  $i \in \text{dom}(\Sigma)$ ,
2.  $gconf \in [r, u, \mathcal{W}]_{\emptyset}$ ,

3.  $(\Sigma, gconf) \Longrightarrow^n (\Sigma', gconf')$ ,
4.  $\forall i \in \text{dom}(\Sigma'). \Sigma'(i).s = \mathbf{skip}$

then there exists  $r', u'$  and  $\mathcal{W}' \geq \mathcal{W}$  such that

1.  $gconf' \in [r', u', \mathcal{W}']_{\emptyset}$ ,
2. and  $(gconf', \Sigma'.V(i), r'(i)) \in \llbracket B_i \rrbracket_{\Sigma'(i), \mu}(\mathcal{W}')$  for all  $i \in \text{dom}(\Sigma')$ .

*Proof.* By induction on  $n$ .

- Case  $n = 0$ : then  $(\Sigma, gconf) = (\Sigma', gconf')$  and we can simply pick  $r' = r$ ,  $u' = u$  and  $\mathcal{W}' = \mathcal{W}$ . Since  $\Sigma(i).s = \mathbf{skip}$  for every  $i \in \text{dom}(\Sigma)$  it follows from the assumed safety that  $(gconf', \Sigma(i).V, r(i)) \in \text{vs}(B_i)(\mathcal{W})$  for all  $i \in \text{dom}(\Sigma)$ . The conclusion now follows from Lemma 26.
- Case  $n > 0$ : then there exists  $\Sigma''$  and  $gconf''$  such that

$$(\Sigma, gconf) \Longrightarrow_i (\Sigma'', gconf'') \Longrightarrow^{n-1} (\Sigma', gconf')$$

From the safety assumption it follows that there exists  $r', u'$  and  $\mathcal{W}' \geq \mathcal{W}$  such that  $gconf'' \in [r[i \mapsto r'], u', \mathcal{W}']_{\emptyset}$  and

$$(gconf'', \Sigma''(i).V, r') \in \text{safe}_{n-1}(\Sigma''(i).s, B_i)(\mathcal{W}')$$

The conclusion follows by the induction hypothesis and upwards-closure of safety and erasure in the world component and upwards-closure of safety in  $gconf$ .

□

## 4 Soundness

In this section we prove soundness of the syntactic proof system with respect to the non-promising semantics.

### 4.1 Structural rules

**Lemma 28** (Soundness of sequential composition).

$$\forall n, m. P(n, m)$$

where  $P(n, m)$  is defined as follows

$$\begin{aligned} & \forall r_1, \mu_1, s_1, s_2, \mathcal{W}_1. \\ & (\forall \mu_2, \mathcal{W}_2 \geq \mathcal{W}_1. B(\mu_2)(\mathcal{W}_2) \subseteq \text{npsafe}_{(n,m)}((\mu_2, s_2), C)(\mathcal{W}_2)) \\ & \Rightarrow \text{npsafe}_{(n,m)}^i((\mu_1, s_1), B)(\mathcal{W}_1) \subseteq \text{npsafe}_{(n,m)}((\mu_1, s_1; s_2), C)(\mathcal{W}_1) \end{aligned}$$

*Proof.* By induction on  $(n, m)$ .

Assume  $P(n', m')$  holds for all  $(n', m') < (n, m)$ .

- Case  $n = 0$ : then  $P(n, m)$  holds vacuously.
- Case  $n > 0$  and  $m = 0$ : then we can assume that

$$\forall k. (gconf, V, r) \in \text{npsafe}_{(n-1,k)}((\mu_1, s_1), B)(\mathcal{W}_1)$$

and

$$\forall \mu_2. \forall \mathcal{W}_2 \geq \mathcal{W}_1. \forall k. B(\mu_2)(\mathcal{W}_2) \subseteq \text{npsafe}_{(n-1,k)}((\mu_2, s_2), C)(\mathcal{W}_2)$$

and must prove  $\forall k. (gconf, V, r) \in \text{npsafe}_{(n-1,k)}((\mu_1, s_1; s_2), C)$ . This follows easily by the induction hypothesis.

- Case  $n > 0$  and  $m > 0$ : Assume

$$(gconf, V, r) \in \text{npsafe}_{(n,m)}((\mu_1, s_1), B)(\mathcal{W}_1)$$

and

$$\forall \mu_2, \mathcal{W}_2 \geq \mathcal{W}_1. B(\mu_2)(\mathcal{W}_2) \subseteq \text{npsafe}_{(n,m)}^i((\mu_2, s_2), C)(\mathcal{W}_2).$$

Since  $s_1; s_2 \neq \mathbf{skip}$  it suffices to consider the case where  $gconf \in [r_F[i \mapsto r \bullet f], u, \mathcal{W}]_{\{i\}}$ ,  $\mathcal{W} \geq \mathcal{W}_1$ ,  $\text{wf}_{\text{conf}}(i, gconf, V)$ ,  $\text{wf}_{\text{conf}}(i, gconf', V')$  and

$$\langle \langle (\mu_1, s_1; s_2), V \rangle, gconf \rangle \xrightarrow{i}_{\text{NP}} \langle \langle (\mu', s'), V' \rangle, gconf' \rangle$$

- Case  $s_1 = \mathbf{skip}$ : then  $\mu' = \mu_1$ ,  $s' = s_2$ ,  $V' = V$  and  $gconf' = gconf$ . Hence,  $(gconf, V, r) \in \text{vs}(B(\mu_1))(\mathcal{W})$ . There thus exists  $r', u'$  and  $\mathcal{W}' \geq \mathcal{W}$  such that

$$(gconf, V, r') \in B(\mu_1)(\mathcal{W}') \quad gconf \in [r_F[i \mapsto r' \bullet f], u', \mathcal{W}']_{\{i\}} \quad r' \leq_o r \quad r' \bullet \text{canAcq}(r', u') \leq_o r \bullet \text{canAcq}(r, u)$$

and thus  $(gconf, V, r') \in \text{npsafe}_{(n,m)}((\mu', s'), C)(\mathcal{W}')$ . The conclusion now follows by picking  $r'$ ,  $u'$  and  $\mathcal{W}'$ , since  $\text{written}(gconf, gconf') = \emptyset$ .



- Case  $s_1 \neq \mathbf{skip}$ : then there exists an  $s'_1$  such that

$$\langle\langle(\mu_1, s_1), V\rangle, gconf\rangle \xrightarrow{\text{NP}}_i \langle\langle(\mu', s'_1), V'\rangle, gconf'\rangle$$

and  $s' = s'_1; s_2$ . Hence, by the assumed safety for  $s_1$  there exists an  $r', u'$  and  $\mathcal{W}' \geq \mathcal{W}$  such that

$$gconf' \in [r_F[i \mapsto r' \bullet f], u', \mathcal{W}]_{\{i\}} \quad (gconf', V', r') \in \text{npsafe}_{(n, m-1)}((\mu', s'_1), B)(\mathcal{W}')$$

and

$$r' \bullet \text{canAcq}(r', u') \leq_o r \bullet \text{canAcq}(r, u) \quad \text{writesAllowed}(r, \text{written}(gconf, gconf'))$$

By downwards-closure and the induction hypothesis, it follows that

$$(gconf', V', r') \in \text{npsafe}_{(n, m-1)}((\mu', s'_1; s_2), C)(\mathcal{W}').$$

Now the conclusion follows easily by picking  $r', u'$  and  $\mathcal{W}'$ .

□

**Lemma 29** (Soundness of while-rule).

$$\llbracket \vdash \{P \wedge e = \top\} s \{P\} \rrbracket^{\text{np}} \Rightarrow \llbracket \vdash \{P\} \mathbf{while} \ e \ \mathbf{do} \ s \ \{P \wedge e = \perp\} \rrbracket^{\text{np}}$$

*Proof.* Assume  $\llbracket \vdash \{P \wedge e = \top\} s \{P\} \rrbracket^{\text{np}}$ . We will prove that  $\forall n, m. P(n, m)$  holds by induction on  $(n, m)$ , where  $P(n, m)$  is defined as follows.

$$P(n, m) \stackrel{\text{def}}{=} \forall \mu, \eta, \mathcal{W}. \llbracket P \rrbracket_\mu^\eta(\mathcal{W}) \subseteq \text{npsafe}_{(n, m)}((\mu, \mathbf{while} \ e \ \mathbf{do} \ s), \lambda \mu. \lambda \mathcal{W}. \llbracket P \wedge e = \perp \rrbracket_\mu^\eta(\mathcal{W}))(\mathcal{W})$$

Assume  $P(n', m')$  holds for all  $(n', m') < (n, m)$ . To show that  $P(n, m)$  holds, assume  $(gconf, V, r) \in \llbracket P \rrbracket_\mu^\eta(\mathcal{W})$ .

- Case  $n = 0$ : trivial.
- Case  $n > 0$  and  $m = 0$ : follows directly from the induction hypothesis.
- Case  $n > 0$  and  $m > 0$ : assume  $gconf \in [r_F[i \mapsto r \bullet f], u, \mathcal{W}]_{\{i\}}$  and

$$\langle\langle(\mu, \mathbf{while} \ e \ \mathbf{do} \ s), V\rangle, gconf\rangle \xrightarrow{\text{NP}}_i \langle\langle(\mu', s'), V'\rangle, gconf'\rangle$$

Then  $\mu = \mu', V' = V$ , and  $gconf' = gconf$ .

- Case  $\llbracket e \rrbracket_\mu = \top$ : then  $s' = s; \mathbf{while} \ e \ \mathbf{do} \ s$ . By assumption,  $P(n, m-1)$  and thus, by Lemma 28,

$$\begin{aligned} & \text{npsafe}_{(n, m-1)}((\mu, s), \lambda \mu. \lambda \mathcal{W}. \llbracket P \rrbracket_\mu^\eta(\mathcal{W}))(\mathcal{W}) \\ & \subseteq \text{npsafe}_{(n, m-1)}((\mu, s; \mathbf{while} \ e \ \mathbf{do} \ s), \lambda \mu. \lambda \mathcal{W}. \llbracket P \wedge e = \perp \rrbracket_\mu^\eta(\mathcal{W}))(\mathcal{W}) \end{aligned}$$

Furthermore, from the assumed triple for the loop body it follows that

$$(gconf, V, r) \in \text{npsafe}_{(n, m-1)}((\mu, s), \lambda \mu. \lambda \mathcal{W}. \llbracket P \rrbracket_\mu^\eta(\mathcal{W}))(\mathcal{W}).$$

Now the conclusion follows easily by picking  $r, u$  and  $\mathcal{W}$ .

- Case  $\llbracket e \rrbracket_\mu = \perp$ : then  $s' = \mathbf{skip}$  and the conclusion follows easily as  $(gconf, V, r) \in \llbracket P \wedge e = \perp \rrbracket_\mu^\eta(\mathcal{W}) \subseteq \text{vs}(\lambda \mathcal{W}. \llbracket P \wedge e = \perp \rrbracket_\mu^\eta(\mathcal{W}))(\mathcal{W})$ .

□

**Lemma 30** (Soundness of precondition strengthening).

$$\models A \Rightarrow A' \wedge \models_{np} \{A'\} s \{B\} \Rightarrow \models_{np} \{A\} s \{B\}$$

*Proof.* Assume

$$(gconf, V, r) \in A(\mu)(\mathcal{W}) \quad gconf \in \lfloor r_F[i \mapsto r \bullet], u, \{i\} \rfloor_{\mathcal{W}} \quad \langle \langle (\mu, s), V \rangle, gconf \rangle \xrightarrow{NP}_i \langle \langle \sigma', V' \rangle, gconf' \rangle$$

From the assumed view-shift it follows that there exists  $r', u'$  and  $\mathcal{W}' \geq \mathcal{W}$  such that  $r' \leq_o r$ ,  $(gconf, V, r') \in \llbracket A' \rrbracket_{\mu}(\mathcal{W}')$ , and

$$gconf \in \lfloor r_F[i \mapsto r' \bullet f], u', \mathcal{W}' \rfloor_{\{i\}} \quad r' \bullet \text{canAcq}(r', u') \leq_o r \bullet \text{canAcq}(r, u)$$

Hence, from the assumed triple it follows that there exists  $r'', u''$  and  $\mathcal{W}'' \geq \mathcal{W}'$  such that  $(gconf', V', r'') \in \text{npsafe}_{(n,m)}(\sigma', B)(\mathcal{W}'')$ ,

$$gconf' \in \lfloor r_F[i \mapsto r'' \bullet f], u'', \mathcal{W}'' \rfloor_{\{i\}} \quad r'' \bullet \text{canAcq}(r'', u'') \leq_o r' \bullet \text{canAcq}(r', u')$$

and  $\text{writesAllowed}(r', \text{written}(gconf, gconf'))$ . Since  $\leq_o$  is transitive it follows that

$$r'' \bullet \text{canAcq}(r'', u'') \leq_o r \bullet \text{canAcq}(r, u).$$

It thus remains to prove that  $\text{writesAllowed}(r, \text{written}(gconf, gconf'))$ . This follows from  $r' \leq_o r$  by Lemma 16 and the fact that  $\text{writesAllowed}(r', \text{written}(gconf, gconf'))$ .  $\square$

**Lemma 31.**

$$\forall \mathcal{W}. \text{vs}(\text{vs}(B))(\mathcal{W}) \subseteq \text{vs}(B)(\mathcal{W})$$

*Proof.* Follows from transitivity of  $\leq_o$  and the order on worlds.  $\square$

**Lemma 32.**

$$\forall \mathcal{W}. \text{npsafe}_{(n,m)}(\sigma, \lambda\mu. \text{vs}(B(\mu)))(\mathcal{W}) \subseteq \text{npsafe}_{(n,m)}(\sigma, B)(\mathcal{W})$$

*Proof.* By well-founded induction, using the induction hypothesis for reduction steps and Lemma 31 for the skip case.  $\square$

**Lemma 33.**

$$\begin{aligned} (\forall \mu, \mathcal{W}. p(\mu)(\mathcal{W}) \subseteq q(\mu)(\mathcal{W})) \Rightarrow \\ \forall n, m, \sigma, \mathcal{W}. \text{npsafe}_{(n,m)}(\sigma, p)(\mathcal{W}) \subseteq \text{npsafe}_{(n,m)}(\sigma, q)(\mathcal{W}) \end{aligned}$$

**Lemma 34** (Soundness of postcondition weakening).

$$\begin{aligned} \forall B, B'. \models B' \Rightarrow B \Rightarrow \\ \forall n, m, \sigma, \mathcal{W}. \text{npsafe}_{(n,m)}(\sigma, B')(\mathcal{W}) \subseteq \text{npsafe}_{(n,m)}(\sigma, B)(\mathcal{W}) \end{aligned}$$

*Proof.* By Lemmas 33 and 32 and the definition of viewshifts it follows that

$$\begin{aligned} \text{npsafe}_{(n,m)}(\sigma, B')(\mathcal{W}) \subseteq \text{npsafe}_{(n,m)}(\sigma, \lambda\mu. \text{vs}(B(\mu)))(\mathcal{W}) \\ \subseteq \text{npsafe}_{(n,m)}(\sigma, B)(\mathcal{W}) \end{aligned}$$

as required.  $\square$

**Lemma 35.**

$$(\forall \mathcal{W}. p(\mathcal{W}) \subseteq q(\mathcal{W})) \Rightarrow \forall \mathcal{W}. \text{vs}(p)(\mathcal{W}) \subseteq \text{vs}(q)(\mathcal{W})$$

## 4.2 View-shift rules

**Lemma 36** (Splitting of acquire permissions).

$$\llbracket \text{Acq}(x, \lambda v. \phi_1(v) * \phi_2(v)) \rrbracket \Rightarrow \text{Acq}(x, \phi_1) * \text{Acq}(x, \phi_2)$$

*Proof.* Assume  $(gconf_1, V_1, r_1) \leq (gconf, V, r)$  and

$$(gconf_1, V_1, r_1) \in \llbracket \text{Acq}(x, \lambda v. \phi_1(v) * \phi_2(v)) \rrbracket_\mu^\eta(\mathcal{W}) \quad gconf \in [r_F[i \mapsto r \bullet f], u, \mathcal{W}]_T$$

Then there exists an  $\iota \in \text{dom}(\mathcal{W}.\text{acq}) \cap r.\text{acq}(\llbracket x \rrbracket_\mu^\eta)$  such that

$$\mathcal{W}.\text{acq}(\iota) = \lambda v. \phi_1(v) * \phi_2(v).$$

Hence, for every  $m \in gconf.M(\text{rel}, \llbracket x \rrbracket_\mu^\eta)$  we have that

$$(gconf, m.\text{view}, u(m)(\iota)) \in \llbracket \mathcal{W}.\text{acq}(\iota)(m.\text{val}) \rrbracket_\mu^\eta(\mathcal{W}) = \\ \llbracket \phi_1(m.\text{val}) * \phi_2(m.\text{val}) \rrbracket_\mu^\eta(\mathcal{W})$$

Hence, there exists  $(r_m^1)_{m \in gconf.M(\text{rel}, x)}$  and  $(r_m^2)_{m \in gconf.M(\text{rel}, \llbracket x \rrbracket_\mu^\eta)}$  such that

$$(m.\text{view}, r_m^1) \in \llbracket \phi_1(m.\text{val}) \rrbracket_\mu^\eta(\mathcal{W}) \quad (m.\text{view}, r_m^2) \in \llbracket \phi_2(m.\text{val}) \rrbracket_\mu^\eta(\mathcal{W})$$

and  $u(m)(\iota) = r_m^1 \bullet r_m^2$  for all  $m \in \text{npln}(gconf.M(x))$ .

Pick fresh  $\iota_1, \iota_2 \notin \text{dom}(\mathcal{W}.\text{acq})$  and let

$$\begin{aligned} \mathcal{W}' &= \mathcal{W}[\iota_1 \mapsto \phi_1, \iota_2 \mapsto \phi_2] \\ u' &= u[m \in \text{npln}(gconf.M(x)) \mapsto u(m)[\iota \mapsto \perp, \iota_1 \mapsto r_m^1, \iota_2 \mapsto r_m^2]] \\ r' &= r[\text{acq}(x) \mapsto (r.\text{acq}(x) \setminus \{\iota\}) \cup \{\iota_1, \iota_2\}] \end{aligned}$$

It remains to show that

$$(gconf, V, r') \in \llbracket \text{Acq}(x, \phi_1) * \text{Acq}(x, \phi_2) \rrbracket_\mu^\eta(\mathcal{W}') \quad gconf \in [r_F[i \mapsto r' \bullet f], u', \mathcal{W}']_T$$

and  $r' \bullet \text{canAcq}(r', u') \leq_o r \bullet \text{canAcq}(r, u)$ ,  $r' \leq_o r$ .

The first proof obligation is easily seen to hold. The second proof obligation reduces to proving that  $\text{promisesAllowed}_T(r_F[i \mapsto r' \bullet f], u', gconf.P)$ . Since  $\iota \notin r_F(j).\text{acq}$  for all  $j \neq i$  this reduces to proving that

$$\forall m \in gconf.P. m.\text{tid} = i \wedge i \notin T \Rightarrow \text{writeAllowed}(r' \bullet \text{canAcq}(r', u'), m)$$

This follows from the  $\text{promisesAllowed}_T(r_F[i \mapsto r \bullet f], u, gconf.P)$  assumption since  $\text{canAcq}(r', u') = \text{canAcq}(r, u)$ , by Lemma 14.

From  $\text{canAcq}(r', u') = \text{canAcq}(r, u)$  it also follows that  $r' \bullet \text{canAcq}(r', u') \leq_o r \bullet \text{canAcq}(r, u)$ .

It thus remains to prove that  $r' \leq_o r$ . This follows easily as  $r.\text{pln} = r'.\text{pln}$  and  $r.\text{wr} = r'.\text{wr}$ .  $\square$

**Lemma 37.**

$$\llbracket \vdash W^1(x, X) * O(x, a, t) \rrbracket \Rightarrow W^1(x, X) * O(x, a, t) * (a, t) \in X$$

*Proof.* Assume  $(gconf_1, V_1, r_1) \in \llbracket W^1(x, X) * O(x, a, t) \rrbracket_\mu^\eta(\mathcal{W})$ ,  $(gconf, V, r) \geq (gconf_1, V_1, r_1)$ ,  $\mathcal{W}' \geq \mathcal{W}$ ,  $gconf \in [r_F[i \mapsto r \bullet f], u, \mathcal{W}']_T \{i\}$  and  $\text{wf}_{\text{conf}}(i, gconf, V)$ .

Hence, there exists an  $m \in gconf_1.M$  such that  $m.\text{val} = \llbracket a \rrbracket_\mu^\eta$ ,  $m.\text{time} = \llbracket t \rrbracket_\mu^\eta$ ,  $m.\text{loc} = \llbracket x \rrbracket_\mu^\eta$ ,  $r.\text{wr}(\llbracket x \rrbracket_\mu^\eta) = (1, \llbracket X \rrbracket_\mu^\eta)$ , and  $\text{snd}(\max(\llbracket X \rrbracket_\mu^\eta)) \leq V.\text{rlx}(\llbracket x \rrbracket_\mu^\eta)$ . Hence, by erasure,  $\{(m.\text{val}, m.\text{time}) \mid m \in \text{seq}(gconf.M(\llbracket x \rrbracket_\mu^\eta) \setminus gconf.P)\} = X$ . It thus suffices to prove that  $m \notin gconf.P$ .

If  $m \in gconf.P$  and  $m.\text{tid} \neq i$  then it follows by erasure that  $\text{validPromise}(r_F, u, m)$ . Hence,  $\text{snd}((r_F(m.\text{tid}) \bullet \text{canAcq}(r_F(m.\text{tid}, u)).\text{wr}(\llbracket x \rrbracket_\mu^\eta))) > 0$ , which is a contradiction since  $\text{snd}(r.\text{wr}(\llbracket x \rrbracket_\mu^\eta)) = 1$ . If  $m \in gconf.P$  and  $m.\text{tid} = i$  then it follows by the  $\text{wf}_{\text{conf}}$  assumption that  $V.\text{rlx}(\llbracket x \rrbracket_\mu^\eta) < m.\text{time}$ , which is a contradiction.  $\square$

**Lemma 38.**

$$\begin{aligned} \forall \pi_1, \pi_2, X_1, X_2. \pi_1 + \pi_2 \leq 1 \wedge 0 < \pi_1 \leq 1 \wedge 0 < \pi_2 \leq 1 \Rightarrow \\ \llbracket \mathbf{W}_{\pi_1}(x, X_1) * \mathbf{W}_{\pi_2}(x, X_2) \vdash \mathbf{W}_{\pi_1 + \pi_2}(x, X_1 \cup X_2) \rrbracket \wedge \\ \llbracket \mathbf{W}_{\pi_1 + \pi_2}(x, X_1 \cup X_2) \vdash \mathbf{W}_{\pi_1}(x, X_1) * \mathbf{W}_{\pi_2}(x, X_2) \rrbracket \end{aligned}$$

*Proof.* Follow directly from the monoid definition.  $\square$

**Lemma 39.**

$$\llbracket \vdash \mathbf{W}^\pi(x, X) * (v, t) \in X * (v', t') \in X * v \neq v' \Rightarrow \mathbf{W}^\pi(x, X) * t \neq t' \rrbracket$$

*Proof.* Assume  $(gconf_1, V_1, r_1) \in \llbracket \mathbf{W}^\pi(x, X) * (v, t) \in X * (v', t') \in X * v \neq v' \rrbracket_\mu^\eta(\mathcal{W})$ ,  $(gconf, V, r) \geq (gconf_1, V_1, r_1)$ ,  $\mathcal{W}' \geq \mathcal{W}$ ,  $gconf \in \llbracket r_F[i \mapsto r \bullet f], u, \mathcal{W}' \rrbracket_{\{i\}}$  and  $wf_{\text{conf}}(i, gconf, V)$ .

By erasure,  $\llbracket X \rrbracket_\mu^\eta \subseteq gconf.M(\llbracket x \rrbracket_\mu^\eta)$  and the conclusion now follows from the definition of a memory.  $\square$

**Lemma 40.**

$$\llbracket \vdash \mathbf{W}^\pi(x, X) * (\_, t) \in X * (\_, t') \in X \Rightarrow \mathbf{W}^\pi(x, X) * t < t' \vee t = t' \vee t' < t \rrbracket$$

*Proof.* Follows from the assumption that the timestamp order is total.  $\square$

**Lemma 41.**

$$\llbracket \vdash \mathbf{W}^\pi(x, X) * (v, t) \in X \Rightarrow \mathbf{W}^\pi(x, X) * \mathbf{O}(x, v, t) \rrbracket$$

*Proof.* Assume  $(gconf_1, V_1, r_1) \in \llbracket \mathbf{W}^\pi(x, X) * (v, t) \in X \rrbracket_\mu^\eta(\mathcal{W})$ ,  $(gconf, V, r) \geq (gconf_1, V_1, r_1)$ ,  $\mathcal{W}' \geq \mathcal{W}$ ,  $gconf \in \llbracket r_F[i \mapsto r \bullet f], u, \mathcal{W}' \rrbracket_{\{i\}}$  and  $wf_{\text{conf}}(i, gconf, V)$ . By erasure,  $\llbracket X \rrbracket_\mu^\eta \subseteq gconf.M(\llbracket x \rrbracket_\mu^\eta)$ .  $\square$

### 4.3 Rules for release/acquire accesses

**Lemma 42.**

$$\begin{aligned} & \iota \in r_F(t).A(x) \cap \text{dom}(\mathcal{W}) \wedge gconf \in [r_F, u, \mathcal{W}]_T \wedge \iota' \text{ fresh for } u \text{ and } r_F \\ & \Rightarrow gconf \in [r_F[\iota \mapsto_x \iota'], u[\iota \mapsto_x \iota'], \mathcal{W}[\iota' \mapsto \mathcal{W}(\iota)[a \mapsto \top]]]_T \end{aligned}$$

*Proof.* Should follow from Lemmas 13 and 11. □

**Lemma 43.**

$$\begin{aligned} & m \in \text{dom}(u) \wedge \iota \in r_F(t).A(x) \cap \text{dom}(u(m)) \wedge \mathcal{W}(\iota)(m.\text{val}) = \top \wedge \\ & gconf \in [r_F, u, \mathcal{W}]_T \wedge \iota' \text{ fresh for } u \text{ and } r_F \\ & \Rightarrow gconf \in [r_F[t \mapsto r_F(t) \bullet u(m)(\iota)], u[m, \iota \mapsto \varepsilon], \mathcal{W}]_T \end{aligned}$$

**Lemma 44** (Soundness of acquire read).

$$\llbracket \vdash \{x = e * \text{Acq}(x, \phi) * \text{O}(x, \_, t)\} a := [e]_{\text{acq}} \{\exists t' \geq t. \text{Acq}(x, \phi[a \mapsto \top]) * \phi(a) * \text{O}(x, a, t')\} \rrbracket^{\text{np}}$$

where  $x$  and  $t$  are assumed to be specification variables.

*Proof.* Let  $(gconf, V, r) \in \llbracket x = e * \text{Acq}(x, \phi) * \text{O}(x, \_, t) \rrbracket_{\mu}^{\eta}(\mathcal{W})$ . Since  $a := [e]_{\text{acq}} \neq \mathbf{skip}$  it suffices to consider the case where

$$\langle (\mu, a := [e]_{\text{acq}}, V), gconf \rangle \xrightarrow{\text{NP}}_i \langle (\sigma', V'), gconf' \rangle,$$

and  $gconf \in [r_F[i \mapsto r \bullet f], u, \mathcal{W}' ]_{\{i\}}$ ,  $\mathcal{W}' \geq \mathcal{W}$ .

By the **THREAD: READ** rule it follows that there exists an  $m = \langle \eta(x) :_j^o v, V_m @(\_, t_m) \rangle \in M$  such that  $V.\text{rlx}(\eta(x)) \leq t_m$ ,  $gconf'.P = gconf.P$ ,  $gconf'.M = gconf.M$ ,  $V' = V \sqcup [pln : \{\eta(x) @ t_m\}, rlx : \{\eta(x) @ t_m\}] \sqcup V_m$  and  $\sigma' = (\mu[a \mapsto v], \mathbf{skip})$ .

From the  $(gconf, V, r) \in \llbracket x = e * \text{Acq}(x, \phi) * \text{O}(x, \_, t) \rrbracket_{\mu}^{\eta}(\mathcal{W})$  assumption it follows that there exists an  $\iota \in \text{dom}(\mathcal{W}.\text{acq}) \cap r.\text{acq}(\eta(x))$  such that  $\mathcal{W}.\text{acq}(\iota) = \phi$  and  $\eta(t) \leq V.\text{rlx}(\eta(x))$ . Hence,  $\eta(t) \leq t_m$ .

- Case  $o = \text{rlx}$ : it follows from the definition of erasure that  $(gconf_{\perp}, V_{\perp}, \varepsilon) \in \llbracket \mathcal{W}.\text{rel}(m.\text{loc})(m.\text{val}) \rrbracket_{\perp}^{\eta}(|\mathcal{W}'|)$ .

Pick an  $\iota'$  that is fresh for  $u$  and  $\mathcal{W}'$ . Pick  $u' = u[\iota \rightsquigarrow_{\eta(x)} \iota']$ ,  $r' = r[\iota \rightsquigarrow_{\eta(x)} \iota']$  and  $\mathcal{W}'' = \mathcal{W}'[\iota' \mapsto \phi[v \mapsto \top]]$ . Then it remains to prove that  $gconf' \in [r_F[i \mapsto r' \bullet f], u', |\mathcal{W}''| ]_{\{i\}}$ ,

$$(gconf', V', r') \in \llbracket \exists t' \geq t. \text{Rel}(x, \phi[a \mapsto \top]) * \phi(a) * \text{O}(x, a, t') \rrbracket_{\sigma', \mu}^{\eta}(|\mathcal{W}''|)$$

and  $r' \bullet \text{canAcq}(r', u') \leq_o r \bullet \text{canAcq}(r, u)$  and  $\text{writesAllowed}(r', \text{written}(gconf, gconf'))$ .

The erasure obligation follows by upwards-closure and Lemma 42. The two last obligations follow easily as  $r' \bullet \text{canAcq}(r', u') = r' \bullet \text{canAcq}(r, u) \leq_o r \bullet \text{canAcq}(r, u)$  by Lemma 13 and  $\text{written}(gconf, gconf') = \emptyset$ .

It thus remains to prove that

$$(gconf', V', r') \in \llbracket \exists t' \geq t. \text{Rel}(x, \phi[a \mapsto \top]) * \phi(a) * \text{O}(x, a, t') \rrbracket_{\sigma', \mu}^{\eta}(\mathcal{W}'').$$

It follows from the erasure assumption that

$$\forall v \vdash \mathcal{W}.\text{rel}(\eta(x))(v) \Rightarrow \otimes_{\iota \in t.\text{acq}(\eta(x))} \mathcal{W}.\text{acq}(\iota)(v)$$

where  $t$  is the composition of  $r_F[i \mapsto r \bullet f]$  and the resources from  $u$ . So, in particular,  $\iota \in t.\text{acq}(\eta(x))$  since by assumption  $\iota \in r.\text{acq}(\eta(x))$  and thus,

$$\vdash \mathcal{W}.\text{rel}(\eta(x))(v) \Rightarrow \phi(v)$$

It follows that  $(gconf_{\perp}, V_{\perp}, \varepsilon) \in \llbracket \phi(m.\text{val}) \rrbracket_{\emptyset}^{\square}(|\mathcal{W}'|)$ . By upwards-closure and the definition of the interpretation it follows easily that

$$(gconf', V', r') \in \llbracket \exists t' \geq t. \text{Rel}(x, \phi[a \mapsto \top]) * \phi(a) * \text{O}(x, a, t') \rrbracket_{\sigma', \mu}^{\eta}(\mathcal{W}'').$$

- Case  $o = \text{rel}$ : from the definition of erasure it follows that

$$(gconf, V_m, u(m)(\iota)) \in \llbracket \phi(v) \rrbracket_{\emptyset}^{\square}(\mathcal{W}')$$

since  $\mathcal{W}.\text{acq}(\iota) = \phi$ ,  $m.\text{view} = V_m$  and  $m.\text{val} = v$ .

Pick an  $\iota'$  that is fresh for  $u$  and  $\mathcal{W}'$ . Pick  $u' = (u[\iota \rightsquigarrow_{\eta(x)} \iota'])[m, \iota' \mapsto \varepsilon]$ ,  $r' = r[\iota \rightsquigarrow_{\eta(x)} \iota'] \bullet u(m)(\iota)$  and  $\mathcal{W}'' = \mathcal{W}'[\iota' \mapsto \phi[v \mapsto \top]]$ . Then it remains to prove that  $gconf' \in [r_F[i \mapsto r' \bullet f], u', \mathcal{W}'']_{\{i\}}$ ,

$$(gconf', V', r') \in \llbracket \exists t' \geq t. \text{Rel}(x, \phi[a \mapsto \top]) * \phi(a) * \text{O}(x, a, t') \rrbracket_{\sigma', \mu}^{\eta}(\mathcal{W}'')$$

and  $r' \bullet \text{canAcq}(r', u') \leq_o r \bullet \text{canAcq}(r, u)$  and  $\text{writesAllowed}(r, \text{written}(gconf, gconf'))$ .

The  $\text{writesAllowed}$  obligation follows trivially as  $\text{written}(gconf, gconf') = \emptyset$  and the

$$(gconf', V', r') \in \llbracket \exists t' \geq t. \text{Rel}(x, \phi[a \mapsto \top]) * \phi(a) * \text{O}(x, a, t') \rrbracket_{\sigma', \mu}^{\eta}(\mathcal{W}'')$$

obligation follows by upwards-closure and the definition of the interpretation.

By Lemmas 7 and 11,

$$\begin{aligned} r' \bullet \text{canAcq}(r', u') &= r[\iota \rightsquigarrow_x \iota'] \bullet \text{canAcq}(r[\iota \rightsquigarrow_x \iota'], u[\iota \rightsquigarrow_x \iota']) \\ &= r[\iota \rightsquigarrow_x \iota'] \bullet \text{canAcq}(r, u) \\ &\leq_o r \bullet \text{canAcq}(r, u) \end{aligned}$$

It remains to prove that  $gconf'$  is in the erasure:  $gconf' \in [r_F[i \mapsto r' \bullet f], u', \mathcal{W}'']_{\{i\}}$ . By Lemma 42 it follows that  $gconf' \in [r_F[i \mapsto (r \bullet f)[\iota \rightsquigarrow_x \iota']], u[\iota \rightsquigarrow_x \iota'], \mathcal{W}'']_{\{i\}}$  and by Lemma 43,  $gconf' \in [r_F[i \mapsto r' \bullet f], u', \mathcal{W}'']_{\{i\}}$  as required. □

#### Lemma 45.

$$\begin{aligned} gconf &\in [r_F[i \mapsto r \bullet f], u, \mathcal{W}]_{\{i\}} \wedge r.\text{wr}(m.\text{loc}) = (1, X) \wedge \\ \pi_1(\max(X)) &\leq V.\text{rlx}(m.\text{loc}) \leq m.\text{time} \wedge m \in gconf.M \wedge \\ \text{wf}_{\text{conf}}(i, gconf, V \sqcup [\text{rlx} : \{m.\text{loc}@m.\text{time}\}]) &\Rightarrow \\ m.\text{val} &= \pi_1(\max(X)) \end{aligned}$$

*Proof.* Since  $(1, X)$  is only composable with  $(0, \emptyset)$  it follows by the definition of erasure that  $gconf.M(m.\text{loc}) \setminus gconf.P = X$ . If  $m \notin gconf.P$  it follows from the assumptions that  $m \in X$  and thus  $\max(X) = (m.\text{val}, m.\text{time})$ . Otherwise,  $m \in gconf.P$ . If  $m.\text{tid} \neq i$  it follows from erasure that  $\text{validPromise}(r_F, u, m)$ . Hence,  $\pi_1(r_F(m.\text{tid}) \bullet \text{canAcq}(r_F(m.\text{tid}, u))).\text{wr}(x) \neq 0$ , which is a contradiction since  $r.\text{wr}(x) = (1, X)$ . If  $m.\text{tid} = i$  then it follows by the  $\text{wf}_{\text{conf}}$  assumption that  $m.\text{time} < m.\text{time}$  which is a contradiction. □

**Lemma 46** (Soundness of acquire read for an exclusive writer).

$$\models_{np} \{x = e * \text{Acq}(x, \phi) * \text{W}^1(x, X)\} a := [e]_{\text{acq}} \{B\}$$

where  $x$  and  $X$  are specification variables and  $B$  is the assertion

$$\text{Acq}(x, \phi[a \mapsto \top]) * \phi(a) * \text{W}^1(x, X) * \text{O}(x, a, \text{snd}(\max(X))) * a = \text{fst}(\max(X)).$$

*Proof.* The same as Lemma 44, but using Lemma 45 to conclude that we read the last fulfilled write as specified by the write permission.  $\square$

**Lemma 47.**

$$\begin{aligned} r &= r_1 \bullet \prod_{a \in Ar_m^a} \wedge u' = u[m \mapsto [a \in A \mapsto r_m^a]] \wedge m \notin \text{dom}(u) \\ &\Rightarrow r_1 \bullet \text{canAcq}(r_1, u') \leq_o r \bullet \text{canAcq}(r, u) \end{aligned}$$

**Lemma 48.**

$$\langle P, M \rangle \xrightarrow{m} \langle P', M' \rangle \wedge P(m.\text{tid}, m.\text{loc}) = \emptyset \Rightarrow P' = P \wedge M' = M \dot{\Leftarrow} m$$

**Lemma 49** (Soundness of release write).

$$\llbracket \vdash \{x = e_1 * v = e_2 * \text{Rel}(x, \phi) * \text{W}^\pi(x, X) * \phi(v) * \text{O}(x, v_0, t_0)\} [e_1]_{\text{rel}} := e_2 \{\exists t. \text{W}^\pi(x, X \cup \{(v, t)\}) * t_0 < t\} \rrbracket^{\text{np}}$$

where  $x, v, \pi$ , and  $X$  are assumed to be specification variables.

*Proof.* Let  $(gconf, V, r) \in \llbracket x = e_1 * v = e_2 * \text{Rel}(x, \phi) * \text{W}^\pi(x, X) * \phi(v) * \text{O}(x, v_0, t_0) \rrbracket_\mu^\eta(\mathcal{W})$ . Since  $[e_1]_{\text{rel}} := e_2 \neq \text{skip}$  it suffices to consider the case where

$$\langle (\mu, [e_1]_{\text{rel}} := e_2), V, gconf \rangle \xrightarrow{\text{NP}}_i \langle (\sigma', V'), gconf' \rangle,$$

and  $gconf \in [r_F[i \mapsto r \bullet f], u, \mathcal{W}]_{\{i\}}$ .

By **THREAD: WRITE** it follows that  $gconf' = (P', M')$ ,  $\sigma' = (\mu, \text{skip})$ , and

$$\langle P, M \rangle \xrightarrow{m} \langle P', M' \rangle \quad V.\text{rlx}(\eta(x)) < t \quad V' = V \sqcup [\text{pln} : \{\eta(x)@t\}, \text{rlx} : \{\eta(x)@t\}]$$

and  $V' = R$ ,  $P(i, \eta(x)) = \emptyset$  where  $gconf = (P, M)$  and  $m = \langle \eta(x) :_i^{\text{rel}} \eta(v), R@(\_, t) \rangle$ . It follows by Lemma 48 that  $P' = P$  and  $M' = M \dot{\Leftarrow} m$ .

From the definition of separating conjunction there exists  $r_1$  and  $r_2$  such that  $r = r_1 \bullet r_2$  with  $(gconf, V, r_1) \in \llbracket \text{Rel}(x, \phi) * \text{W}_\pi(x, X) \rrbracket_\mu^\eta(\mathcal{W})$  and  $(gconf, V, r_2) \in \llbracket \phi(a) \rrbracket_\mu^\eta(\mathcal{W})$ . It follows that there exists an  $\iota \in \text{dom}(\mathcal{W}.\text{rel})$  and  $\pi'$  such that  $\vdash \phi \Rightarrow \mathcal{W}.\text{rel}(\iota)$  and  $r_1.\text{wr}(\eta(x)) = (\pi', \eta(X))$  with  $\pi \leq \pi'$ .

Let  $t$  denote  $r \bullet \prod_{t \in \text{TI}d \setminus \{i\}} r_F(t) \bullet \prod_{m \in M} \prod_{\iota \in \text{dom}(u(m))} u(m)(\iota)$ . From the definition of erasure it follows that  $\vdash \mathcal{W}.\text{rel}(\iota)(\eta(v)) \Rightarrow \otimes_{\iota \in t.\text{acq}(\eta(x))} \mathcal{W}.\text{acq}(\iota)(\eta(v))$  and thus that there exists  $(r_\iota)_{\iota \in t.\text{acq}(\eta(x))}$  such that  $r_2 = \prod_{\iota \in t.\text{acq}(\eta(x))} r_\iota$  and  $(gconf, V, r_\iota) \in \llbracket \mathcal{W}.\text{acq}(\iota)(\eta(v)) \rrbracket_\mu^\eta(\mathcal{W})$  for all  $\iota \in t.\text{acq}(\eta(x))$ .

Pick  $u' = u[m \mapsto (\lambda \iota \in t.\text{acq}(\eta(x)). r_\iota)]$ ,  $r' = r_1.\text{wr}[\eta(x) \mapsto (\pi', \{(\eta(v), t)\} \cup \eta(X))]$  and  $\mathcal{W}' = \mathcal{W}$ . Then it remains to show that

1.  $gconf' \in [r_F[i \mapsto r' \bullet f], u', \mathcal{W}']_{\{i\}}$ ,
2.  $(gconf', V', r') \in \llbracket \exists t. \mathcal{W}_\pi(x, X \cup \{(v, t)\}) * t_0 < t \rrbracket_{\sigma'.\mu}^\eta(\mathcal{W}')$ ,
3.  $r' \bullet \text{canAcq}(r', u') \leq_o r \bullet \text{canAcq}(r, u)$ ,
4. and  $\text{writesAllowed}(r, \text{written}(gconf, gconf'))$ .

Starting with the last proof obligation,  $\text{writesAllowed}(r, \text{written}(gconf, gconf'))$ : since  $\text{written}(gconf, gconf') = \{m\}$ , the  $\text{writesAllowed}(r, \text{written}(gconf, gconf'))$  proof obligation reduces to proving that  $\text{writeAllowed}(r, m)$ . This is easily seen to hold since  $\pi_1(r.wr(\eta(x))) > 0$ .

The second-to-last proof obligation,  $r' \bullet \text{canAcq}(r', u') \leq_o r \bullet \text{canAcq}(r, u)$  follows from Lemma 47.

The second proof obligation follows easily by the assertion semantics since  $t_0 \leq V.rlx(\eta(x)) < t$ .

It remains to prove that  $gconf' \in \llbracket r_F[i \mapsto r' \bullet f], u', \mathcal{W}' \rrbracket_{\{i\}}$ : Since  $M' = M \stackrel{\epsilon}{\sim} m$  it follows that  $M'(\eta(x)) \setminus P = (M(\eta(x)) \setminus P) \cup \{m\}$ . Furthermore, since  $V \leq R$  and  $gconf \leq gconf'$  it also follows that

$$\forall \iota \in \text{dom}(u'(m)). (gconf', R, u'(m)(\iota)) \in \llbracket \mathcal{W}.acq(\iota)(m.val) \rrbracket_{\square}^{\square}(\mathcal{W}')$$

as required, by upwards-closure. □



## 4.4 Rules for relaxed accesses

**Lemma 50.**

$$\iota \in \text{dom}(\mathcal{W}.\text{acq}) \cap r.\text{acq}(m.\text{loc}) \wedge g\text{conf} \in [r_F[i \mapsto r \bullet f], u, \mathcal{W}]_{\{i\}} \wedge m \in g\text{conf}.M \Rightarrow \\ (g\text{conf}_\perp, V_\perp, \varepsilon) \in \llbracket \nabla(\mathcal{W}.\text{acq}(\iota)(m.\text{val})) \rrbracket_{\square}^{\square}(\mathcal{W})$$

**Lemma 51** (Soundness of relaxed read).

$$\llbracket \vdash \{x = e * \text{Acq}(x, \phi) * \text{O}(x, \_, t)\} a := [e]_{\text{rlx}} \{\text{Acq}(x, \phi) * \exists t' \geq t. \nabla(\phi(a)) * \text{O}(x, a, t')\} \rrbracket^{\text{np}}$$

where  $x$  and  $t$  are specification variables.

*Proof.* Let  $(g\text{conf}, V, r) \in \llbracket x = e * \text{Acq}(x, \phi) * \text{O}(x, \_, t) \rrbracket_{\mu}^{\eta}(\mathcal{W})$ . It suffices to consider the case where

$$\langle (\mu, a := [e]_{\text{rlx}}, V), g\text{conf} \rangle \xrightarrow{i}_{\text{NP}} \langle (\sigma', V'), g\text{conf}' \rangle,$$

and  $g\text{conf} \in [r_F[i \mapsto r \bullet f], u, \mathcal{W}' ]_{\{i\}}$ ,  $\mathcal{W}' \geq \mathcal{W}$ .

By the **THREAD: READ** rule it follows that there exists an  $m = \langle \eta(x) :_j^o v, V_m @ (\_, t_m) \rangle \in M$  such that  $V.\text{rlx}(\eta(x)) \leq t_m$ ,  $g\text{conf}'.P = g\text{conf}.P$ ,  $g\text{conf}'.M = g\text{conf}.M$ ,  $V' = V \sqcup [\text{pln} : \{\eta(x) @ t_m\}, \text{rlx} : \{\eta(x) @ t_m\}]$  and  $\sigma' = (\mu[a \mapsto v], \text{skip})$ .

From the  $(g\text{conf}, V, r) \in \llbracket \text{Acq}(x, \phi) * \text{O}(x, \_, t) \rrbracket_{\mu}^{\eta}(\mathcal{W})$  assumption it follows that there exists an  $\iota \in \text{dom}(\mathcal{W}.\text{acq}) \cap r.\text{acq}(\eta(x))$  such that  $\mathcal{W}.\text{acq}(\iota) = \phi$  and  $\eta(t) \leq V.\text{rlx}(\eta(x)) \leq t_m$ . Hence, by Lemma 50,

$$(g\text{conf}_\perp, V_\perp, \varepsilon) \in \llbracket \nabla(\phi(v)) \rrbracket_{\square}^{\square}(\mathcal{W}')$$

Pick  $r' = r$ ,  $u' = u$  and  $\mathcal{W}'' = \mathcal{W}'$ . It thus remains to prove that

$$(g\text{conf}', V', r') \in \llbracket \text{Acq}(x, \phi) * \exists t' \geq t. \nabla(\phi(a)) * \text{O}(x, a, t') \rrbracket_{\sigma', \mu}^{\eta}(\mathcal{W}'')$$

which follows easily from the definition of the interpretation.  $\square$

**Lemma 52** (Soundness of relaxed read for an exclusive writer).

$$\llbracket \vdash \{x = e * \text{Acq}(x, \phi) * W^1(x, X)\} a := [e]_{\text{rlx}} \{B\} \rrbracket^{\text{np}}$$

where  $x$  and  $X$  are specification variables and  $B$  is the following assertion

$$\text{Acq}(x, \phi) * W^1(x, X) * (\nabla\phi(a)) * \text{O}(x, a, \text{snd}(\max(X))) * a = \text{fst}(\max(X)).$$

*Proof.* Similar to the proof of Lemma 51, but using Lemma 45 to prove properties about the value read.  $\square$

**Lemma 53.**

$$m.\text{mod} = \text{rlx} \wedge \langle P, M \rangle \xrightarrow{m} \langle P', M' \rangle \Rightarrow \\ \text{values}(M'(\text{rlx})) = \text{values}(M(\text{rlx})) \cup \{(m.\text{loc}, m.\text{val})\}$$

where  $\text{values}(M) = \{(m.\text{loc}, m.\text{val}) \mid m \in M\}$ .

**Lemma 54.**

$$(M, P) \in [r_F[i \mapsto r \bullet f], u, \mathcal{W}]_{\{i\}} \wedge \langle P, M \rangle \xrightarrow{m} \langle P', M' \rangle \wedge m.\text{time} > \text{snd}(\max(X)) \wedge \\ m.\text{mod} = \text{rlx} \wedge r.\text{wr}(m.\text{loc}) = (\pi, X) \wedge r' = r.\text{wr}[m.\text{loc} \mapsto (\pi, X \cup \{(m.\text{val}, m.\text{time})\})] \wedge \\ (g\text{conf}_\perp, V_\perp, \varepsilon) \in \llbracket \mathcal{W}.\text{rel}(m.\text{loc})(m.\text{val}) \rrbracket_{\square}^{\square}(|\mathcal{W}|) \Rightarrow \\ (M', P') \in [r_F[i \mapsto r' \bullet f], u, \mathcal{W}]_{\{i\}}$$

**Lemma 55** (Soundness of relaxed write).

$$\llbracket \vdash \{A\} [e_1]_{\text{rlx}} := e_2 \{\exists t. \mathbf{W}^\pi(x, X \cup \{(v, t)\}) * t_0 < t\} \rrbracket^{\text{PP}}$$

where  $x, v, X$  and  $\pi$  are specification variables and  $A$  is the following assertion

$$x = e_1 * v = e_2 * \text{Rel}(x, \phi) * \mathbf{W}^\pi(x, X) * \phi(v) * \text{pure}(\phi(v)) * \mathbf{O}(x, v_0, t_0).$$

*Proof.* Let  $(gconf, V, r) \in \llbracket A \rrbracket_\mu^\eta(\mathcal{W})$ . It suffices to consider the case where

$$\langle \langle (\mu, [e_1]_{\text{rlx}} := e_2), V \rangle, gconf \rangle \xrightarrow{\text{NP}}_i \langle \langle \sigma', V' \rangle, gconf' \rangle,$$

and  $gconf \in [r_F[i \mapsto r \bullet f], u, \mathcal{W}']_{\{i\}}$ ,  $\mathcal{W}' \geq \mathcal{W}$ .

By **THREAD: WRITE** it follows that  $gconf' = (P', M', O)$ ,  $\sigma' = (\mu, \mathbf{skip})$ , and

$$\langle P, M \rangle \xrightarrow{m} \langle P', M' \rangle \quad V.\text{rlx}(\eta(x)) < t \quad V' = V \sqcup [\text{pln} : \{\eta(x)@t\}, \text{rlx} : \{\eta(x)@t\}]$$

and  $R = [\text{pln} : \{\eta(x)@t\}, \text{rlx} : \{\eta(x)@t\}]$ , where  $gconf = (P, M, O)$  and  $m = \langle \eta(x) :_i^{\text{rlx}} \eta(v), R@(\_, t) \rangle$ .

Pick  $r' = r[\text{wr}(\eta(x)) \mapsto (\pi, \{(\eta(v), t)\} \cup \eta(X))]$ ,  $u' = u$  and  $\mathcal{W}'' = \mathcal{W}'$ . Then we need to prove that  $r' \bullet \text{canAcq}(r', u) \leq_o r \bullet \text{canAcq}(r, u)$ ,

$$gconf' \in [r_F[i \mapsto r' \bullet f], u, \mathcal{W}']_{\{i\}} \quad (gconf', V', r') \in \llbracket \exists t. \mathbf{W}^\pi(x, X \cup \{(v, t)\}) * \text{snd}(\max(X)) < t \rrbracket_\mu^\eta(\mathcal{W}')$$

and  $\text{writesAllowed}(r, \text{written}(gconf, gconf'))$ .

Firstly,  $r' \bullet \text{canAcq}(r', u) \leq_o r \bullet \text{canAcq}(r, u)$  holds trivially since  $r' \leq_o r$  and  $r'.\text{acq} = r.\text{acq}$ . Furthermore,  $\text{writesAllowed}(r, \text{written}(gconf, gconf'))$  follows from  $\text{writeAllowed}(r, m)$ , which itself follows from the assumption that

$$(gconf, V, r) \in \llbracket A \rrbracket_\mu^\eta(\mathcal{W}).$$

The third proof obligation,  $(gconf', V', r') \in \llbracket \exists t. \mathbf{W}^\pi(x, X \cup \{(v, t)\}) * t_0 < t \rrbracket_\mu^\eta(\mathcal{W}')$ , is easily seen to hold by the definition of the interpretation.

It remains to prove that  $gconf' \in [r_F[i \mapsto r' \bullet f], u, \mathcal{W}']_{\{i\}}$ , which reduces to proving that

$$(gconf'_\perp, V_\perp, \varepsilon) \in \llbracket \mathcal{W}.\text{rel}(m.\text{loc})(m.\text{val}) \rrbracket_\perp^\perp(|\mathcal{W}|)$$

by Lemma 54. This in turn follows from the fact that  $\phi(m.\text{val})$  is pure.  $\square$

## 4.5 Rules for plain accesses

**Lemma 56** (Soundness of plain read).

$$\left[ \vdash \{x = e * x \xrightarrow{\pi} v\} a := [e]_{\text{pIn}} \{x \xrightarrow{\pi} v * a = v\} \right]^{\text{np}}$$

where  $x$  and  $v$  are specification variables.

*Proof.* Let  $(gconf, V, r) \in \left[ \left[ x = e * x \xrightarrow{\pi} v \right]_{\mu}^{\eta} \right] (\mathcal{W})$ . It suffices to consider the case where

$$\langle \langle (\mu, a := [e]_{\text{pIn}}), V \rangle, gconf \rangle \xrightarrow{\text{NP}}_i \langle \langle \sigma', V' \rangle, gconf' \rangle,$$

and  $gconf \in [r_F[i \mapsto r \bullet f], u, \mathcal{W}' ]_{\{i\}}$ ,  $\mathcal{W}' \geq \mathcal{W}$ .

By the **THREAD: READ** rule it follows that there exists an  $m = \langle \eta(x) :_j^{\text{pIn}} v', V_m @ (\_, t) \rangle \in M$  such that  $V.\text{pIn}(\eta(x)) \leq t$ ,  $gconf'.P = gconf.P$ ,  $gconf'.M = gconf.M$ ,  $V' = V \sqcup [\text{pIn} : \perp, \text{rIx} : \{\eta(x)@t\}]$  and  $\sigma' = (\mu[a \mapsto v'], \mathbf{skip})$ .

From the  $(gconf, V, r) \in \left[ \left[ x \xrightarrow{\pi} v \right]_{\mu}^{\eta} \right] (\mathcal{W})$  assumption it follows that there exists a  $\pi' \geq \pi$  and  $t' \leq V(\eta(x)).\text{pIn}$  such that  $r.\text{pIn}(\eta(x)) = \text{inj}_2(\pi', \eta(v), t')$  and  $\pi' > 0$ . Hence,  $t' \leq t$ .

From the erasure assumption it follows that  $\text{last}(M(\eta(x)) \setminus P).\text{val} = v$  and  $\text{last}(M(\eta(x)) \setminus P).\text{time} = t'$ .

If  $m \in P$  it follows that  $m.\text{tid} \neq i$  since otherwise,  $V'.\text{rIx}(\eta(x)) = V.\text{rIx}(\eta(x)) \sqcup t < t$  which is a contradiction. Hence, by erasure,

$$\text{writeAllowed}(r_F(m.\text{tid}) \bullet \text{canAcq}(r_F(m.\text{tid}), u), m).$$

This is a contradiction, since  $\text{canAcq}(r_F(m.\text{tid}), u).\text{pIn}(\eta(x)) = (1, \_)$ ,  $r.\text{pIn}(\eta(x)) = (\pi, \_)$  and  $r \# \text{canAcq}(r_F(m.\text{tid}), u)$ .

Otherwise, if  $m \notin P$  it follows that  $m \in (M(\eta(x)) \setminus P)$  and since  $t' \leq t$  it must be the case that  $\text{last}(M(\eta(x)) \setminus P) = m$ . It follows that  $v = v'$ .

Pick  $r' = r$ ,  $u' = u$  and  $\mathcal{W}'' = \mathcal{W}'$ . Now the remain proof obligations are easy.  $\square$

**Lemma 57.**

$$\begin{aligned} x \in PLoc \wedge m = \langle x :_i^{\text{pIn}} v, R @ (\_, t) \rangle \wedge (P, M) \xrightarrow{m} (P', M') \wedge \\ \text{last}(M(x) \setminus P).\text{time} < t \Rightarrow \text{last}(M'(x) \setminus P') = m \end{aligned}$$

**Lemma 58** (Soundness of plain write).

$$\left[ \vdash \{x = e * x \xrightarrow{1} \_ \} [e]_{\text{pIn}} := a \{x \xrightarrow{1} a\} \right]^{\text{np}}$$

where  $x$  is a specification variable.

*Proof.* Let  $(gconf, V, r) \in \left[ \left[ x = e * x \xrightarrow{1} \_ \right]_{\mu}^{\eta} \right] (\mathcal{W})$ . It suffices to consider the case where

$$\langle \langle (\mu, [e]_{\text{pIn}} := a), V \rangle, gconf \rangle \xrightarrow{\text{NP}}_i \langle \langle \sigma', V' \rangle, gconf' \rangle,$$

and  $gconf \in [r_F[i \mapsto r \bullet f], u, \mathcal{W}' ]_{\{i\}}$ ,  $\mathcal{W}' \geq \mathcal{W}$ .

By the **THREAD: WRITE** rule it follows that there exists a  $t$  such that

$$(gconf.P, gconf.M) \xrightarrow{\langle \eta(x) :_i^{\text{pIn}} \mu(a), R @ (\_, t) \rangle} (gconf'.P, gconf'.M) \quad V' = V \sqcup [\text{pIn} : \{\eta(x)@t\}, \text{rIx} : \{\eta(x)@t\}]$$

$V(\eta(x)).\text{rIx} < t$  and  $\sigma' = (\mu, \mathbf{skip})$ .

From the  $(gconf, V, r) \in \llbracket x \xrightarrow{1} \_ \rrbracket_{\mu}^{\eta}(\mathcal{W})$  assumption it follows that there exists a  $t' \leq V.\mathbf{pln}(\eta(x))$  such that  $r.\mathbf{pln}(\eta(x)) = inj_2(1, \_, t')$ .

By definition of a timemap,  $V.\mathbf{pln}(\eta(x)) \leq V.\mathbf{rlx}(\eta(x))$  and thus  $t' < t$ .

Pick  $r' = r[\mathbf{pln}(x) \mapsto inj_2(1, \mu(a), t)]$ ,  $\mathcal{W}' = \mathcal{W}$  and  $u' = u$ . By Lemma 57 it follows that  $last(gconf'.M(\eta(x)) \setminus gconf'.P) = \langle \eta(x) :_i^{\mathbf{pln}} v, R@(\_, t) \rangle$  and thus,  $gconf' \in [r_F[i \mapsto r' \bullet f], u', \mathcal{W}']_{\{i\}}$ . It remains to prove that  $(gconf', V', r') \in \llbracket x \xrightarrow{1} a \rrbracket_{\sigma'.\mu}^{\eta}(\mathcal{W}')$ ,  $r' \bullet \mathbf{canAcq}(r', u') \leq_o r \bullet \mathbf{canAcq}(r, u)$ , and  $writeAllowed(r, written(gconf, gconf'))$ . The first obligation follows easily from the definition of the interpretation. The second obligation follows easily from the fact that  $r' \leq_o r$  and  $r'.\mathbf{acq} = r.\mathbf{acq}$ . For the last obligation, it suffices to prove that  $writeAllowed(r, \langle \eta(x) :_i^{\mathbf{pln}} v, R@(\_, t) \rangle)$ , which holds since  $r.\mathbf{pln}(\eta(x)) = inj_2(1, \_, t')$  by assumption.  $\square$

## 5 Examples

### 5.1 Random number generator

We do not need to transfer any information, so we can pick  $\lambda v. \top$  as the predicate for  $x$  and  $y$ .

$$\begin{array}{c}
 \{W^1(y, [0]) * \text{Rel}(y, \lambda v. \top) * \text{Acq}(x, \lambda v. \top) * \text{O}(x, 0, 0)\} \\
 \{x = x * \text{Acq}(x, \lambda v. \top) * \text{O}(x, 0, 0)\} \\
 r_1 := [x]_{\text{rlx}}; \\
 \{\text{Acq}(x, \lambda v. \top) * (\exists t_1. \text{O}(x, r_1, t_1) * 0 \leq t_1) * \nabla \top\} \\
 \{\text{O}(x, r_1, \_)\} \\
 \{W^1(y, [0]) * \text{Rel}(y, \lambda v. \top) * \text{O}(x, r_1, \_)\} \\
 \{W^1(y, [0]) * \text{Rel}(y, \lambda v. \top) * \top * \text{pure}(\top)\} \\
 [y]_{\text{rlx}} := r_1 + 1 \\
 \{W^1(y, [r_1 + 1; 0]) * \text{Rel}(y, \lambda v. \top)\} \\
 \{W^1(y, [r_1 + 1; 0])\} \\
 \{W^1(y, [r_1 + 1; 0]) * \text{O}(x, r_1, \_)\} \\
 \\
 \{W^1(x, [0]) * \text{Rel}(x, \lambda v. \top) * \text{Acq}(y, \lambda v. \top) * \text{O}(y, 0, 0)\} \\
 \{y = y * \text{Acq}(y, \lambda v. \top) * \text{O}(y, 0, 0)\} \\
 r_2 := [y]_{\text{rlx}}; \\
 \{\text{Acq}(y, \lambda v. \top) * (\exists t_1. \text{O}(x, r_2, t_1) * 0 \leq t_1) * \nabla \top\} \\
 \{\text{O}(y, r_2, \_)\} \\
 \{W^1(x, [0]) * \text{Rel}(x, \lambda v. \top) * \text{O}(y, r_2, \_)\} \\
 \{W^1(x, [0]) * \text{Rel}(x, \lambda v. \top) * \top * \text{pure}(\top)\} \\
 [x]_{\text{rlx}} := r_2 \\
 \{W^1(x, [r_2; 0]) * \text{Rel}(x, \lambda v. \top)\} \\
 \{W^1(x, [r_2; 0])\} \\
 \{W^1(x, [r_2; 0]) * \text{O}(y, r_2, \_)\} \\
 \\
 \{r_1 \in \{r_2, 0\} \wedge r_2 \in \{r_1 + 1, 0\}\} \\
 \{r_1 = 0 \wedge (r_2 = 0 \vee r_2 = 1)\}
 \end{array}
 \quad \Bigg| \quad x = y = 0$$

### 5.2 Separation

We can define a self-looping list by having a location hold the address of the next element of the list, and using an address already in the list to mark the end the list:

$$\begin{aligned}
 \text{is-list}(v) &\stackrel{\text{def}}{=} \exists s_2. \text{finite}(s_2) * v \notin s_2 * \text{is-list}'(v, \{v\}, s_2) \\
 \text{is-list}'(v, s_1, s_2) &\stackrel{\text{def}}{=} \exists \ell, v'. W^1(v, v' :: \ell) * \text{Rel}(v, \lambda v. \top) * \\
 &\quad (v' \in s_1 \vee (v' \in s_2 * \text{is-list}'(v', s_1 \cup \{v'\}, s_2 \setminus \{v'\})))
 \end{aligned}$$

Because we do not transfer anything between threads (we are trying to show the threads do not interact!), we can use  $\lambda v. \top$  as the the predicate for the locations of the list. To avoid clutter, we elide  $\text{Rel}(-, \lambda v. \top)$  and the associated assertions below.

We then have the following proof outline for the first thread (the second thread is entirely symmetric):

$$\begin{aligned}
 &\{\text{is-list}(a)\} \\
 &\{\exists s. \text{finite}(s) * a \notin s * \text{is-list}'(a, \{a\}, s)\} \\
 &\{\exists s, a'. W^1(a, a' :: \_) * (a' \in \{a\} \vee (a' \notin \{a\} * \text{is-list}'(a', \{a\} \cup \{v'\}, s \setminus \{a'\})))\} \\
 &\quad \{a = a * \dots * W^1(a, a' :: \_)\} \\
 &r_1 := [a]_{\text{rlx}}; \\
 &\quad \{\dots * W^1(a, a' :: \_) * \dots * \text{O}(a, r_1, \_) * r_1 = a'\} \\
 &\{\exists s, a'. W^1(a, a' :: \_) * r_1 = a' * (a' \in \{a\} \vee (a' \notin \{a\} * \text{is-list}'(a', \{a\} \cup \{a'\}, s \setminus \{a'\})))\} \\
 &\{\exists a'. W^1(a, a' :: \_) * r_1 = a' * (a' \in \{a\} \vee (a' \notin \{a\} * \exists a''. W^1(a', a'' :: \_) * \dots))\} \\
 &\quad \{r_1 = a' * a = a * W^1(a', \_) * \dots\} \\
 &[r_1]_{\text{rlx}} := a \\
 &\quad \{W^1(a', a :: \_) * \dots\} \\
 &\{\exists a'. W^1(a, a' :: \_) * ((a' \in \{a\}) \vee (a' \notin \{a\} * W^1(a', a :: \_)))\} \\
 &\{W^1(a, a :: \_) \vee (\exists a'. W^1(a, a' :: \_) * a' \notin \{a\} * W^1(a', a :: \_))\} \\
 &\{(\text{is-list}'(a, \{a\}, \emptyset)) \vee (\exists a'. a' \notin \{a\} * \text{is-list}'(a, \{a\}, \{a'\}))\} \\
 &\{\text{is-list}(a)\}
 \end{aligned}$$

### 5.3 Non-deterministic write

#### 5.3.1 Example using non-deterministic choice

Invariant-based methods cannot be used to prove that  $\neg(r_1 = r_2 = 1)$  in the following program where all locations initially hold 0, and where the  $*$  stands for a non-deterministic choice.

$$\begin{array}{l} \mathbf{if} * [x]_{r1x} := 1 \\ \mathbf{else} [y]_{r1x} := 1 \end{array} \parallel \begin{array}{l} r_1 := [x]_{r1x}; \\ r_2 := [y]_{r1x}; \end{array} \quad (\text{nondet})$$

We elide most things.

$$\begin{array}{l} \{W^1(x, [0]) * W^1(y, [0])\} \\ \mathbf{if} * \\ \quad [x]_{r1x} := 1 \\ \quad \{W^1(x, [1; 0]) * W^1(y, [0])\} \\ \mathbf{else} \\ \quad [y]_{r1x} := 1 \\ \quad \{W^1(x, [0]) * W^1(y, [1; 0])\} \\ \quad \{W^1(x, [1; 0]) * W^1(y, [0])\} \vee \{W^1(x, [0]) * W^1(y, [1; 0])\} \end{array} \parallel \begin{array}{l} \{Acq(x, \lambda v. \top) * Acq(y, \lambda v. \top)\} \\ r_1 := [x]_{r1x}; \\ r_2 := [y]_{r1x}; \\ \{O(x, r_1, \_) * O(y, r_2, \_)\} \end{array}$$

We can conclude, by case analysis on the disjunction, and by using lemma 37, that  $(r_1 \in \{0, 1\} \wedge r_2 \in \{0\}) \vee (r_1 \in \{0\} \wedge r_2 \in \{0, 1\})$ , and therefore  $\neg(r_1 = r_2 = 1)$ .

#### 5.3.2 Example within the language

Invariant-based methods cannot be used to prove that  $\neg(r_1 = r_2 = 1)$  in the following program where all locations initially hold 0.

$$[t]_{r1x} := 1 \parallel \begin{array}{l} r_3 := [t]_{r1x}; \\ \mathbf{if} r_3 [x]_{r1x} := 1 \\ \mathbf{else} [y]_{r1x} := 1 \end{array} \parallel \begin{array}{l} r_1 := [x]_{r1x}; \\ r_2 := [y]_{r1x}; \end{array}$$

### 5.4 Coherence

#### 5.4.1 CoRW

The following program, where location  $x$  initially holds 0, cannot have the following outcome:

$$\begin{array}{l} r_1 := [x]_{r1x}; \text{ // reads 2} \\ [x]_{r1x} := 1 \end{array} \parallel [x]_{r1x} := 2 \parallel \begin{array}{l} r_2 := [x]_{r1x}; \text{ // reads 1} \\ r_3 := [x]_{r1x}; \text{ // reads 2} \end{array} \quad (\text{CoRW})$$

The proof outline for the first thread crucially records that the value it reads has a smaller timestamp than the value it writes:

$$\begin{array}{l} \{W^{\frac{1}{2}}(x, \{(0, 0)\}) * Acq(x, \lambda v. \top)\} \\ r_1 := [x]_{r1x}; \\ \{\exists t. W^{\frac{1}{2}}(x, \{(0, 0)\}) * O(x, r_1, t)\} \\ [x]_{r1x} := 1 \\ \{\exists t, t_1. W^{\frac{1}{2}}(x, \{(0, 0), (1, t_1)\}) * O(x, r_1, t) * t < t_1\} \end{array}$$

The proof outline for the second thread just records it writes 2:

$$\begin{array}{l} \{W^{\frac{1}{2}}(x, \{(0, 0)\})\} \\ [x]_{r1x} := 2 \\ \{\exists t_2. W^{\frac{1}{2}}(x, \{(0, 0), (2, t_2)\}) * 0 < t_2\} \end{array}$$

The proof outline for the third thread just records the order of the timestamps of 1 and 2:

$$\begin{aligned}
& \{\text{Acq}(x, \lambda v. \top)\} \\
& r_2 := [x]_{\text{rlx}}; \\
& \{\text{Acq}(x, \lambda v. \top) * \exists t_a. \text{O}(x, r_1, t_a)\} \\
& r_3 := [x]_{\text{rlx}} \\
& \{\exists t_a, t_b. \text{O}(x, r_2, t_a) * \text{O}(x, r_3, t_b) * t_a \leq t_b\}
\end{aligned}$$

We can then combine the write permissions to obtain  $W^1(x, \{(0, 0), (1, t_1), (2, t_2)\})$ . If moreover we assume that  $r_2 = 1 * r_3 = 2$ , from the postcondition of the third thread, we have that  $t_1 < t_2$  by Lemmas 37 and 39. Also, if moreover we assume that  $r_1 = 2$ , we have that  $t_2 < t_1$  by Lemma 37. Therefore, we can conclude  $\neg(r_1 = 2 * r_2 = 1 * r_3 = 2)$ .

### 5.4.2 CoWR

The following program, where location  $x$  initially holds 0, cannot have the following outcome:

$$\begin{array}{l}
[x]_{\text{rlx}} := 1; \\
r_1 := [x]_{\text{rlx}} // \text{reads } 2
\end{array}
\parallel
\begin{array}{l}
[x]_{\text{rlx}} := 2 \\
r_2 := [x]_{\text{rlx}} // \text{reads } 2 \\
r_3 := [x]_{\text{rlx}} // \text{reads } 1
\end{array}
\quad (\text{CoWR})$$

The proof outline for the first thread records that the read reads a timestamp no older than that of the write:

$$\begin{aligned}
& \{W^{\frac{1}{2}}(x, \{(0, 0)\}) * \text{Acq}(x, \lambda v. \top)\} \\
& [x]_{\text{rlx}} := 1 \\
& \{\exists t_1. W^{\frac{1}{2}}(x, \{(0, 0), (1, t_1)\}) * \text{Acq}(x, \lambda v. \top)\} \\
& r_1 := [x]_{\text{rlx}}; \\
& \{\exists t, t_1. W^{\frac{1}{2}}(x, \{(0, 0), (1, t_1)\}) * \text{O}(x, r_1, t) * t_1 \leq t\}
\end{aligned}$$

The proof outline for the second and the third threads are identical to that of CoRW. As for CoRW, we can combine the write permissions to obtain  $W^1(x, \{(0, 0), (1, t_1), (2, t_2)\})$ . If moreover we assume that  $r_2 = 2 * r_3 = 1$ , symmetrically to CoRW, from the postcondition of the third thread, we have that  $t_2 < t_1$  by Lemmas 37 and 39. Also, if moreover we assume that  $r_1 = 2$ , we have that  $t_1 \leq t_2$  by Lemma 37, and thus  $t_1 < t_2$  by Lemma 39. Therefore, we can conclude  $\neg(r_1 = 2 * r_2 = 2 * r_3 = 1)$ .

## 5.5 Release/acquire

### 5.5.1 Split permission message passing example

The following program, where locations  $x$ ,  $y$ , and  $z$  initially hold 0, cannot have the following outcome:  $(r_1 = 1 * r_2 = 0) * (r_3 = 1 * r_4 = 0)$ :

$$\begin{array}{l}
[x]_{\text{rlx}} := 1; \\
[y]_{\text{rlx}} := 1; \\
[z]_{\text{rel}} := 1
\end{array}
\parallel
\begin{array}{l}
r_1 := [z]_{\text{acq}}; \\
\text{if } r_1 = 1 \\
r_2 := [x]_{\text{rlx}}
\end{array}
\parallel
\begin{array}{l}
r_3 := [z]_{\text{acq}}; \\
\text{if } r_3 = 1 \\
r_4 := [y]_{\text{rlx}}
\end{array}
\quad (\text{Split-MP})$$

We pick

$$\phi_z = \lambda v. (\text{if } v = 1 \text{ then } W^1(x, [1; 0]) \text{ else } \top) * (\text{if } v = 1 \text{ then } W^1(y, [1; 0]) \text{ else } \top)$$

for  $z$ . This ought to be  $\lambda v. \text{if } v = 1 \text{ then } (W^1(x, [1; 0]) * W^1(y, [1; 0])) \text{ else } \top$ , but the  $\phi$  are stored and treated as syntactic assertions.

The proof outline for the writer transfers the write permissions for  $x$  and  $y$  away on  $z$  using  $\phi_z$ :

$$\begin{aligned}
& \{W^1(x, [0]) * \text{Rel}(x, \lambda v. \top) * W^1(y, [0]) * \text{Rel}(y, \lambda v. \top) * W^1(z, [0]) * \text{Rel}(z, \phi_z)\} \\
& [x]_{\text{rlx}} := 1; \\
& \{W^1(x, [1; 0]) * W^1(y, [0]) * \text{Rel}(y, \lambda v. \top) * W^1(z, [0]) * \text{Rel}(z, \phi_z)\} \\
& [y]_{\text{rlx}} := 1; \\
& \{W^1(x, [1; 0]) * W^1(y, [1; 0]) * W^1(z, [0]) * \text{Rel}(z, \phi_z)\} \\
& \quad \{z = z * 1 = 1 * W^1(z, \{(0, 0)\}) * \text{Rel}(z, \phi_z) * \phi_z(1) * O(z, 0, 0)\} \\
& [z]_{\text{rel}} := 1 \\
& \quad \{\exists t_1. W^1(z, \{(1, t_1)\} \cup \{(0, 0)\}) * 0 < t_1\} \\
& \quad \{W^1(z, [1; 0]) * \text{Rel}(z, \phi_z)\}
\end{aligned}$$

The proof outline for the first reader starts with an acquire permission for first half of  $\phi_z$  for  $z$ , obtained by splitting  $\phi_z$  from the initial  $\text{Acq}(z, \phi_z)$  using lemma 36, and uses it to obtain  $W^1(x, [1; 0])$ , which it then uses to know that it reads 1 from  $x$ .

$$\left\| \begin{aligned}
& \{\text{Acq}(z, \lambda v. \text{if } v = 1 \text{ then } W^1(x, [1; 0]) \text{ else } \top) * O(z, 0, 0) * \text{Acq}(x, \lambda v. \top)\} \\
& \quad \{z = z * \text{Acq}(z, \lambda v. \text{if } v = 1 \text{ then } W^1(x, [1; 0]) \text{ else } \top) * O(z, 0, 0)\} \\
& r_1 := [z]_{\text{acq}}; \\
& \quad \left\{ \begin{aligned}
& \{\text{Acq}(z, (\lambda v. \text{if } v = 1 \text{ then } W^1(x, [1; 0]) \text{ else } \top)[r_1 \mapsto \top]) * \\
& \quad (\exists t_1^z. O(z, r_1, t_1^z) * 0 \leq t_1^z) * (\lambda v. \text{if } v = 1 \text{ then } W^1(x, [1; 0]) \text{ else } \top)(r_1)\} \\
& \{(\lambda v. \text{if } v = 1 \text{ then } W^1(x, [1; 0]) \text{ else } \top)(r_1) * \text{Acq}(x, \lambda v. \top)\}
\end{aligned} \right\} \\
& \text{if } r_1 = 1 \\
& \quad \{W^1(x, [1; 0]) * \text{Acq}(x, \lambda v. \top)\} \\
& \quad r_2 := [x]_{\text{rlx}} \\
& \quad \{\text{Acq}(x, \lambda v. \top) * W^1(x, [1; 0]) * (\nabla(\lambda v. \top)(r_2)) * O(x, r_2, \text{snd}(\max([1; 0]))) * r_2 = \text{fst}(\max([1; 0]))\} \\
& \quad \{r_2 = 1\} \\
& \{r_1 = 1 \implies r_2 = 1\}
\end{aligned} \right\|$$

The proof outline for the second reader is symmetric.

### 5.5.2 WRC

The following program, where locations  $x$  and  $y$  initially hold 0, cannot have the following outcome:  $r_2 = 1 * r_3 = 0$ :

$$[x]_{\text{rlx}} := 1; \left\| \begin{array}{l} r_1 := [x]_{\text{acq}}; \\ \text{if } r_1 = 1 \\ \quad [y]_{\text{rel}} := 1 \end{array} \right\| \left\| \begin{array}{l} r_2 := [y]_{\text{acq}}; \\ \text{if } r_2 = 1 \\ \quad r_3 := [x]_{\text{rlx}} \end{array} \right\| \quad (\text{WRC})$$

We pick  $\phi_y = \lambda v. \text{if } v = 1 \text{ then } \exists t. O(x, 1, t) \text{ else } \top$ . The proof outline for the first thread just records the write of 1 to  $x$ :

$$\begin{aligned}
& \{W^1(x, \{(0, 0)\}) * \text{Rel}(x, \lambda v. \top)\} \\
& [x]_{\text{rlx}} := 1; \\
& \{\exists t. W^1(x, \{(0, 0), (1, t)\}) * 0 < t\}
\end{aligned}$$

The proof outline for the second thread feeds the  $O(x, 1, t)$  it obtains in the ‘if’ branch in the release write to  $y$ :

$$\begin{aligned}
& \{\text{Acq}(x, \lambda v. \top) * O(x, 0, 0) * W^1(y, \{(0, 0)\}) * \text{Rel}(y, \phi_y)\} \\
& r_1 := [x]_{\text{rlx}}; \\
& \text{if } r_1 = 1 \\
& \quad \{\exists t. O(x, 1, t) * W^1(y, \{(0, 0)\}) * \text{Rel}(y, \phi_y)\} \\
& \quad \quad \{y = y * 1 = 1 * \text{Rel}(y, \phi_y) * W^1(y, \{(0, 0)\}) * (\exists t. O(x, 1, t)) * O(y, 0, 0)\} \\
& [y]_{\text{rel}} := 1 \\
& \quad \{\exists t'. W^1(y, \{(0, 0)\} \cup \{(1, t')\}) * 0 < t'\} \\
& \quad \{\top\}
\end{aligned}$$



The proof outline for the third thread relates the timestamp of the value it reads from  $x$  with what it learns using  $\phi_y$  from the acquire read from  $y$ :

$$\begin{aligned}
& \{ \text{Acq}(y, \phi_y) * \text{O}(y, 0, 0) * \text{Acq}(x, \lambda v. \top) \} \\
& \{ y = y * \text{Acq}(y, \phi_y) * \text{O}(y, 0, 0) \} \\
r_2 & := [y]_{\text{acq}}; \\
& \{ \exists t' \geq 0. \text{Acq}(y, \phi_y[r_2 \mapsto \top]) * \phi(r_2) * \text{O}(y, r_2, t') \} \\
& \{ \text{Acq}(x, \lambda v. \top) * \phi(r_2) \} \\
\mathbf{if} \ r_2 & = 1 \\
& \{ \exists t. \text{Acq}(x, \lambda v. \top) * \text{O}(x, 1, t) \} \\
r_3 & := [x]_{\text{rlx}} \\
& \{ \exists t, t'. \text{O}(x, 1, t) * \text{O}(x, r_3, t') * t \leq t' \}
\end{aligned}$$

At the end of the execution, assuming  $r_2 = 1$ , we can confront  $\text{W}^1(x, \{(0, 0), (1, t)\})$  with  $\exists t, t'. \text{O}(x, 1, t) * \text{O}(x, r_3, t') * t \leq t'$  to conclude that  $r_3 = 1$ , as desired.